

Effect of Carrier Frequency in Grid Inter Connected Wind System with SSFC Controller

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ABSTRACT

In this paper a grid interconnected system with wind energy source linked with a FACTS based SSFC device (Static switched filter compensator) at load for enhancing power quality is considered .Analysis is done for the proposed system by varying Carrier frequency over a wide range and observed system performance at all 3 busses wise Grid bus, Generator Bus and Load Bus. Two regulators are used to organize the FACTS SSFC-device, these are based on a tri-loop dynamic error obsessed inter-coupled input to VSC controller. Investigation is made in MATLAB/SIMULINK Environment for the proposed system ,it is observed that system performance in terms of percentage Total harmonic Distortion is satisfactory along with the Enhanced Power Quality.

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1. INTRODUCTION

Renewable Energy sources such as Wind ,Solar ,Biomass etc are way out for the present day rising demand for electricity consumption with sustainable intensification and societal development. The green energy on conventional plant is minimized by the incorporation of the renewable energy resource akin to wind energy into power system [1]. This incorporation presents methodological challenges for which voltage regulation and power quality problems are to be measured. The power quality is a fundamental customer-central assess which is greatly affected by the distribution and transmission network's operation. The power quality issue is of great significance to the wind turbine. There has been an extended growth and fast development in using wind energy in the present day scenario. Wind energy conversion systems (WECS) are a outline of practicable and valuable renewable energy source that translate the wind kinetic energy to the mechanical energy [2]. This energy can be used to drive unlike AC and DC type generators. Generally, the WECS consists of a wind turbine, generator and its associated equipment, and the crucial control systems.

The WECS can be allied as either standalone or associated to the electric grid system, in the standalone connection the power is supplied to local isolated loads in isolated areas. It is required to endow with economical and effective technical solutions for both power quality and security issues anxious to the electric grid with schemes such as scattered and isolated wind energy schemes. Satisfyingly, the new arising FACTS technologies can act upon stabilization and power control functions by rapidly switching solid-state devices. Mostly FACTS devices are implemented for transmission control and Custom power devices for distribution control [3].

UPFC and STATCOM are custom power devices. Solid-state fault current limiters and Dynamic voltage Restorers are Apt for enhancing reliability and power quality of a system. The progress of these devices such as FACTS and custom power are for reducing definite power quality problems. For example, UPFC works most excellent for control of power flow, DVR as a series compensator is new for voltage sag compensation and STATCOM as a shunt compensator is in use for compensation of both reactive power and voltage sag[4] - [6]. Therefore different kinds of FACTS devices as STATCOM, DVR and UPS are functional for compensating a thorough kind of power quality problems.

This paper presents Effect of carrier frequency in the proposed FACTS based Static Switched Filter Compensator (SSFC) scheme incorporated wind energy system with efficient power quality enrichment[7]-[9]. Carrier frequencies are varied over range of 500 Hz to 10,000Hz and observed variations in % Total Harmonic Distortions[10],[11] at different busses in FACTS-SSFC scheme, it is satisfactory as per IEEE Standards[12]. This System is validated for well-organized precision of power quality, power losses reduction, voltage stabilization, and power factor improvement by means of Matlab/Simulink environment.

The above literature does not deal with Effect of variation of carrier frequency in the Grid Interconnected Wind System with FACTS-SSFC used for Improving Power Quality by Diminishing Total harmonic Distortion. This Paper presents that analysis. This paper is organized as follows: Section II describes the proposed system Section III explains about controller. Section IV Provides Simulation results and analysis. conclusion is given in Section V.

2.THE STATIC SWITCHED FILTER COMPENSATOR

Figure 1 depicts the FACTS SSFC scheme in which the series capacitor $Cs1$ is in series with the line conductors in order to reimburse part of the feeder inductance vigorously. Such fall enhances the power flow and mitigates the feeder reactive power loss. The two three phase shunt capacitor banks $Cf1$ and $Cf2$ are connected with the two series capacitor terminals in parallel. The shunt capacitor banks contribute reactive power compensation and as well get better the regulation of distribution feeder.

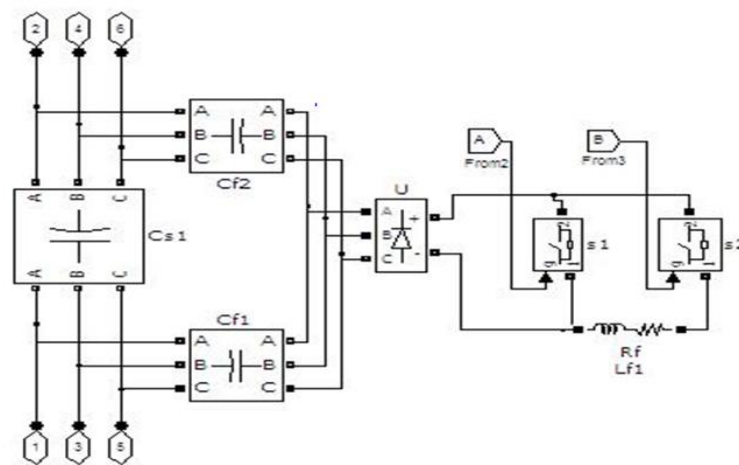


Figure 1. The FACTS Static Switched Filter Compensator scheme

The series capacitor bank operates as a dynamic voltage booster and inflow current preventive device. For the capacitor, the energy discharge path is formed by the six pulses diode rectifier including the resistance (R_f) and inductance (L_f) branch which forms a tuned arm filter at the DC side of the rectifier. The two complementary switching pulses P1 and P2 controls the two IGBT switches S1 and S2. The pulses are generated by the modified VSC controller. The fluctuating topology of the FACTS-SSFC can be varied by the complementary PWM pulses as follow:

Case 1: If P1 is high and P2 is low, the resistor and inductor will be fully shorted and the combined shunt and series capacitors will provide the required shunt and series capacitive compensation to the AC distribution system.

Case 2: If P1 is low and then P2 is high, the resistor and inductor will be connected into the circuit as a tuned arm filter.

3. CONTROLLER DESIGN

Inter-coupled dynamic control depending on two regulators A and B are anticipated to moderate the harmonics, stabilize the buses voltage and develop the power factor by means of the FACTS SSFC. To regulate the switched filter compensator the tri-loop error driven dynamic controller is worn which is a dual action control. The global error is the totting up of the output of the two inter-coupled regulators. The input of the VSC controller is the global error signal which regulates the modulating control signal to the PWM switching block as depicted in Figure 2.

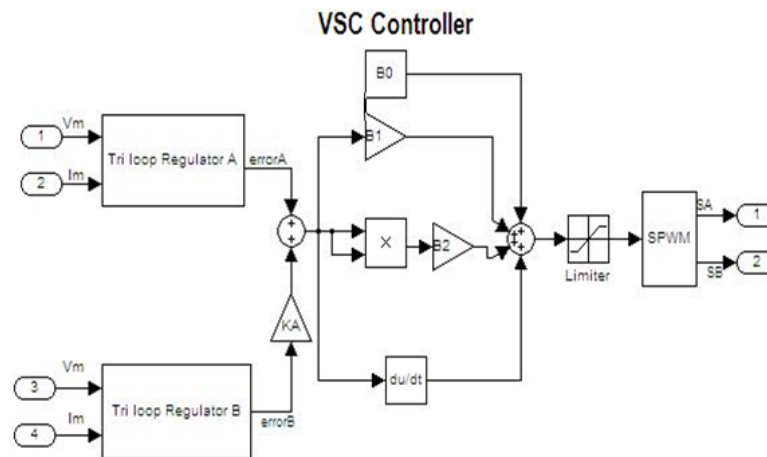


Figure 2. The VSC control of the FACTS SSFC scheme

The function of the Regulator A is to furnish a stable voltage at all busses and progress the power factor. This is achieved by modulating the SSFC admittance. The purpose of the regulator B is to repress harmonics in voltage and current waveforms.

4. SIMULATION RESULTS AND ANALYSIS

4.1. AC system configuration

The proposed AC system is 11 kV distribution network with a renewable wind energy source which is associated to AC grid of 138 kV via step up transformer which is of 11/138kV. A hybrid load consisting of a linear, nonlinear and an induction motor load is connected to the distribution network via 11/4.16kV step down transformer. Figure.3 shows Block Diagram of proposed Grid connected wind system With FACTS-SSFC.

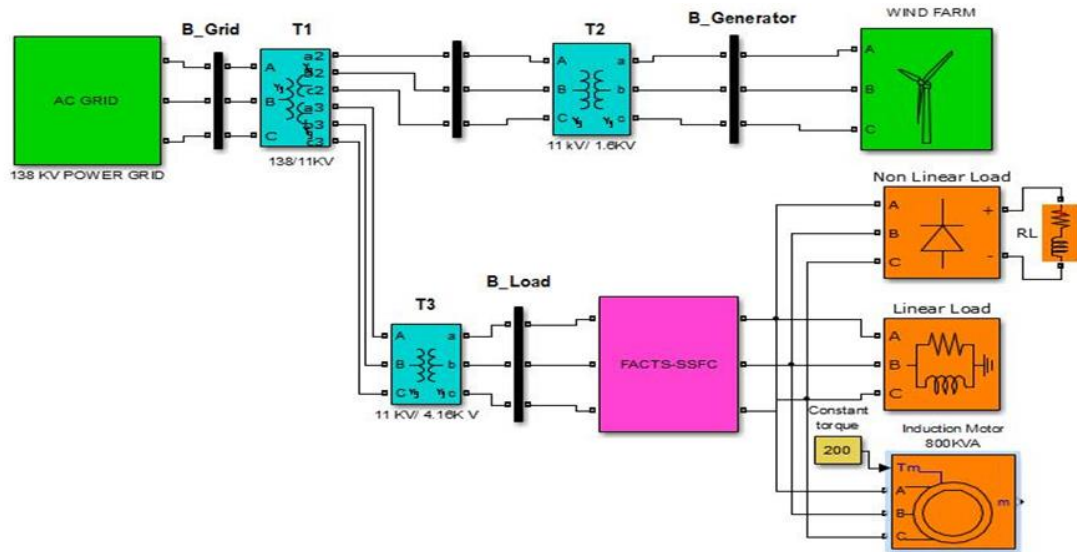


Figure 3. Block diagram of proposed grid connected wind system with FACTS-SSFC

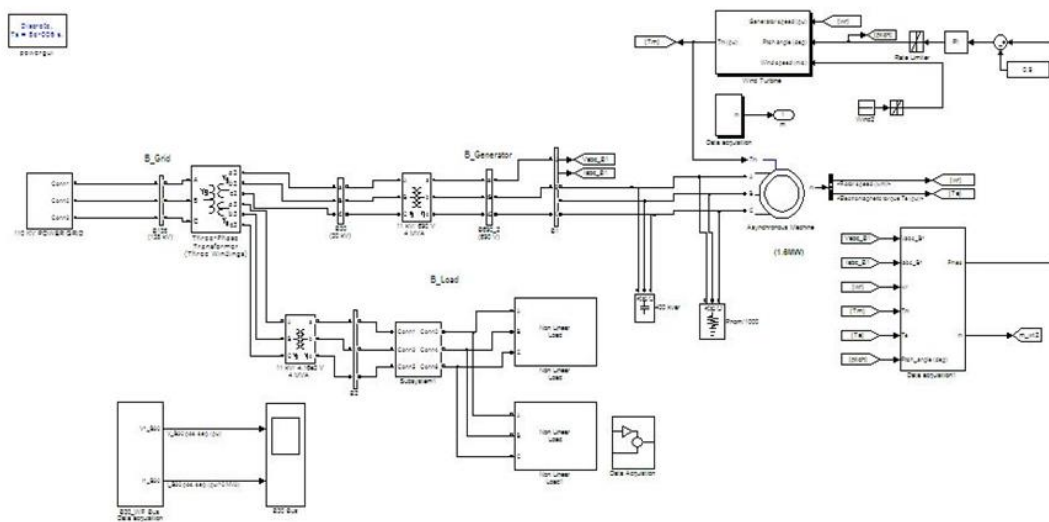


Figure 4. Simulation diagram of proposed grid connected wind system with FACTS –SSFC

Table 1. IEEE-519 Voltage Distortion Limits

	Maximum Individual Harmonic component, %	Maximum THD, %
69KV and below	3.0	5.0
69 KV to 161 KV	1.5	2.5
Above 161 KV	1.0	1.5

In this paper analysis is done for different carrier frequencies at busses wise Grid bus Generator Bus ,Load Bus of the proposed Grid connected wind system with FACTS –SSFC and it is observed in the range of 500Hz to 10000Hz. From the analysis it is observed that the Percentage voltage THD is almost zero through out ,its value is minimum 0.18 to 0.3 maximum for Generator bus ,for load bus its value is 0.02 through out. All these are with in the limits specified by IEEE-519 Voltage Distortion Limits as mentioned in Table 1.

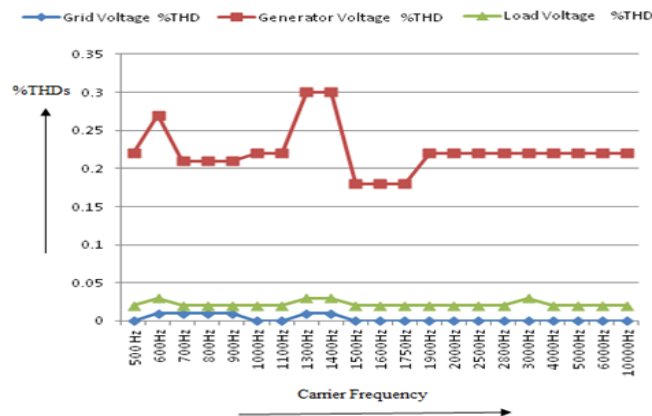


Figure 5. Variation of % voltage THDs of different busses with variation of carrier frequencies

Similarly analysis is done for different carrier frequencies at busses wise Grid bus, Generator Bus, and Load Bus of the proposed Grid connected wind system with FACTS –SSFC and it is observed for the range 500Hz to 10000Hz. From the analysis it is observed that the Percentage current THDs is 0.73 throughout Grid Bus, its value is 0.74 at Generator bus, but at load bus its value varies from 0.61 to 1.11.

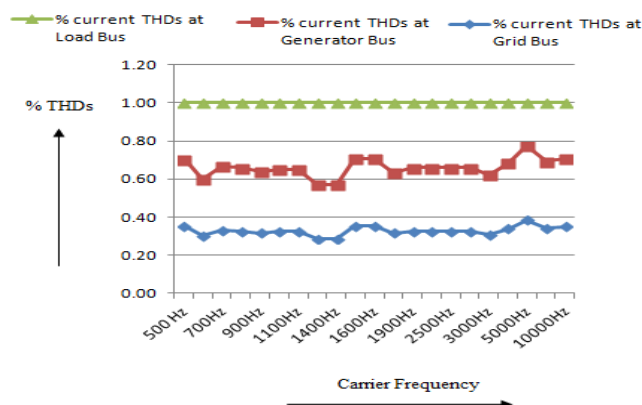


Figure 6. Variation of % Current THDs of different busses with variation of carrier frequencies

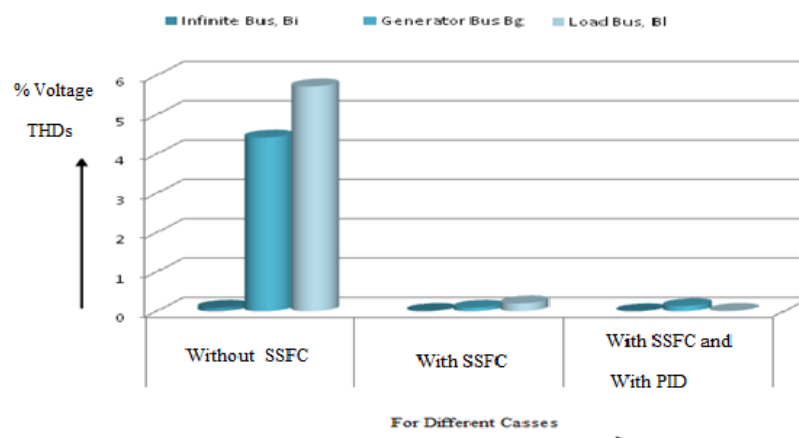


Figure 7. Percentage THDs for voltages at Different busses for different cases

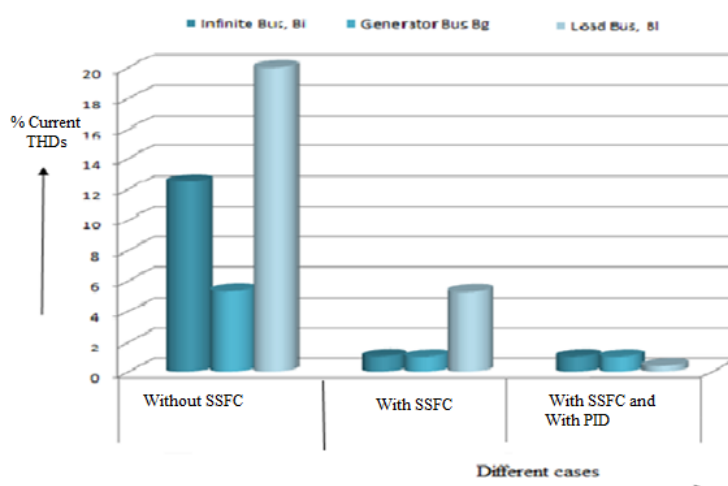


Figure 8. Percentage THDs for currents at Different busses for different cases

Percentage THDs for Voltages and Currents at different busses as Infinite bus, Generator bus and Load bus for different cases as Without SSFC, With SSFC and With SSFC and With PID are shown in Figures 7 and 8 respectively. It is observed that Percentage Total Harmonic Distortions are minimised and it is within the limits as per IEEE Standards.

5. CONCLUSION

This paper provides a concise investigation of the effect of carrier frequency on the proposed grid interconnected wind energy system with FACTS-SSFC at different busses as Grid bus, Generator bus and Load bus in terms of percentage Total Harmonic Distortion of voltages and currents. It is clear from the analysis that carrier frequency is varied from 500Hz to 10,000Hz. There is very less variation in voltages and currents percentage THDs at all busses except at load buses that too for current THDs. For this reason the power lines besides communication lines are not affected by the noise signals and hence Power Quality is improved. The current THD is fluctuating around 1% while minimum being 0.61% at 500Hz and maximum being 1.11% at 1400Hz at Load bus. Similarly voltage THD is fluctuating around 0.3%. It is observed that THDs are well below 2.5% as specified by IEEE standards.

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