CLL/S detuned compensation network for electric vehicles wireless charging application

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Article Info	ABSTRACT	
Article history:	An electric vehicle (EV) can be charged wirelessly through an inductively coupled power transfer system where the system resonates the inductive coupling coil at the operating frequency by means of a compensation network. However, the resonant behaviour implies overcurrent in the inverter MOSFETs during uncoupled and high load resistor conditions, which affect to the inverter MOSFETs failure whenever the current exceeds their	
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Keywords:	maximum current rating. Therefore, this paper presents a CLL/S detuned compensation network for EV wireless charging application to limit the	
Compensation circuit ICPT Inductor detuned Maximum current rating Zero voltage switching	inverter current during the uncoupled state and open circuit load conditions. The proposed compensation consists of a series capacitor and parallel inductor at the primary side and series capacitor at the secondary side. By using Kirchhoff Voltage Law (KVL) analysis, the primary side is detuned to inductive behaviour to ensure zero voltage switching (ZVS), and the secondary side remains at natural resonant tuning. From the simulation analysis, the slight increase of the parallel inductor value at the primary side can limit of the inverter current at uncoupled and high load resistor conditions. The proposed system also offers higher efficiency than the seriesseries detuned compensation and slightly lower than the double-sided LCC compensation network at different coupling factor.	
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1. INTRODUCTION

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Wireless charging is an upgraded technology to charge electric vehicles (EV) instead of the plug-in method since it promotes convenience to the EV drivers [1]. The wireless charger pad will charge EV automatically when the vehicle parks over the pad [2]. This system is safer than wired charging in terms of electricity exposure as an insulated plate covers and seals the transmission coil [3]. Most of EV wireless charging systems are inductively coupled power transfer (ICPT) where the magnetic resonance field transfers the electric power [4]. The system consists of transmitter and receiver coils, and they locate near to each other. Both coils must operate at a similar resonant frequency to transfer energy efficiently [5]. Thus, a compensation circuit assists the coils to achieve resonance.

Two-capacitor compensations are famous compensations in the two-coil resonator system of ICPT. Each coil utilizes one compensated capacitor. The capacitor connection at primary and secondary coils are including, series-series (SS), series-parallel (SP), parallel-series (PS) or parallel-parallel (PP) [6]. The SS compensation is the most practical among them for EV wireless charging application as it offers high tolerance of misalignment and independent from load variation [7]. However, these capacitors suffer from high voltage stress at resonance [8]. An extended compensation topology such as Pi-network or T-network connection is introduced by adding inductor in order to distribute voltage stress between passive elements

and coils [9]. Besides, the T-network and Pi-network generate constant voltage output and constant current output, respectively [10]. Thus, their resonant frequencies are irrelevant to the coupling coefficient [11]. For example, LCL, LCC-S, S-CLC, LC-S and double-sided LCC are compensation structures presented in previous articles [11–15]. Although all these compensations are tuned to zero voltage switching (ZVS), the coupling coil size limits the parameter of compensation elements.

The ZVS operation is a necessary criterion to obtain soft switching in the inverter power MOSFETs [16, 17]. Based on previous research, the compensation circuit must be tuned slightly to the inductive region from the resonance point by increasing the secondary capacitor value or reducing the coil self-inductance parameter slightly [15]. Maintaining unity power factor or zero-phase angle (ZPA) operation in square wave input voltage is challenging since most of the designed circuit is based on fundamental harmonic analysis (FHA) [18]. Furthermore, the voltage transition of MOSFET between on and off produces slew rates that leads to significant switching loss if high input current at switching point [15, 19]. Moreover, the ZVS is preferable than zero current switching (ZCS) since most of the power MOSFET embeds flywheel diode that conducts reverse current in the transistor [15]. Even though the ZVS tuning is approximate to the natural resonant frequency, the ICPT still draws overcurrent issue from the inverter, especially at uncoupled and high load resistor conditions [20, 21]. Consequently, the excessive current brakes down the inverter since the current exceeds the MOSFET maximum current rating and damages the MOSFETs [22].

There are passive and active control techniques that can prevent excessive current through the inverter. The passive methods are including compensation structure of double-sided LCC and SS detuning [15, 21]. The inverter current flows to the double-sided LCC compensation is directly proportional to the coupling factor. Nevertheless, the current contains high total harmonic distortion, which leads to hard switching in the inverter [23]. On the other hand, the capacitor detuning technique of SS compensation changes the value of the capacitors so that the primary side tune to inductive characteristic while the secondary circuit tune to capacitive characteristic. It benefits the system to regulate power transfer at a wide range of coupling factor and prevent the excessive current issue. Unfortunately, the detuning approach exacerbates the power transfer efficiency. Otherwise, active control methods such as phase shifting at the H-bridge inverter and duty cycle variation at DC boost converter require feedback information from the receiving side, which utilizes wireless communication systems [20, 24]. Hence, it implies to high possibility of receiver data to be interrupted due to signal interference with a strong magnetic field [25]. Thus, this problem deteriorates ICPT system reliability.

In this study, a novel compensation scheme of CLL/S will be proposed to constraint the inverter current to the coupling coils as according to maximum rating current of power MOSFET inverter. The proposed compensation consists of a series capacitor and a parallel inductor connected in L-network at primary circuit while a series capacitor at the secondary circuit. It only requires the inductor parameter tuning at the primary circuit to match the MOSFET current rating without affecting its power transfer efficiency. This paper addressed four objectives which are understanding the input current characteristic based on mutual inductance equivalent circuit model at series-series resonant state, deriving detuned parameter of proposed compensation network using KVL circuit analysis on reactance representation of the compensation network and the equivalent circuit model, analysing input current characteristic at different detuning factor and comparing the proposed power transfer efficiency with previous compensation networks that consider overcurrent issue at uncoupled condition.

2. ICPT FUNDAMENTAL STUDY

Before deriving and analysing the proposed compensation network, the ICPT system need to be simplified in order to comprehend the input current characteristic at resonant state with varying coupling coefficients. Figure 1 illustrates a basic configuration of ICPT which covers DC voltage source, inverter, impedance matching network (IMN) or compensation circuit, coupling coils, rectifier and battery. The DC voltage source can represent the output voltage of the boost converter at the primary side. A square wave voltage generated by the inverter, vs, can be defined in Fourier expansion form as

$$v_{S}(t) = \frac{4}{\pi} \sum_{k=1}^{\infty} \frac{\sin((2k-1)\omega t)}{2k-1},$$
(1)

 ω and t is angular frequency and time, respectively. Total summation of odd harmonics produces approximately a square wave signal. At the secondary side, an equivalent end load resistor, R_E replaces the batteries and rectifier. Considering R_E accepts any voltage levels to disregard DC buck converter function at the secondary side. Thus, R_E based on equivalent resistance battery, $R_{battery}$ and receiver rectifier is [11]

$$R_E = \frac{8}{\pi} R_{Battery} \tag{2}$$

This simplification model is presumed for studying mutual inductance of the coupling coils, especially the excitation voltage characteristic at the secondary coil and designing the proposed compensation network.



Figure 1. Simplified ICPT system. (IMN is impedance matching network)

2.1. Mutual inductance of coupling coils

According to mutual inductance principle, when two separated coils locate near to each other, additional excitation voltages appear to both coils caused by mutual inductance phenomena where the mutual inductance is defined as

$$M = k \sqrt{L_1 L_2} \tag{3}$$

M, *k*, L_1 and L_2 are mutual inductance, coupling coefficient, primary coil self-inductance and secondary coil self-inductance, respectively. Both primary and secondary coils consist of excitation voltages derived from the multiplication of mutual inductance reactance, $j\omega M$ and current of the opposite side. Figure 2 shows an equivalent circuit model of the coupling coil in phasor form based mutual inductance principle. The voltage across at primary, v_1 and secondary circuit, v_2 are

$$v_1 = j\omega L_1 I_1 - j\omega M I_2 \tag{4}$$

$$v_2 = -j\omega L_2 I_2 + j\omega M I_1 \tag{5}$$



Figure 2. Mutual inductance model

 I_1 and I_2 are the primary and secondary current flow through the coil. Because both excitation voltages, $j\omega MI_n$, rely on opposite current, I_n , the excitation voltage of the primary side can be replaced with a reflected impedance to the primary side, Z_R . Let's say, $v_2 = Z_{Load}$. I_2 , then

$$I_{2} = \frac{j\omega M I_{1}}{Z_{Load} + j\omega L_{2}}$$

$$= \frac{j\omega M I_{1}}{Z_{s}},$$
(6)

where Z_s is the total impedance of the secondary side. Substitute I_2 in (2) with (6). Then the primary voltage, v_l will be

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$$v_{1} = j\omega L_{1}I_{1} + \frac{(\omega M)^{2}}{Z_{s}}I_{1}$$

$$= (j\omega L_{1} + Z_{R})I_{1}.$$
(7)

From (7), the Z_R is inversely proportional to the Z_s and directly proportional to $(\omega M)^2$. Figure 3 illustrates a phasor model of coupling coil after Z_R replaces the primary coil excitation voltage, $j\omega MI_2$. In order to understand the characteristic of secondary voltage, series capacitors need to compensate both self-inductance coils. At resonance coupling, the current flows through the capacitor and coil is considered as short-circuited. Hence, the I_I varies concerning Z_R values. Then, the voltage at the secondary coil is induced based on two factors, which are the primary current, I_I and mutual inductance reactance, $j\omega M$.



Figure 3. Equivalent circuit model of ICPT system for series-series compensation network

Figure 4 shows the secondary excitation voltage, ωMI_I , I_I and ωM at different coupling coefficients, k. The I_I current decreases exponentially when k increases, whereas the I_I reciprocal curve lessens as the Z_R value gains. The ωM graph is a linear increase, and its gradient depends on the coil quality factor and its physical structure, including coil geometry, size, and material [5]. On the other hand, the I_I curve can be manipulated by implementing passive control or active control techniques that have been mentioned in the introduction section. Hence, the proposed CLL/S compensation network will focus on altering the I_I characteristic at varying coupling coefficient.

As refer to Figure 4, the I_1 curve escalates gradually when the secondary circuit reaches an open circuit condition. The Z_R at the primary circuit is inversely proportional to the total impedance of the secondary circuit, Z_s . When the Z_s increases due to R_E increment, the Z_R decreases. As a result, the primary current increases gradually during charging the battery and increases tremendously as coupling weaken or reduces. The latter circumstance, which occurred when the receiver coil is moving away from the transmitter coil while the I_I is still flowing in the transmitter coil, must avoid since overcurrent can damage the MOSFETs in H-bridge inverter due to the drain-to-source current exceeding the maximum current rating of MOSFET.



Figure 4. Characteristic of excitation voltage from the secondary side obtained from primary current and mutual inductance reactance

3. CLL/S COMPENSATION NETWORK

In this proposed system, the current can be reduced during the uncoupled condition by detuning the compensated inductive at the primary side. A CLL compensation structure is a T-network structure where compensated capacitor, C_I is connected in series to self-inductance of the primary coil, L_P while compensated inductor, L_I is in parallel to L_P as illustrated in Figure 5. A compensated capacitor, C_2 at the secondary side

connects in series to self-inductance of the secondary coil, L_S in order to generate constant output voltage to the end load and minimize the utilization of passive components at the pickup coil area. The *CL* compensation network is selected since the detuning element of L_I in the *CL* network will not lessen the primary coil current [26]. Thus, it affects the excitation voltage of the secondary coil.



Figure 5. Proposed CLL/S compensation network

Derivation of CLL compensation network begins by defining the reactance of capacitor, X_c and inductor, X_L which is

$$X_C = \frac{1}{\omega C} \tag{8}$$

$$X_I = \omega L \tag{9}$$

where ω is angular frequency. The proposed network must be tuned to inductive characteristic since to maintain ZVS in MOSFET inverter. The tuning must move near to natural resonant frequency for utilizing magnetic resonant coupling behaviour in the system. Thus, to obtain natural resonance frequency in the primary side, the coil is assumed to be uncoupled where the Z_R is equal to zero value. Therefore, total reactance in the primary side, X_T must be zero

$$-jX_{C} + (jX_{Lp} \parallel jX_{L1}) = X_{T} = 0$$

$$X_{C} = \frac{X_{L1}X_{Lp}}{(X_{L1} + X_{Lp})}$$
(10)

 X_{LI} , X_{Lp} , X_C are reactance of compensated inductor, L_I , self-inductance coil, L_P , and compensated capacitor, X_C . By replacing reactance terms in (10) with (8) and (9), the natural resonant frequency of CLL compensation is

$$\omega = \sqrt{\frac{1}{L_1 C_1} + \frac{1}{L_P C_1}} \tag{11}$$

If the value of L_P is equal to L_I , the value of C is

$$-jX_{C} = -j\frac{X_{Lp}}{2}$$

$$C = \frac{2}{\omega^{2}L_{p}}$$
(12)

All compensated reactance parameters in the circuit are referring to reactance X_{Lp} parameter. The critical parameter to investigate is the magnitude and phase angle of input current, I_l , since the concern of this proposed structure to limit the current flow through the MOSFETs inverter by tuning to inductive behaviour. Thus, the I_l can be obtained by using Kirchhoff Voltage Law (KVL) to the primary circuit,

$$I_{1} = \frac{2U_{AB} \left(Z_{R} + j \left(X_{Lp} + X_{L1} \right) \right)}{\left(X_{Lp}^{2} - X_{Lp} X_{L1} \right) + j Z_{R} \left(2X_{L1} - X_{Lp} \right)}$$
(13)

and the I_l angle concerning voltage source, U_{AB}

$$\alpha_{1} = \arctan \frac{X_{Lp} \left(X_{Lp}^{2} - X_{L1}^{2} \right) - Z_{R}^{2} \left(2X_{L1} - X_{Lp} \right)}{2Z_{R} X_{L1}^{2}}$$

$$= \arctan \frac{\omega^{2} L_{P} \left(L_{P}^{2} - L_{1}^{2} \right) - Z_{R}^{2} \left(2L_{1} - L_{P} \right)}{2Z_{R} \omega L_{1}^{2}}$$
(14)

To achieve ZVS operation, the I_l angle, α_l must less than 0° to indicate current lagging condition. Therefore,

$$Z_{R}^{2}(2L_{1}-L_{P}) > \omega^{2}L_{P}(L_{P}^{2}-L_{1}^{2})$$
(15)

The value of L_1 can be calculated from (13) by assuming $Z_R = 0$

$$X_{L1} = \frac{X_{Lp} \left(X_{Lp} I_1 + 2U_{AB} \right)}{X_{Lp} I_1 - 2U_{AB}}$$

$$L_1 = \frac{L_p \left(\omega L_p I_1 + 2U_{AB} \right)}{\omega L_p I_1 - 2U_{AB}}$$
(16)

Meanwhile, the primary coil current, I_{Coil}, is

$$I_{Coil} = \frac{2U_{AB}X_{L1}}{Z_R \left(2X_{L1} - X_{Lp}\right) + j \left(X_{L1}X_{Lp} - X_{Lp}^2\right)}$$
(17)

The I_{Coil} is another concerned value where it determines the secondary side excitation voltage. Its angle will not affect the excitation voltage value, but it shifts the excitation voltage phase angle with respect to the voltage source, U_{AB} .

4. SIMULATION RESULT AND ANALYSIS

A simulation has been conducted to analyse the CLL/S compensation network using MATLAB Simulink R2018 platform. This simulation observes four main parameters, which are input current, I_I , its phase angle, α_I , coil current, I_{coil} as well as power transfer efficiency for varying values of compensation inductor, L_I . In simulation analysis, assumptions of the voltage source, U_{AB} and equivalent load resistance, R_E are necessary for analytical model. The series compensation capacitor at secondary side eliminates coil self-inductance reactance at secondary which remains the R_E only. Therefore, the Z_S in (6) is equivalent to R_E so that the Z_R can be estimated based on the coupling coefficient.

Table 1 listed other parameters for simulation analysis. The value is selected based on wireless charging of electric vehicle application in [27]. The R_E value has two different value to indicate two conditions of battery charging, 5 Ω is equivalent to 5% charging battery level while 200 Ω is assumed to be 100% or full battery charge condition. These two values are studied since the characteristic of primary current change as the end load becomes an open circuit. In the graph results, all reactance, including the R_E , mutual inductance reactance, is normalized by primary coil self-inductance reactance.

Table 1. Parameter for CLL/S compensation network

Parameter	Symbol	Value
Input Voltage	U_{AB}	200 V
Operating frequency	f	85 kHz
Primary coil self-inductance	L_P	120 µH
Secondary coil self-inductance	L_S	120 µH
Equivalent Load Resistance	R_E	5Ω/200Ω
Primary compensated capacitor	C_{I}	58.4 nF
Secondary compensated capacitor	C_2	29 nF

Figure 6 illustrates the normalized coil current values and its phase angle when the L_1 is varied. The base coil current, I_{Coil} (base) is equivalent to voltage source squared, U_{AB}^2 divided by R_E . When the battery is charging, the R_E value increases gradually, and Z_R decreases at the primary side. It causes high current flows to the primary circuit at resonant state, which is the ratio X_{LI}/X_{Lp} equals unity. The ratio must detune to over unity value to reduce the current at the required level. The ratio must exceed than unity value to ensure lagging current in the circuit so that ZVS can be realized. Consideration of compensated inductor, L_1 is critical at high load R_E ($R_E = 200 \Omega$) because a slight change of compensated inductor leads to current drop significantly, especially at weak coupling. At low load ($R_E = 5 \Omega$), the variation of L_1 has less implication to I_{Coil} value. For weak coupling at low load, the normalize current, I_{Coil} declines from 0.4 to 0.2 A when the ratio X_{LI}/X_{Lp} change from 1 to 1.2. If the ratio higher than 1.2, the energy transfer between the coil becomes less effective since the current flow through the coil is inadequate to induce a strong magnetic field [26]. Therefore, the selection of compensated inductor is critical since it must reconcile between effective power transfer and limitation of current through MOSFETs inverter.

According to (13), a slight increase of L_1 will reduce the input current, I_1 to a specified value. Figure 7 illustrates the effect of input current when the R_E increases at varying k. When the battery is fully charged, the input current will not exceed than the current limit at uncoupled state, as for this case, 43 A. Meanwhile, the input impedance angles remain in the inductive region at varying coupling coefficient. The angle increases to 87° as the R_E increases. However, it is still operating in the inductive region. The low R_E changes the input current significantly as the coupling coefficient rises since the mutual inductance reactance squared, $(\omega M)^2$ influences the Z_R value more than the R_E which result on changes to input current as k increases. As the R_E load gains, the influence of $(\omega M)^2$ diminishes that results on constant current supply from input current at varying k. Thus, the variation of L_1 can limit the current supply and preserve the MOSFETs in inverter from exceeding their maximum rating.



Figure 6. Normalized coil current and its phase angle characteristic at two load conditions based on ratio X_{LI}/X_{Lp} (left k = 0.18, right k = 0.32)



Figure 7. Input current and angle of input impedance for L_l equal to 140 μ H ($X_{Ll}/X_{Lp} = 1.167$)

Figure 8 shows the voltage gain at different L_1 where the peak voltages are shifting to higher k as the L_1 increases but the peak voltage gain not as high as the lower L_1 . When the L_1 value increment from 140 μ H to 160 μ H, the current limit value decline almost half. The significant change is due to the high-quality factor in the primary circuit, where the ratio of primary impedance between the imaginary part to the real part is very

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high. Thus, the circuit has narrow bandwidth and sensitive to any reactance change inside the circuit. However, as the L_1 gains from 160 μ H to 180 μ H, the current limit reduces almost one third or 30% of the previous value. At this time, the coil current is reaching to the saturated value of the inductive region as if the L_1 value gain more, the coil current remains consistent to a specific value as illustrated in Figure 5. Hence, the maximum current rating of MOSFETs inverter determines the L_1 values in this compensation network.

In terms of efficiency, the proposed compensation scheme is approximately similar as the double-sided LCC and better than detuning of the SS technique. Even though the compensated inductor tunes to other value, as to match with a maximum current rating of MOSFETs in an inverter, its efficiency remain unchanged. This efficiency characteristic is simulated at low battery condition; for this case, R_E is equal to 5 Ω . The proposed compensation efficiency achieves more than 80% when the coupling coefficient higher than 0.12. The efficiency of all compensation, however, drops when the R_E increases due to the power consumed by the battery decreases at full charge state. Still, the highest efficiency is SS tune compensation at resonant.



Figure 8. Voltage gain at three different parameters of L_1 (left) and Comparison of Efficiency between proposed CLL/S, double-sided LCC and SS detuned (right)

5. CONCLUSION

This paper proposed a detuned compensation network to solve the overcurrent issue at MOSFETs inverter during uncoupled and high resistor load conditions while maintaining soft switching. From mutual inductance equivalent circuit model study, the primary coil current influence the voltage excitation level at the receiver coil where the current increases tremendously at weak coupling and high load resistor value. This circumstance can cause MOSFETs inverter failure if the input current exceeds its maximum current rating. The proposed CLL compensation at the primary side can constrain the input current by adjusting the compensation inductor. The simulation analysis indicates that the compensation inductor value must be higher than the self-inductance coil for ZVS operation in MOSFET. In terms of efficiency, the proposed compensation offers better efficiency than detuned series-series compensation and almost equivalent to double-sided LCC compensation efficiency.

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