Application of direct MRAC in PI controller for DC-DC boost converter

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Article Info	ABSTRACT Almost all electronic components require a DC power supply at present days.	
Article history:		
Received Jul 27, 2019 Revised Oct 22, 2019 Accepted Jan 9, 2020	The needs of DC power supplies from low voltage scales, medium voltages such as generators, to high voltage scales for high voltage electricity transmission. The improvement of PI controller performances is presented in this paper. The adaptation gains improve transient response of DC-DC Boost Converter several operating conditions. Massachusetts Institute of	
<i>Keywords:</i> Boost Converter PI	Technology (MIT) rule is applied as an adaptive mechanism to determine the optimal control parameters in some conditions. The used adaptive control technique is Direct Model Reference Adaptive Control (MRAC), this method as able to control system in some various input voltage. The proposed	
DMRAC Adaptation Gain	method has a stable response and able to reach the model reference smoothly. However, the response of the system has instantaneously overshoot and follows the response back of model reference. The responses of proposed controller have short period of rise time, settling time, and overshoot.	
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1. INTRODUCTION

Nowadays, the high efficiency is suitable to build the electronic devices and power converter has an important role in this system. The stepped up or down DC voltage is the purpose of power converter, even though using a transformer are commonly used in AC voltage. A conventional power converter still an option in industry such as industry of battery charger, uninterrupted power supplies, and other energy conversion systems [1-7].

It is required electronic components to reach the desired voltage. Recent power converter components have high quality and high efficiency. One of them is DC-DC boost converter which have utilization to step the output voltage up [8, 9]. In order to create the DC-DC boost converter rapid changes of rensponse, it must work in high frequency [10, 11]. In this condition, DC-DC boost converter requires controller to manage the desired value.

Adaptive controls are widely used by researchers to solve dynamic problems [12-18] and some of them use PID as its control structure. Conventional PID is based on a mathematical model, such that it has stability, reliability and control capabilities. Conventional PID controllers are effective in linear systems, but it is not suitable for non-linear systems and high-order systems. Determination of PID parameters has been used in many ways [19]. Several methods have their own advantages and drawbacks to determine PID parameters for reaching stable system. The fixed parameter in PID controller is not quite robust or not able to adapt and therefore the adaptive controller techniques is required improve system response [20, 21]. Several

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adaptive control techniques are employed to fix this problem and one of them use Direct Model Reference Adaptive Control (DMRAC) [22, 23]. DMRAC performances are provided by model as reference, it means the plant response must follow model response. The following parameter adjustment mechanism is calculated by using Massachusetts Institute of Technology (MIT) rule [24, 25]. In order to find out the response of boost converter as a non-linear system, the paper offers DMRAC in PI controller as an alternative controller to compensate for input voltage variations.

2. PROPOSED METHOD

This section describes a DC-DC boost converter model and derivation of adaptation gain using DMRAC.

2.1. DC-DC Boost Converter Model

DC-DC boost converter is commonly used in DC system and also known as step up DC converter. It has simple configuration and one of non-isolated power converter as shown in Figure 1. The output voltage demand must be greater than the input voltage and continuous. It utilizes two semiconductors such as a controlled power device, and an uncontrolled device. They consist basically of an inductor in series and capacitor in parallel.

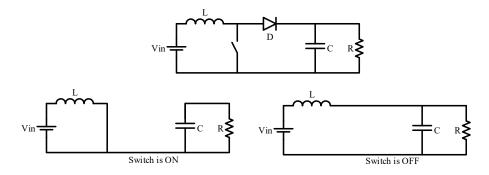


Figure 1. DC-DC boost converter states; switch ON and switch OFF

The current of inductor i_L and voltage of capacitor V_C are the main parameters. During the switch is ON, equation (1) specify i_L and (2) is derived to obtain V_C .

$$V_{in} = L \frac{di_L}{dt} \tag{1}$$

$$0 = C \frac{dv_c}{dt} + \frac{v_c}{R}$$
⁽²⁾

During the switch is OFF, the inductor releases the charge to the capacitor and load. The condition of v_C is defined in (3) and i_L can be showed in (4).

$$v_c = V_{in} - L \frac{di_L}{dt}$$

$$\frac{dv_c}{dt} = V_c$$
(3)

$$i_L = C \frac{dt}{dt} + \frac{c}{R} \tag{4}$$

According to the ON and OFF state, the average value of model for boost converter is shown in state space as (5).

$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_C}{dt} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{(1-d)}{L} \\ \frac{(1-d)}{C} & -\frac{(1-d)}{RC} \end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} V_{in}$$
(5)

In order to define the small signal model of boost converter, substituting every variable is required. Each parameter is presented in steady state part and small signal variation as follows,

$$d = D + \hat{d}$$

$$i_{L} = I_{L} + \hat{i}_{L}$$

$$v_{c} = V_{c} + \hat{v}_{c}$$

$$v_{in} = V_{in} + \hat{v}_{in}$$

$$L \frac{d(I_{L} + \hat{i}_{L})}{dt} = (V_{in} + \hat{v}_{in}) - (1 - D - \hat{d})(V_{c} + \hat{v}_{c})$$

$$C \frac{d(V_{c} + \hat{v}_{c})}{dt} = -(1 - D - \hat{d})(I_{L} + \hat{i}_{L}) - \frac{(V_{c} + \hat{v}_{c})}{R}$$
(6)
(7)

The steady state variables become zero by deriving these terms and the multiplication results of small signal variation are neglected. The small signal model for inductor current and capacitor voltage of boost converter as given by (8).

$$\frac{d\hat{i}_{L}}{dt} = \frac{\hat{d}V_{c} + \hat{v}_{in} - (1 - D)\hat{v}_{c}}{L}$$

$$\frac{d\hat{v}_{c}}{dt} = \frac{(1 - D)\hat{i}_{L} - \hat{d}I_{L}}{C} - \frac{\hat{v}_{c}}{RC}$$
(8)

The derived transfer function (9) from the output voltage to the duty cycle shows the effect of the duty cycle modify the capacitor voltage when the duty cycle performs.

$$\frac{\hat{v}_{c}(s)}{\hat{d}(s)} = \frac{(1-D)V_{c} - LI_{L}s}{LCs^{2} + \frac{L}{R}s + (1-D)^{2}}$$
(9)

2.2. Direct Model Reference Adaptive Control with PI Controller

Adaptive control system is a control system that has adjusted control parameters according to some disturbances. In DMRAC, the desired system performance is stated in a reference model. Figure 2 shows the block diagram of the DMRAC. The control parameters are changed based on error feedback, which is the difference between the output system and the output of reference model. The structure of DMRAC is divided in two loops, the inner loop and outer loop. The PI controller is employed in this research because the structure of PI controller is simple to execute in implementation. PI controller is one of single input single output (SISO) method, this controller has no ability to follow the diversity of environment.

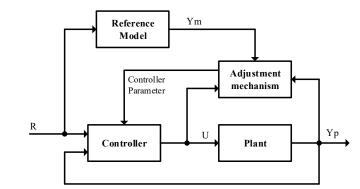


Figure 2. Direct model reference adaptive control

The boost converter is 2nd order system, such that the described system by 2nd order model as:

$$\frac{Y_p(s)}{U(s)} = \frac{-b_1 s + b_2}{s^2 + a_1 s + a_2}$$
(10)

The reference model in 2nd order system given by:

$$\frac{Y_m(s)}{U(s)} = \frac{bm_1s^2 + bm_2s + bm_3}{s^3 + am_1s^2 + am_2s + am_3}$$
(11)

The term of PI controller as:

$$\frac{U(s)}{R(s) - Y_p(s)} = K_p + \frac{K_i}{s}$$
(12)

Transfer function of boost converter and PI controller is

$$\frac{Y_p(s)}{R(s)} = \frac{(b_1 s + b_2)(K_p s + K_i)}{s(s^2 + a_1 s + a_2) + (b_1 s + b_2)(K_p s + K_i)}$$
(13)

And

$$Y_{p}(s) = \frac{(b_{1}s + b_{2})(K_{p}s + K_{i})}{s(s^{2} + a_{1}s + a_{2}) + (b_{1}s + b_{2})(K_{p}s + K_{i})}R(s)$$
(14)

The difference between the output system and reference model:

$$\varepsilon = Y_p - Y_m \tag{15}$$

The form of cost function is chosen by:

$$J(\theta) = \frac{1}{2}\varepsilon^{2}(\theta) \tag{16}$$

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Where \mathcal{E} indicates the error between plant output and model reference output. The θ has no fix value and it is set in such a way such that J is minimized to zero. In MIT rule, Negative gradient of J is needed to determine time rate of change of θ as shown in (17).

$$\frac{d\theta}{dt} = -\gamma \frac{\partial J}{\partial \theta} = -\gamma \varepsilon \frac{\partial \varepsilon}{\partial \theta}$$
(17)

$$\frac{dK_p}{dt} = -\gamma_p \frac{\partial J}{\partial K_p} = -\gamma \frac{\partial J}{\partial \varepsilon} \frac{\partial \varepsilon}{\partial Y_p} \frac{\partial Y_p}{\partial K_p}$$
(18)

Where; $\frac{\partial J}{\partial \varepsilon} = \varepsilon$, $\frac{\partial \varepsilon}{\partial Y_p} = 1$

Where, $\partial J/\partial \varepsilon = \varepsilon$, $\partial \varepsilon/\partial y = 1$

Parameters K_p and K_i are determined by applying MIT gradient rules (19).

$$\frac{dK_{p}}{dt} = -\gamma_{p} \frac{\partial J}{\partial K_{p}} = -\gamma_{p} \left(\frac{\partial J}{\partial \varepsilon}\right) \left(\frac{\partial \varepsilon}{\partial Y_{p}}\right) \left(\frac{\partial Y_{p}}{\partial K_{p}}\right)$$

$$\frac{dK_{i}}{dt} = -\gamma_{i} \frac{\partial J}{\partial K_{i}} = -\gamma_{i} \left(\frac{\partial J}{\partial \varepsilon}\right) \left(\frac{\partial \varepsilon}{\partial Y_{p}}\right) \left(\frac{\partial Y_{p}}{\partial K_{i}}\right)$$
(19)

$$\frac{\partial Y_{p}}{\partial K_{p}} = \left(R(s) - Y_{p}(s)\right)
\frac{(-b_{1}s + b_{2})s}{\left(a_{1}s^{3} + \left(a_{2} - b_{1}K_{d} - b_{1}K_{p}\right)s^{2} + \left(a_{2} + b_{2}K_{d} - b_{1}K_{i} + b_{2}K_{p}\right)s + b_{2}K_{i}\right)}
\frac{\partial Y_{p}}{\partial K_{i}} = \left(R(s) - Y_{p}(s)\right)
\frac{-b_{1}s + b_{2}}{\left(a_{1}s^{3} + \left(a_{2} - b_{1}K_{d} - b_{1}K_{p}\right)s^{2} + \left(a_{2} + b_{2}K_{d} - b_{1}K_{i} + b_{2}K_{p}\right)s + b_{2}K_{i}\right)}$$
(20)

(21)

From equation (20) and (21), it is obtained, $\frac{\partial Y_p}{\partial K_p}$, $\frac{\partial Y_p}{\partial K_i}$ as equation (22) and (23).

$$\frac{dK_{p}}{dt} = -\gamma_{p} \frac{\partial J}{\partial K_{p}}
= -\gamma_{p} \varepsilon \left(R(s) - Y_{p}(s)\right)
\frac{\left(-b_{1}s + b_{2}\right)s}{\left(a_{1}s^{3} + \left(a_{2} - b_{1}K_{d} - b_{1}K_{p}\right)s^{2} + \left(a_{2} + b_{2}K_{d} - b_{1}K_{i} + b_{2}K_{p}\right)s + b_{2}K_{i}\right)}$$

$$\frac{dK_{i}}{dt} = -\gamma_{i} \frac{\partial J}{\partial K_{i}}
= -\gamma_{i} \varepsilon \left(R(s) - Y_{p}(s)\right)
\frac{-b_{1}s + b_{2}}{\left(a_{1}s^{3} + \left(a_{2} - b_{1}K_{d} - b_{1}K_{p}\right)s^{2} + \left(a_{2} + b_{2}K_{d} - b_{1}K_{i} + b_{2}K_{p}\right)s + b_{2}K_{i}\right)}$$
(22)

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 $b_1, b_2, a_1, \text{ and } a_2$ have an unknown number.

3. RESEARCH METHOD

The research is based on the output voltage generated by the boost converter which has not been properly regulated. This problem occurs when there are changes in input voltage, reference voltage, and the load. This research that will be carried out in a boost converter using a PID controller and a boost converter simulation using DMRAC. The conventional controller PID is utilized to compare the proposed controller that verified by varying input voltage. Table 1 presents the parameters of boost converter.

Table 1. DC-DC boost converter parameters		
Parameter	Symbol	Value
Input Voltage	V_{in}	12V
Inductance	L	4.7 <i>mH</i>
Resistance	R	100Ω
Capacitance	С	$470\mu F$

By using these pasameters, the model of DC-DC boost converter (5) is utilized as a plant of the system. The derivation DMRAC based on PI controller obtain (22) and (23), these equations are the adaptive controller model. The value of gammas is specified to achieve the appropriate response. Some scenarios for changing the input voltage and load are set to find out the response of the proposed controller.

4. RESULTS AND ANALYSIS

The performance of boost converter in proposed controller is proven in simulation such that any changed responses are able to be observed. The input voltage and resistor load of boost converter are 12 V and 100 Ω , respectively. Reference voltage is set to be 48 V and the value of gammas as following,

$$\gamma_p = -2 \times 10^3$$
$$\gamma_i = -7 \times 10^5$$

The comparison of conventional PID and adaptive PI in boost converter is shown in Figure 3 and Figure 4. Increasing of the input voltage from 12V to 24V at 0.3s makes the little oscillation in output voltage. The proposed controller can reach steady state less than 0.03s with a slight overshoot. The transient response of proposed controller has a good response and same response to reference model. In Figure 3,

tension stress of proposed controller is more stable even though the V_{in} is changed at 0.3s. The conventional PID needs 0.06s toward the reference model when there is an increase in the input voltage.

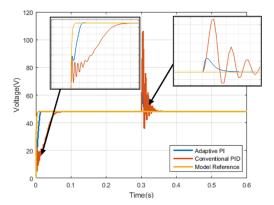


Figure 3. Output voltage rensponse of DC-DC boost converter with changing input voltage

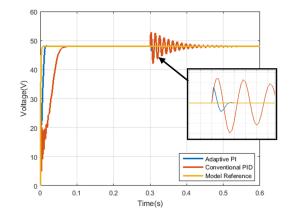


Figure 4. Output voltage rensponse of DC-DC boost converter with changing load

In second scenario, the load is changed to 300Ω at 0.3 s, a small oscillation occurs and finally follows the reference model. The transient response of the proposed controller shows it has fast response to achieve the voltage reference, the load change makes conventional PID requires more time. The voltage deviation in proposed controller by 6.25% for 0.01 s, this response is quite fast than conventional PID controller. The output voltage of the changing load is shown in Figure 4. However, the proposed controller has shorter oscilation compared to conventional PID controller, the voltage deviation in less than PID controller.

5. CONCLUSION

In this paper, DMRAC is chosen for controlling DC-DC boost converter, this method is satisfied for its controller structure and good performance in various input voltages and loads. The proposed system is stable and able to reach model reference perfectly with shorter recovery time. However, the response of the system has instantaneously overshoot and voltage deviation. The rise time, settling time, and overshoot for step response to follow the response of model reference are some objectives that determine whether the adaptation gains work properly. The adaptation gains determine the success of adaptive control. The adaptation gains of the proposed controller are obtained by empirical gains.

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