Multi-machine transient stability by using static synchronous series compensator

Nur Ashida Salim¹, Nur Diyana Shahirah Mohd Zain², Hasmaini Mohamad³, Zuhaila Mat Yasin⁴, Nur Fadilah Ab Aziz ⁵
¹,²,³,⁴ Faculty of Electrical Engineering, Universiti Teknologi MARA (UiTM), Selangor
⁵ Department of Electrical Power Engineering, Universiti Tenaga Nasional, Malaysia

ABSTRACT

Transient stability in power system is vital to be addressed due to large disturbances that could damage the system such as load changes and voltage increases. This paper presents a multi-machine transient stability using the Static Synchronous Series Compensator (SSSC). SSSC is a device that is connected in series with the power transmission line and produces controllable voltage which contribute to a better performance in the power system stability. As a result, this research has observed a comparison of the synchronization of a three-phase system during single-phase faults before and after installing the SSSC device. In addition, this research investigates the ability of three different types of controllers i.e. Proportional Integral (PI), Proportional Integral Derivation (PID), and Generic controllers to be added to the SSSC improve the transient stability as it cannot operate by itself. This is because the improvement is too small and not able to achieve the desired output. The task presented is to improve the synchronization of the system and time taken for the voltage to stabilize due to the fault. The simulation result shows that the SSSC with an additional controller can improve the stability of a multi-machine power system in a single phase fault.

Keywords:
Controller
FACTS device
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Corresponding Author:
Nur Diyana Shahirah Mohd Zain,
Faculty of Electrical Engineering,
Universiti Teknologi MARA (UiTM),
40450 Shah Alam, Selangor
Email: dieyanaz12@gmail.com

1. INTRODUCTION

Power system operation is the process of transmitting electrical energy based on required demand. Stability in a power system is the ability of the system to return to normal condition [1-3] which is the steady state condition within a minimum possible time after a transient fault or disturbance and it is dependent on the behaviour of the synchronous machines after a disturbance. Therefore, stability in a power system is one of the main problems in the power system operation [4] due to the huge demand for which there is a need to add the installation [5], connection, or generation of a large unit of voltage etc. When the system is unstable, a three-phase system will oscillate asynchronously between each other and affect the system. Stability disturbances are caused by load changing, short circuit between line and ground, line to line fault, three-phase fault, switching, etc [6]. Normally, the three-phase fault is the most common fault to occur as it causes maximum acceleration of the connected machines.
Transient stability is taken into consideration in this paper since it has the ability to maintain and synchronise the acceleration of a machine when it is subjected to transient disturbance. The transient stability of a multi-machine system [7] can be maintained and improved by installing a Flexible AC Transmission Systems (FACTS) device [8, 9]. FACTS is a device that is effective in controlling power flow, damping power system oscillations and improving the controllability and transfer capability of a power network. FACTS can also control the parameters and variables of a transmission line such as line impedance, terminal voltage, and voltage angle in effective ways since it is being increasingly used in power systems currently [10]. There are many FACTS devices available. SSSC (Static Synchronous Series Compensator) is a series FACTS device and one of the components that is proposed in this paper as a way to improve multi-machine transient stability [11]. An SSSC device will generate AC voltage and when connected in series to a power transmission line, it is able to control three parameters simultaneously such as line impedance, voltage, and phase angle. This paper only focuses on the effect of voltage if the disturbance occurs and the difference between three types of SSSC controllers which are PI, PID, and Generic controllers [12].

A simulation of stability in a power system has been carried out by using MATLAB Simulink [13]. Simulink is a software package that enables us to model, simulate, and analyse systems whose outputs change over time [14]. A Simulink system is configured in terms of a block diagram which is easy to build as it provides a drag-drop method to build a model in block diagram and the simulation results are displayed perfectly [8]. In this paper, a Simulink model is developed to improve the multi-machine system by using an SSSC device as it is widely used for research work and in the field of power systems [9].

2. CONCEPT OF STATIC SYNCHRONOUS SERIES COMPENSATOR

In order to test a power system’s stability, a two-machine system is connected with a Static Synchronous Series Compensator (SSSC). The SSSC consists of a voltage source converter and coupling transformer that will connect to a transmission line in series [15], [16]. The SSSC is used to control the active and reactive power in the transmission line. Based on Figure 1, the transmission line is connected to an AC system and the other side is a capacitor and battery. The battery is assumed as a DC source to allow active power as well as reactive power exchanges with the AC system. The exchanges of real and reactive power can be determined from voltage, $V_q$ with respect to the transmission line [17]. In Figure 1, $V_{dc}$ and $V_{out}$ are the DC voltage source and output voltage of the converter, respectively, $I_{line}$ is the line current while $I_{ac}$ and $I_{dc}$ are the AC current and DC current, respectively.

Because of the operating characteristic, it has the ability to control the steady state performance and play a more important role in reactive power and voltage [18]. In SSSC, when VSC is connected in series with the coupling transformer, it will produce controllable voltage ($V_q$) in quadrature with the line current to perform the function as a variable reactance compensator, either in capacitive or inductive mode. Meanwhile, VSC uses a forced commutated power electronic device to produce an AC voltage from a DC voltage source [12, 16, 18, 19]; SSSC uses additional controllers such as Proportional Integral (PI), Proportional Integral Derivative (PID), and Generic controllers to modulate the injected voltage of $V_q$ [20].

$$V_q = \Delta V_q + V_{qref}$$

- PI controller: proportional gain (Kp) and integral gain (Ki/s)
- PID controller: proportional gain (Kp), integral gain (Ki/s) and derivative gain (Kd)
- Generic controller: gain (Ks) and time constants (T1, T2, T3, T4)

Block diagram of PI, PID and Generic controllers as in Figure 2 to Figure 4 [21].

In Figure 2 to Figure 4, $V_{qref}$ represents the reference injected voltage as desired by the steady state power flow control loop. The steady state power flow loop acts quite slowly, hence by injecting the $V_{qref}$ it will be constant during the disturbance period [22]. The output of the block diagram will be supplied to the SSSC and the desired value of $V_{qref}$ is recorded to differentiate the stability time of the three types of controllers.
Multi-machine transient stability by using static synchronous series compensator (Nur Ashida Salim)

3. RESEARCH METHOD

3.1 Multi-machine model

The multi-machine model consists of two power generation substations and one load centre (dynamic load) at bus 4 as shown in Figure 5. The power generation G1 and G2 were set as 2100 MVA and 1400 MVA, respectively while the dynamic load as a major load was set as 2200MW. G1 is connected with L1 with 280 km and L2 is divided by two sides of 150 km to create a single-phase fault at the midpoint of the line. The generation G2 is connected with L3 of 50 km. After that, to improve the stability, the SSSC device will be located at bus B2 in series with L1 with three types of controllers i.e. PI, PID and Generic controllers [21] as shown in Figure 6.

3.2 Multi-machine transient stability

Firstly, design the circuit diagram by using MATLAB Simulink based on a single line diagram [16] which consists of power generation as a power source, transmission line, load and bus. After that, set all the parameters of the components that have been used to avoid errors. Choosing suitable parameter settings is important as the SSSC device is a phasor type. Then, run the simulation before and during the fault. If there is a fault, the system will become unstable between each phase. Therefore, the SSSC device will be
implemented to improve the system by using three types of controllers. Finally, record the time taken for the system to stabilise and compare between the three types of controllers. The overall procedure is shown in Figure 7.

Figure 5. Single line diagram of multi-machine power system without SSSC device

Figure 6. Single line diagram of multi-machine power system with SSSC device

Figure 7. Flowchart of a complete multi-machine transient stability system
3.3 Multi-machine system without SSSC

Figure 8 illustrates the Simulink model without any disturbance occurring in the system. The power generation block that acts as the power source was taken from Mathwork [23].

![Simulink model of multi-machine power system without SSSC device](image)

The waveform shown in Figure 9 shows that the system voltage is stable since each of the phases is synchronous with each other. Therefore, the FACTS device is not required during this condition.

![Voltage waveform of multi-machine power system without fault](image)

4. RESULTS AND ANALYSIS

System stability was tested when a single phase fault occurred in the middle of the transmission line between L2-1 and L2-2 and setting time to fault occurrence is at the early stage of initial system stabilisation.

4.1 Case 1: Fault at the Beginning of System

Figure 10 shows the model of a multi-machine system used in this research. The single phase fault is initiated at L2-2. The analysis of the stability will be explained in the following sub-section.

![Model of a multi-machine system used in this research](image)
4.1.1 Single phase fault of multi-machine model without SSSC device

During the occurrence of a single-phase fault, the fault affected the system at 0.2 s and the circuit breaker opened at 0.3 s at an early stage of fault as shown in Figure 11. After clearing the fault, Phase A reached 1 p.u at 0.708 s.

![Figure 11. Voltage waveform of single phase fault of multi-machine power system without SSSC](image)

4.1.2 Single phase fault of multi-machine model with SSSC device

An SSSC device is proposed in this research, therefore the system is improved by installing the SSSC device that is connected in series with the transmission line. The SSSC block diagram can be obtained from the Simulink library and set as a phasor type with a frequency of 60 Hz. The model is shown in Figure 12. At the beginning of the test, the SSSC device is tested without using any additional controller. As a result, Phase A took 0.6962 s to reach 1 p.u which is 0.012 s faster than without using SSSC. This is shown in Figure 13.

![Figure 12. Simulink model of single phase fault of multi-machine power system with SSSC](image)

![Figure 13. Voltage waveform of single phase fault of multi-machine power system with SSSC](image)
4.1.3 Single phase fault of multi-machine model with SSSC PI controller

A Proportional Integral (PI) controller is shown in Figure 14 which consists of proportional and integration gain blocks [24]. The input of the controller uses angular speed deviation, $dw$ from both machines.

![Figure 14. SSSC with PI controller](image)

After installing the SSSC with a PI controller, the system became stable at 0.814 s and each phase oscillated at the same value of voltage at 1 p.u as shown in Figure 15. Since the PI controller used angular speed deviation $dw$ as an input to the Vqref, therefore the damping of Vqref stabilised at 8 s as shown in Figure 16.

![Figure 15. Voltage waveform of single phase fault of multi-machine power system with SSSC PI controller](image)

![Figure 16. Vqref waveform of single phase fault of multi-machine power system with SSSC PI controller](image)

4.1.4 Single phase fault of multi-machine model with SSSC PID controller

The Proportional Integral Derivation (PID) controller contains proportional, integral and derivative gain blocks [25]. The PID controller also uses angular speed deviation, $dw$ as an input to the system as shown in Figure 17.
Figure 17. SSSC with PID controller

After the single-phase fault was cleared, the system became stable and synchronous with each other at 0.776 s as shown in Figure 18 and the damping of Vqref stabilised at 6 s which is much faster than the PI controller and is shown in Figure 19.

Figure 18. Voltage waveform of single phase fault of multi-machine power system with SSSC PID controller.

Figure 19. Vqref waveform of single phase fault of multi-machine power system with SSSC PID controller.

4.1.5 Single phase fault of multi-machine model with SSSC Generic controller

Figure 20 shows the block diagram of the generic controller. Based on the block diagram, the input is the same as the PI and PID controllers which use angular speed deviation, \( \Delta \omega \) as an input parameter. The block diagram consists of a washout that has a constant value (1-20 s) in order to pass the high oscillation signal to the next block and a two stage lead-lag block is used to adjust the phase lag between the outgoing and ingoing signals [13]. Here the value of parameters, \( T_\text{w} = 10 \), \( T_2 = T_4 = 0.3 \) are set as a constant and gain, \( T_1 \) and \( T_3 \) are selected by the trial and error method. For this paper, the suitable values were gain = 65.49, \( T_1 = 0.5527 \) and \( T_3 = 0.2563 \) [20].

Figure 20. SSSC generic controller
As a result, Figure 21 shows the system becoming stable at 0.7737 s and each phase oscillating in the same value of voltage which is 1 p.u. Since the angular speed deviation $w$ was also used as an input to the Vqref, the damping of Vqref was stable at 2 s as shown in Fig 22.

The performance result of the system stability test has been recorded and summarised as in Table 1. Firstly, the stability system was tested for with and without the SSSC device. As a FACTS device is capable of improving voltage stability and transient stability, one FACTS member, the SSSC device was installed in order to improve the stability and prevent unstable condition. Therefore, SSSC is connected in series with the transmission line and set as a phasor type. Since the purpose of an SSSC device is to improve transient stability, the value of Phase A increased to 0.83 p.u but did not reach 1 p.u like the other phases. As a result, the system is said to be unstable without using any additional SSSC controller.

Since the system was still not stable even though SSSC was installed, three types of controllers were added to the system i.e. proportional integral (PI), proportional integral derivative (PID) and Generic controllers. All three controllers used angular speed deviation, $w$ as an input and were injected to Vqref. From Table 1, all controllers have improved the system by oscillating all the phases at the same value after clearing the fault. The difference between voltage stability doesn’t make any difference but for Vqref, the Generic controller only took 2s to stabilise and was the fastest among the three controllers. As a result, the SSSC device needs an additional controller to improve the power system stability.

5. CONCLUSION

The study of the multi machine transient stability has been proven by using Matlab Simulink. The operation of the multi machine transient stability required two power generation with a huge power value to supply to the dynamic load as a major load. The system operation will run smoothly if no disturbance occurs. This research was able to observe the multi machine stability when subjected to a single phase fault. The SSSC device has proven its ability to improve the power system stability in the system. However, SSSC cannot operate by itself without using any additional controller. Three types of controllers have been modelled to control the SSSC device. They are the Proportional Integral (PI), Proportional Integral Derivative (PID), and Generic controllers. As a conclusion, SSSC with Generic controller is very suitable because of its shorter stability time and less damping as the Generic controller is suitable to use with any FACTS device.

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