Utilization Cat Swarm Optimization Algorithm for Selected Harmonic Elimination in Current Source Inverter

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ABSTRACT
The voltage source inverter (VSI) and Current source inverter (CSI) are two types of traditional power inverter topologies. In this paper selective harmonic elimination (SHE) Algorithm was implemented to CSI and results has been investigated. Cat swarm (CSO) optimization is a new meta-heuristic algorithm which has been used in order to tuning switching parameters in optimized value. Objective function is reduction of total harmonic distortion (THD) in inverters output currents. All of simulation has been carried out in Matlab/Software.

Keywords:
Current source inverter
Meta-heuristic algorithm
Switching parameters tuning
Total harmonic distortion

1. INTRODUCTION
There are two types of traditional power inverter topologies, Voltage source inverter (VSI) and Current source inverter (CSI). The voltage source inverter produces a specified three-phase PWM voltage waveform for the load while the current source inverter outputs a specified three-phase PWM current waveform. Figure 1 show the CSI. The CSI is able to inject power to the grid without any additional dc/dc converter, but low harmonic is major problem in this kind of inverter and reduction of these harmonic is object of several researchers work. In additional these harmonics limited applications of CSI in frequency domain. CSI derives in the megawatt range are widely used in the industry[1],[2]. The conventional three phase current source inverter has the defect of introducing increased lower order harmonics in the output current which give rise in to losses and torque pulsation in the machine. Elimination of low order harmonic is so important in CSIs to avoid possible resonance between the input/output filter capacitance and input/output circuit inductance.

In this paper current source inverter has been investigated and Selected Harmonic Elimination (SHE) switching algorithms has been implemented. Several types of switching introduced in literature are introduced like: Trapezoidal PWM switching, Selective Harmonic Elimination (SHE) and Space vector Modulation (SVM) [3].
Selective harmonic elimination (SHE) is a method to eliminate a number of low-order harmonic in the inverter PWM current. It is a well-known method for generating PWM signals that can eliminate low order harmonic in specified voltage or current waveform [4]. One of easy way to have qualified output waveform is that eliminate some low harmonic if number of pulses per half cycle is $N_p$, the number of angles that implemented to harmonic elimination is achived (1):

$$j = \frac{N_p - 1}{2}$$

(1)

For example with five pulses per half cycle, there are two independent angles $\Theta_1$ and $\Theta_2$, the two switching angles provide two degrees of freedom which can be used to either eliminate two harmonic in current or voltage waveform. In this paper current source inverter has been utilized [5]. The inverter PWM current can generally be expressed (2):

$$i(t) = \sum_{n=1}^{\infty} a_n \sin(nwt)$$

(2)

$$a_n = \frac{4}{\pi} \int_0^{\pi/2} i(t) \sin(nwt) \, dt$$

The Fourier coefficient $a_n$ can be found from (3)

$$a_n = \frac{4 \times I_{dc}}{n \pi} \begin{cases} 
\cos(n\Theta_1) + \cos(n\frac{\pi}{3} - \Theta_1) - \cos(n\Theta_2) - \cos(n\frac{\pi}{3} - \Theta_2) + ... \\
+ \cos(n\Theta_1) + \cos(n\frac{\pi}{3} - \Theta_1) - \cos(n\frac{\pi}{6}), j = odd \\
\cos(n\Theta_1) + \cos(n\frac{\pi}{3} - \Theta_1) - \cos(n\Theta_2) - \cos(n\frac{\pi}{3} - \Theta_2) + ... \\
- \cos(n\Theta_1) - \cos(n\frac{\pi}{3} - \Theta_1) + \cos(n\frac{\pi}{6}), j = even
\end{cases}$$

(3)

To eliminate $j$ harmonics, $j$ equations can be formulated by setting $a_n = 0$. 

Utilization Cat Swarm Optimization (CSO) Algorithm for Selected Harmonic .... (Hamed Hosseinnia)
2. CAT SWARM OPTIMIZATION (CSO) ALGORITHM

Cat swarm algorithm (CSO) is based on the behavior of cats with exceptionally vigorous vitality of curiosity toward moving objects and possessing good hunting skills. Chu and Tsai proposed a new optimization algorithm that imitates the natural behavior of cats. Even though cats spend most of their time resting, they always remain alert and move very slowly. These two characteristics of resting with slow movement and chasing with high speed represented by seeking and tracing, respectively. In CSO, these two modes of operations are mathematically modeled for solving complex optimization problems. These modes are termed the "seeking and tracing" modes. A combination of these two modes allows CSO has better performance.

Seeking mode has four essential factors: seeking memory pool (SMP), seeking range of the selected dimension (SRD), counts of dimension to change (CDC), and the self position consideration (SPC). Once a cat goes into tracing mode, it moves according to its own velocities for every dimensions. Every cat has its own position composed of D dimension, velocities for each dimension, a fitness value representing the accommodation of the cat to the benchmark function and a flag to identify whether the cat is in seeking mode or tracing mode. These two modes are dedicated to join with each other by mixture ratio (MR). The final position would be the best position of one of the cats [6].

The computational procedure of the proposed optimization algorithm in the form of flowchart is shown in Figure 2. Which is now described in detail:

I. Randomly initialize the initial set of cats of size $N_{pop}$, where each cat is of dimension $D$, $X_i = \{x_{i1}, x_{i2}, \ldots, x_{id}\}$

II. Initialize the velocity of each cat, i.e., the velocity of cat I in the D-dimentional space as $V_i = \{V_{i1}, V_{i2}, \ldots, V_{id}\}$.

III. Evaluate the fitness of each cat and keep the position of the cat that has the highest fitness value.

IV. According to parameter mixing ratio (MR), cats are randomly distributed to seeking and tracing modes.

V. If cat $k$ is in seeking mode then
   a) Create SMP-1 copies of the kth cat and retain the present position as one copy;
   b) For each copy according to CDC, randomly select the dimension to be mutated.
   c) For the dimension selected for each copy, randomly add or subtract the SRD percent of the present value;
   d) Calculate the fitness value of all copies and replace original cat $k$ with the copy having best fitness value.

VI. If cat $k$ is in tracing mode, then
   a) Update the velocity for every dimension of the kth cat:

$$v_{k,j}^{t+1} = w \times v_{k,j}^t + r \times (GB_j - x_{k,j}^t);$$

b) Check if the velocities are in the range of maximum velocity; in case the new velocity is over range, set it equal to the maximum limit;
   c) Update the position for every dimension of the kth cat:

$$x_{k,j}^{t+1} = x_{k,j}^t + v_{k,j}^{t+1};$$

d) Constrain the position of the cat so that it doesn’t exceed the limits of interest;
   e) Evaluate the fitness of each cat and store the position of the cat that has the best fitness value and compare the previous global best value with the current best value accordingly;
   f) Check if the maximum pre-specified number of iterations is reached, which is used as the termination criterion, if YES terminate the program, else go to step.

Because the importance of minimizing THD value, the objective function is defined as below (4) [7]:

$$M = \int_0^{t_{max}} t \cdot |THD| \, dt$$  (4)
START

Create N Cats

Initialize the position, velocities and the flag of every cat

Evaluate the cats according to the fitness function and keep the position of the cat, which has the best fitness value

Cat K is in the seeking mode?

Apply Cat K into tracing mode process

Apply Cat K into Seeking mode Process

Re-Peek Number of Cats and set them into tracing mode according to MR, and set the others into seeking mode

END

Figure 2. Cat Swarm Optimization (CSO) Flowchart

3. Tuning Simulation Parameters

In this paper CSO algorithm implement to tuning switching parameters to minimize objective function. Objective function (OF) is minimization of total harmonic distortion (THD). For example for reduction THD in waveform with \( N_p = 5 \), and to eliminate 5th and 7th harmonics, the following two equation can be drive (5):

\[
\begin{align*}
\cos(5\Theta_1) + \cos\left(5\left(\frac{\pi}{3} - \Theta_1\right)\right) & - \cos(5\Theta_2) - \cos\left(5\left(\frac{\pi}{3} - \Theta_2\right)\right) - \frac{\sqrt{3}}{2} = 0 \\
\cos(7\Theta_1) + \cos\left(7\left(\frac{\pi}{3} - \Theta_1\right)\right) & - \cos(7\Theta_2) - \cos\left(7\left(\frac{\pi}{3} - \Theta_2\right)\right) - \frac{\sqrt{3}}{2} = 0
\end{align*}
\] (5)

In SHE algorithm minimized value for THD is obtained with tuning angles in best value. In Figure 3 number of signals for eliminating two harmonic and its manner has been shown. In Figure 4 proposed way to generating pulses for switches has been illustrated [8],[9].
3.1. Simulation Results
Simulation was conducted with the configuration shown in Figure 5. The simulation parameters are: \( I_{dc} = 6A, C = 40\mu F, L = 10mH, R = 10\Omega \). The simulation results with CSO are shown in Figure 6. This Figure included of Line to Line voltage and Load Current and output current of Inverter \( (I_w) \). In additional FFT analysis of \( I_w \) has been shown in Figure 7. This figure proving CSO algorithm operation in 5th and 7th harmonic elimination with SHE switching method [10],[11]. For additional example to prove CSO algorithm operation this method done in three harmonic elimination \( (5^{th}, 7^{th}, 11^{th}) \) and results have been illustrated in Figure 8 and Figure 9.
Figure 5. Simulated circuit

Figure 6. Simulation Results for Two Harmonic Elimination (5th, 7th), Load Current (Is), Inverter output Current (Iw), Line to Line Voltage (V)

Figure 7. FFT analysis of (Iw)
Figure 8. Simulation Results for Three Harmonic Elimination (5th, 7th, 11th). A. Inverter output Current ($I_w$) b. Load Current ($I_s$) C. Line to Line Voltage ($V$)

Figure 9. FFT analysis of ($I_w$)

Results that has been achieved for eliminate different harmonics by implementation CSO with SHE algorithm, be showed. These angles are optimized value that has been achieved with CSO algorithm.
Table 1. Optimized Value for switching angle in SHE Algorithm

<table>
<thead>
<tr>
<th>Harmonic to be Eliminated</th>
<th>$\theta_1$</th>
<th>$\theta_2$</th>
<th>$\theta_3$</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>17.15</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>7</td>
<td>20.56</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>11</td>
<td>23.75</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>13</td>
<td>25.6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>5, 7</td>
<td>7.85</td>
<td>14.2</td>
<td>-</td>
</tr>
<tr>
<td>5, 11</td>
<td>13</td>
<td>19.5</td>
<td>-</td>
</tr>
<tr>
<td>5, 13</td>
<td>14.75</td>
<td>22</td>
<td>-</td>
</tr>
<tr>
<td>7, 11</td>
<td>15.21</td>
<td>19.45</td>
<td>-</td>
</tr>
<tr>
<td>7, 13</td>
<td>16.23</td>
<td>21</td>
<td>-</td>
</tr>
<tr>
<td>5, 7, 11</td>
<td>2.42</td>
<td>5.8</td>
<td>21.43</td>
</tr>
<tr>
<td>7, 11, 13</td>
<td>9.6</td>
<td>11.72</td>
<td>24.13</td>
</tr>
<tr>
<td>5, 11, 13</td>
<td>7.62</td>
<td>10.82</td>
<td>23.4</td>
</tr>
</tbody>
</table>

4. CONCLUSION

As simulation result showed, by tuning selected angles in optimized value, THD of the output current is better than arbitrarily chosen values and quality of CSI output is improved. This verifies the effectiveness of the Selected Harmonic Elimination (SHE) with Cat Swarm Optimization (CSO). CSO can be applied to any problem where optimization is required. Therefore, it can be applied in many usage in power electronic and power marketing. The comparison of the results in this paper with similar work in other literature show CSO algorithm befit for optimization issues [12]-[15].

REFERENCES

BIOGRAPHIES OF AUTHORS

Hamed Hosseinnia was born in Khoy, Iran in June 1988. He received his B.Sc degree in Azarbayjan Shahid Madani University, Tabriz, Iran and M.Sc degrees (with honor) in Urmia University, both in Electrical Engineering in 2010 and 2013 respectively. He is currently a Ph.D. student in Electrical Engineering at Urmia University with a focus on Optimization and Power System. His interests include FACTS, Power Electronic, Microgrid Operation and Planning, and Advanced Power System.

Murtaza Farsadi was born in Khoy, Iran in September 1957. He received his B.Sc. degree in Electrical Engineering, M.Sc. degree in Electrical and Electronics Engineering, and Ph.D. degree in Electrical Engineering (High Voltage) from Middle East Technical University (METU), Ankara, Turkey in 1982, 1984, and 1989, respectively. He is now an assistant professor in the Electrical Engineering Department of Urmia University, Urmia, Iran. His main research interests include high voltage engineering, industrial power system studies, FACTS, HVDC transmission systems, DC/AC active power filters, renewable energy, hybrid and electrical Vehicles, and new control methods.