

Design of PI and Fuzzy Logic Controllers for Distribution Static Compensator

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ABSTRACT

This paper presents study of distribution static compensator (D-STATCOM) for compensation of reactive power, harmonic distortion mitigation and load balancing in three phase three wire nonlinear load distribution system. The proposed control algorithm is developed based on synchronous reference frame theory using PI and FUZZY logic controller. The obtained reference current signal from control algorithm is compared in hysteresis band current controller for better switching of D-STATCOM. The performance of DSTATCOM with PI and fuzzy logic controller is also analysed and compared for DC voltage regulation and harmonic distortion mitigation. The proposed method is provided effective compensation for reactive power, harmonic distortion mitigation and load voltage balancing. The simulation results are obtained using MATLAB/SIMULINK soft ware.

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1. INTRODUCTION

In last one decade, power electronic devices are widely used in industrial applications for transferring power in more efficient way [1]. The excessive use of power electronic equipments, which represent nonlinear loads, in a distribution network has caused many disturbances in the quality of power such as harmonic pollutions, unbalanced load currents, and reactive power problems [2]. As a result poor power factor, weakening efficiency, overheating of motors and transformers, malfunction of sensitive devices etc [3-4]. Next power quality problem is unbalancing the source voltage due to unbalanced load. The unbalanced source voltage may generate the lower order harmonic component in the power system and also cause a negative sequence current and torque reduction in case of electric machine drive system[5-8] These power quality problems also appear due to plenty of industrial products and equipments containing nonlinear loads [9]. These power quality problems are compensated using custom power devices such as distribution static compensators (DSTATCOM), dynamic voltage restorer (DVR) and unified power quality conditioner (UPQC) [10]. In which the distribution static compensator is more suitable for reactive power compensation, harmonic mitigation, load unbalancing voltage variations and current harmonics in the distribution network compared to other custom power devices[11]. The SRF control scheme need PI controller for regulating DC-link voltage. The PI controller need precise mathematical model values which will be tough to get and it may not give suitable results under parameter variation, load variations etc [12-14]. The implementation of fuzzy logic controller for DSTATCOM has a great deal of attention. The advantages of fuzzy logic controller over

PI controller are it doesn't require accurate mathematical model values and it can handle any non linearity with inaccurate inputs values. Mostly mamdani type fuzzy logic controller is used and it gives better results for SRF controlled DSTATCOM. The fuzzy logic controller gives better results for DSTATCOM application while comparing with PI controller [15, 16].

In this paper SRF controlled DSTATCOM is proposed and implemented on 11/0.4kv distribution system. The performance of the proposed DSTATCOM is analyzed under nonlinear load conditions in terms of harmonic mitigation and DC link voltage regulations. The objectives of the this paper are

- Extracting the reference current control signal, using synchronous reference frame theory to compensate current harmonic at the PCC produced by nonlinear load.
- Also Compensate source current harmonic and regulating the load voltage
- Regulating the dc link voltage of voltage source inverter using with PI and Fuzzy logic controller and comparing the performance of DSTATCOM with separately using PI and Fuzzy logic controller.
- Reactive power compensation and maintaining the constant voltage at PCC, under nonlinear load conditions.
- Power factor correction.

2. SYSTEM CONFIGURATION

Figure 1 shows the system configuration of proposed Distribution static compensator (D-STATCOM) for the three phase three wire distribution system with nonlinear load. Three phase source voltage of 11kv is stepped down to 400V by using 11/0.4Kv three phase transformers and connected to three phase nonlinear load. The connected nonlinear load introduces harmonic at the PCC. By Connecting DSTATCOM at the PCC harmonics elimination, load balancing with power factor correction and line voltage regulation at the bus are compensated. The DSTATCOM consist of three leg voltage source inverter using six IGBTs switches with interface inductor (L_f), dc link capacitor. The switching transients during DSTATCOM switching can be controlled by connecting a three phase capacitor ripple filter (C_f)

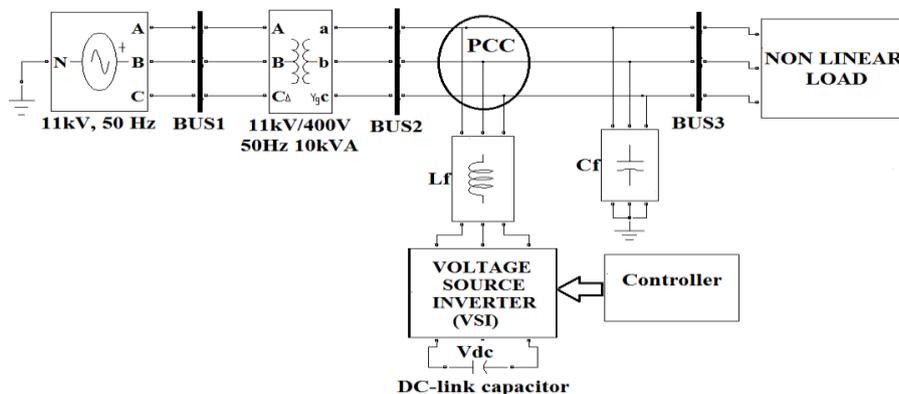


Figure 1. The system configuration

The modulated control switching signals to the VSI DSTATCOM is obtained from the synchronous reference control (SRF) strategy with hysteresis band control.

3. SYSTEM CONFIGURATION

The block diagram of the proposed SRF control algorithm is shown in Figure 2. The control algorithm is used for extracting the fundamental reference control signals to switching of VSI based DSTATCOM for harmonic and reactive power compensation during balanced nonlinear load condition.

The non linear load currents consist of active, reactive and harmonic currents in three phase system. In this, the reactive and harmonic components of currents are separated, for compensation. The separation includes converting the instantaneous three phase load currents into two phase stationary α - β -0 axis using Clark's transformation equation.

Similarly for regulating the voltage at the PCC, the source must need to deliver the reactive current (i_{qr}), to be added to the average reactive reference component of current ($i_{q\ dc}$) of q-axis in d-q frame same as direct current axis component. Then the resultant reactive reference current component is

$$i_{Lq}^* = i_{q\ dc} + i_{qr} \quad (8)$$

The reactive current (i_{qr}) is obtained from the PI controller output, the input to the PI controller is obtained by subtracting the voltage amplitude V_s from the reference voltage V_s^* and is fed to PI controller. Here the amplitude of the PCC voltage is given as

$$V_s = \sqrt{\frac{2}{3} (V_{sa}^2 + V_{sb}^2 + V_{sc}^2)} \quad (9)$$

The PI controller output is given as

$$V_{qr}(n) = V_{qr}(n-1) + K_{pq}(V_{te}(n) - V_{te}(n-1)) + K_{iq}V_{te}(n) \quad (10)$$

Where $V_{te}(n) = V_s^* - V_s(n)$ is the error between the reference (V_s^*) and actual (V_s) terminal voltage amplitude at the n th sampling instant. Here K_{pq} and K_{iq} are the proportional and the integral gains of the PI controller. The reactive reference current component (i_{Lq}^*) is used for ac voltage regulation and compensation of load reactive power. The Active and reactive reference current component (i_{Ld}^* , i_{Lq}^*) are transformed in to α - β -0 frame obtained by using inverse park's Equation (11)

$$\begin{bmatrix} i_{s0}^* \\ i_{s\alpha}^* \\ i_{s\beta}^* \end{bmatrix} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & \cos \theta & -\sin \theta \\ 0 & \sin \theta & \cos \theta \end{bmatrix} \begin{bmatrix} i_{L0}^* \\ i_{Ld}^* \\ i_{Lq}^* \end{bmatrix} \quad (11)$$

The reference currents from the Inverse Park's are transformed into three phase reference current (a-b-c) by using inverse Clark's Equation (11).

$$\begin{bmatrix} i_{sa}^* \\ i_{sb}^* \\ i_{sc}^* \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 0 & 1 & 0 \\ 0 & -1/2 & \sqrt{3}/2 \\ 0 & -1/2 & -\sqrt{3}/2 \end{bmatrix} \begin{bmatrix} i_{s0}^* \\ i_{s\alpha}^* \\ i_{s\beta}^* \end{bmatrix} \quad (12)$$

The obtained three phase reference currents (i_{sa}^* , i_{sb}^* , i_{sc}^*) are compared with the actual compensating filter currents in hysteresis band controller for better switching of VSI of IGBT's. The main advantage of this hysteresis band controller is easy implementation, better stability, faster response as compared to the other controller like carrier based, beat beat and feed forward etc [17]. The main disadvantage of the carrier based controller is variable switching frequency which may causes stress on the switching devices as compared to the hysteresis band controller

4. DC LINK VOLTAGE REGULATION

The quality and performance of DSTATCOM depends upon the generation of the compensating reference current, In which dc link voltage regulation is the key factor. According to requirement of compensating current, the dc-link voltage (V_{dc}) is either increases or decreases. For proper operation of VSI the dc side of the inverter should kept constant at a particular reference value. The filter and switching power losses of VSI overcome by maintaining constant dc-link voltage. In order to regulating or maintaining dc-link voltage constant a controller is needed to add the average active current component ($i_{d\ dc}$) of d-axis in rotating frame theory. Here the following two controllers have been added and compared.

- a. PI controller
- b. Fuzzy logic controller

4.1. PI Controller

Figure 3 shows the PI controller block diagram. It is the internal structure of the control circuit. The source must need to deliver the loss reference component of current (i_{LOSS}) along with active reference current component (i_{Ld}), for the supplying of losses in the DSTATCOM and filter. The loss reference

component of current (i_{LOSS}) is extracted, by comparing the reference dc bus voltage V_{dc}^* with the actual dc bus voltage V_{dc} of VSI at the n^{th} sampling instant.

$$V_{de}(n) = V_{dc}^*(n) - V_{dc}(n) \tag{13}$$

The compared error signal $V_{de}(n)$ is processed through a PI controller to compute the loss component (i_{LOSS}) at n^{th} sampling instant is expressed as

$$i_{Loss}(n) = i_{loss}(n-1) + k_{pd} (V_{de}(n) - V_{de}(n-1)) + k_{id} V_{de}(n) \tag{14}$$

Here the k_{pd} and k_{id} are the proportional and integral gains of PI controller. The values given $k_{pd} = 0.6$, $k_{id} = 0.007$. the output of the PI controller is the loss reference component (i_{LOSS}) of the DSTSTCOM. The loss reference current component (i_{loss}) is added to the average active reference component (i_{Ld}) for regulating the active reference component of current (i_{Ld}^*). The estimated reference current component is compared with the actual compensating filter currents in hysteresis band controller for better switching of VSI of IGBT's

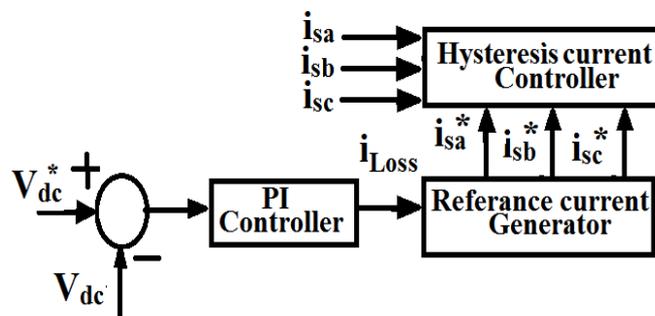


Figure 3. Block diagram of PI controller

4.2. Fuzzy logic controller

Figure 4(a) shows the proposed internal structure of Fuzzy logic controller and the Figure 4(b) shows the proposed control block diagram of the fuzzy logic controller. The actual dc link capacitor voltage is compared with the reference dc voltage. The error signal is processed through the fuzzy logic controller, reference.

For supplying the active power to compensate the losses in VSI and maintain the dc-link voltage constant, the fuzzy logic controller output is added to the fundamental active current component and the resultant compensating current are compared with the actual sensed compensating filter current in hysteresis band, which gives error signal. The error signal decides the operation of the VSI. FIS consists of Fuzzy Inference System (FIS) Editor, Membership Function Editor, Rule Editor, and Rule Viewer, Surface Viewer and defuzzification [18 -21].

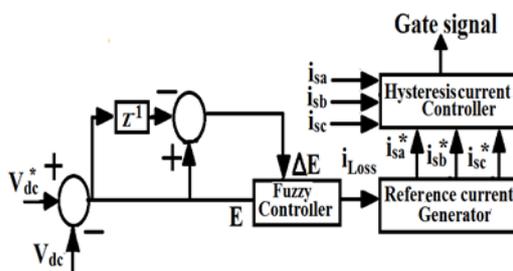


Figure 4(a). Block diagram of Fuzzy logic controller

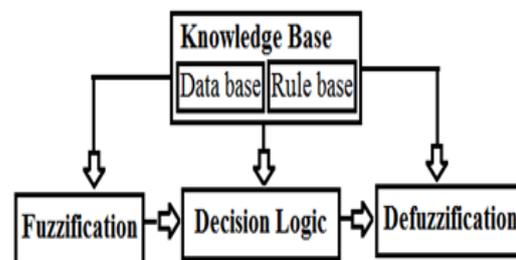


Figure 4(b).The proposed control block diagram

In this paper the fuzzy interface system we have designed as

1. No of inputs and outputs (2 inputs and 1 output).
2. No of membership functions (seven).

3. Type of implication (Mamdani max–min operation).
4. Type of defuzzification method (centroid of area method).
5. No. of rules (49 rules).
6. Input membership function (Gaussian)
7. Input membership function (triangular)

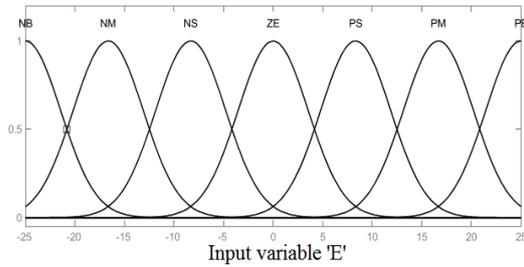


Figure 4(c). Gaussian membership function for input variable

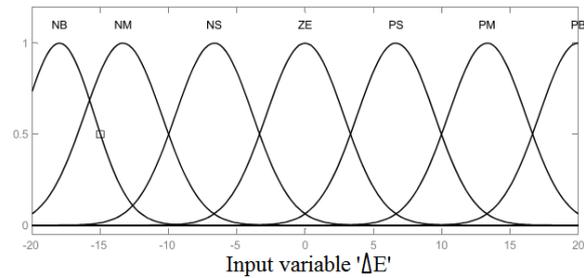


Figure-4(d). Gaussian membership function for change in input variable

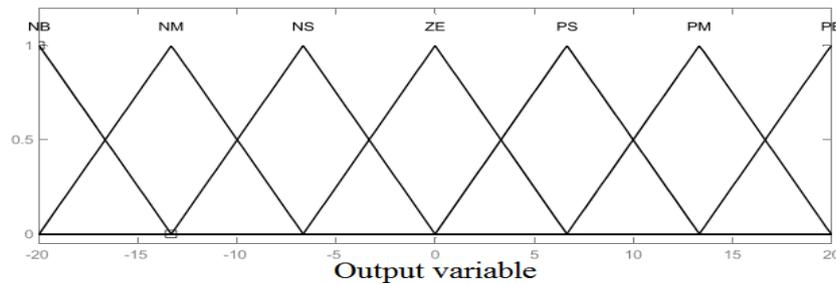


Figure 4(e). Triangular output variable membership function

The decision rule table relating to the input to the output fuzzy sets as shown in Table-I.

Table 1. Decision rule table

E / ΔE	NB	NM	NS	ZE	PS	PM	PB
NB	NB	NB	NB	NB	NM	NS	ZE
NM	NB	NB	NB	NM	NS	ZE	PS
NS	NB	NB	NM	NS	ZE	PS	PM
ZE	NB	NM	NS	ZE	PS	PM	PB
PS	NM	NS	ZE	PS	PM	PB	PB
PM	NS	ZE	PS	PM	PB	PB	PB
PB	ZE	PS	PM	PB	PB	PB	PB

For the N linguistic variables for each of the input and output there are N² possible combinations resulting into any of M values for the decision variables. All the possible combination is called states. The resulting fuzzy matrix are (N² *M) =49. All this 49 states are control variables of membership functions.

In defuzzification the fuzzy set output produced according to decision logic (linguistic variables) is transformed into crisp values of output. For this defuzzification ‘centroid’ method is used. The crisp output value expressed in ‘centroid’ is given as

$$u = \frac{\sum_{q=1}^7 \mu_{\Delta\mu q} X_{Gq}}{\sum_{q=1}^7 \mu_{\Delta\mu q}} \tag{15}$$

Where X_{Gq} is the centre of membership function, $\mu_{\Delta i q}$ is the max of all rules that give the same decision from the both the controller the fuzzy logic controller provided the better response and improve the behaviour of DSTATCOM.

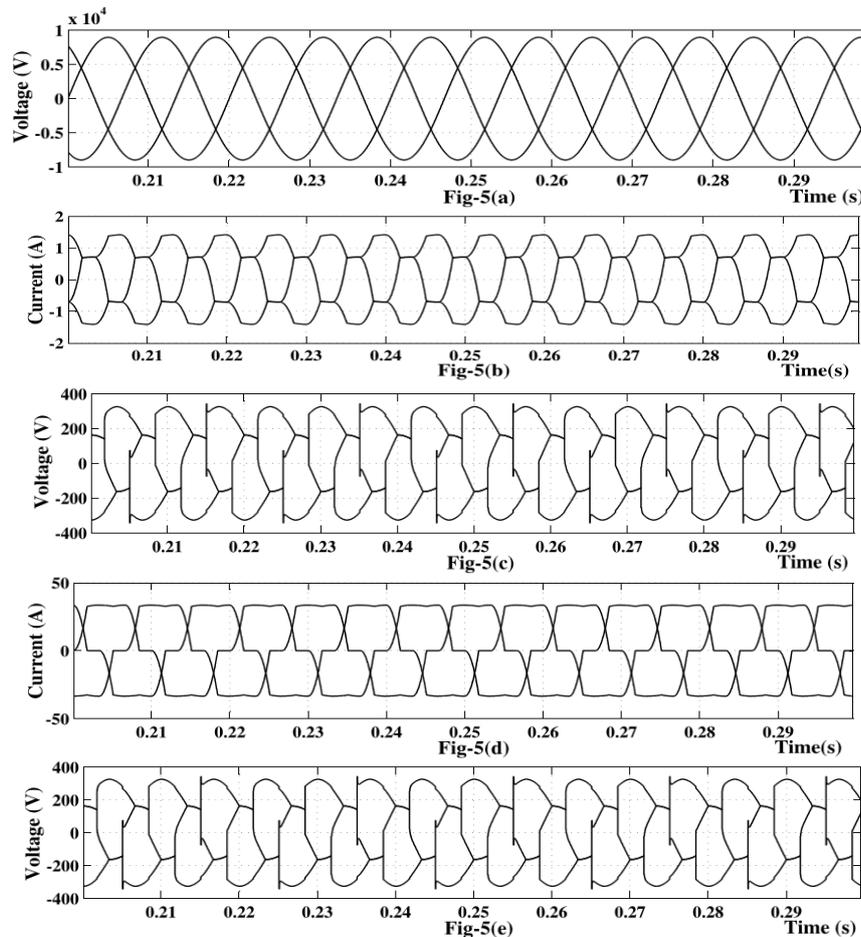
5. RESULTS AND DISCUSSION

The proposed DSTATCOM model is implemented in MATLAB/SIMULINK software using synchronous reference frame control algorithm with PI and FUZZY logic controller. The model is verified under nonlinear load condition. The proposed model is used for harmonic compensation, power factor correction and voltage regulation at the PCC. The simulation results with PI and FUZZY logic controller is also compared. The simulation period is taken from 0.2 s to 0.3s for the better observation. The performance of DSTATCOM is observed for the following cases

- Non Linear load without DSTATCOM
- DSTATCOM with PI Controller
- DSTATCOM with Fuzzy logic controller
- THD analysis
- Comparison of DC-link voltage (PI &Fuzzy logic controller).

5.1. Non Linear load without DSTATCOM

The When nonlinear load is connected to the proposed distribution system causes, harmonic in source current (Bus-1) as shown in Figure- 5(b). The nonlinear load also introduces harmonics in the voltage and current wave form at the PCC (bus-2).is shown in Figure-5(c) & 5(d).The harmonics effects the loads connected the PCC. Figure-5(e) &5(f) shows the load voltage and load current. The per phase representation of the current at the PCC is shown in Figure-5(g).



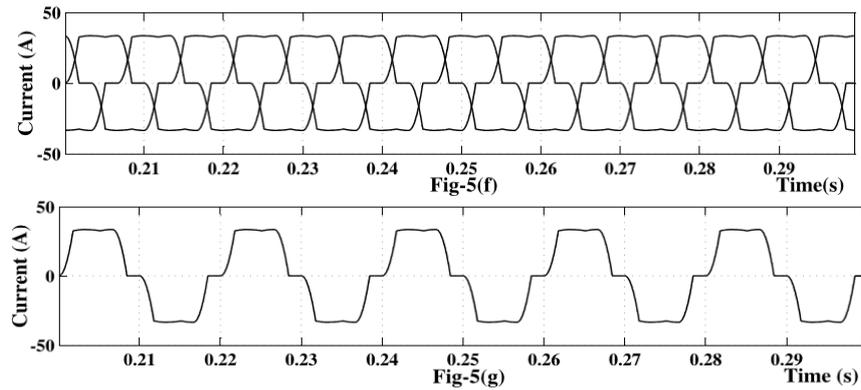
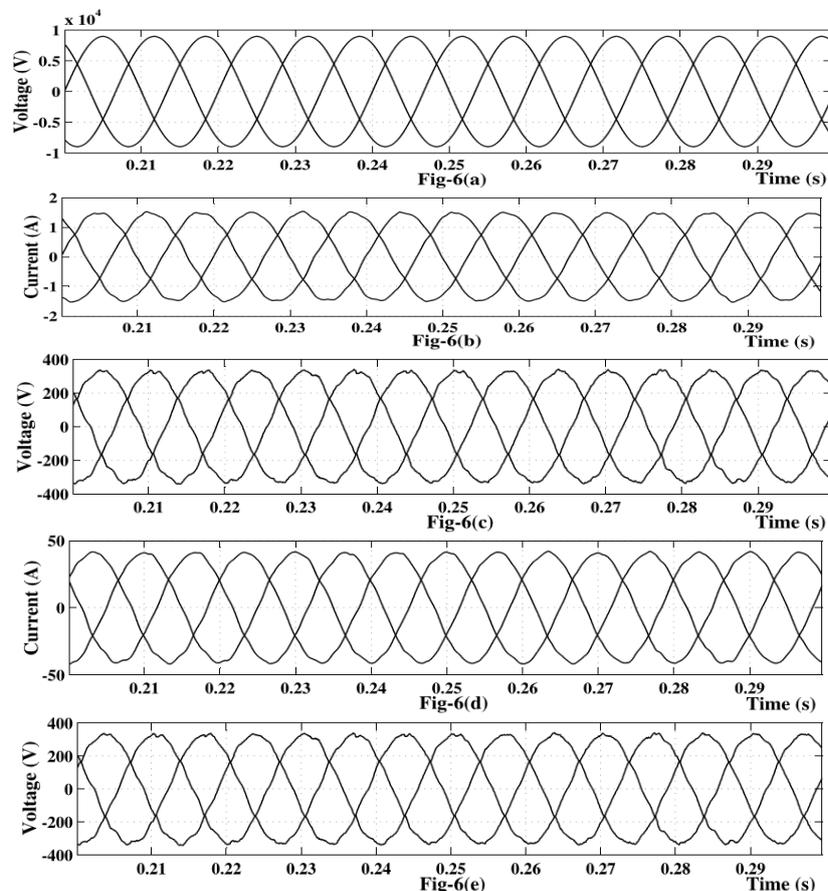


Figure 5 (a)Source voltage wave form, (b). Source current wave form, (c).voltage waveform at the PCC (Bus-2), (d). Current waveform at the PCC (Bus-2), (e).Load voltage wave form, (f).Load current wave form, (g). Per phase representation of PCC current wave form, under nonlinear load without D-STATCOM

5.2 D-STATCOM with PI Controller

When SRF controlled D-STATCOM is connected at the PCC, the D-STATCOM is injecting the required amount of reactive power for compensation of current and voltage harmonics at the PCC and it also compensating the source current harmonics. The DC-link voltage is regulating with PI controller. The compensated source and PCC current waveforms as shown in Figure-6(b) & 6(c). Using R-C filter load and PCC Voltage is compensated. Figure-6(d) shows the compensated and regulated voltage waveform at the PCC. Load voltage is also compensated as shown in Figure-6(e). Figure 6(f) shows the load current waveform. The DC-link voltage, per phase representation of the current wave form wave form as shown in Figure-6(g) & 6(h).



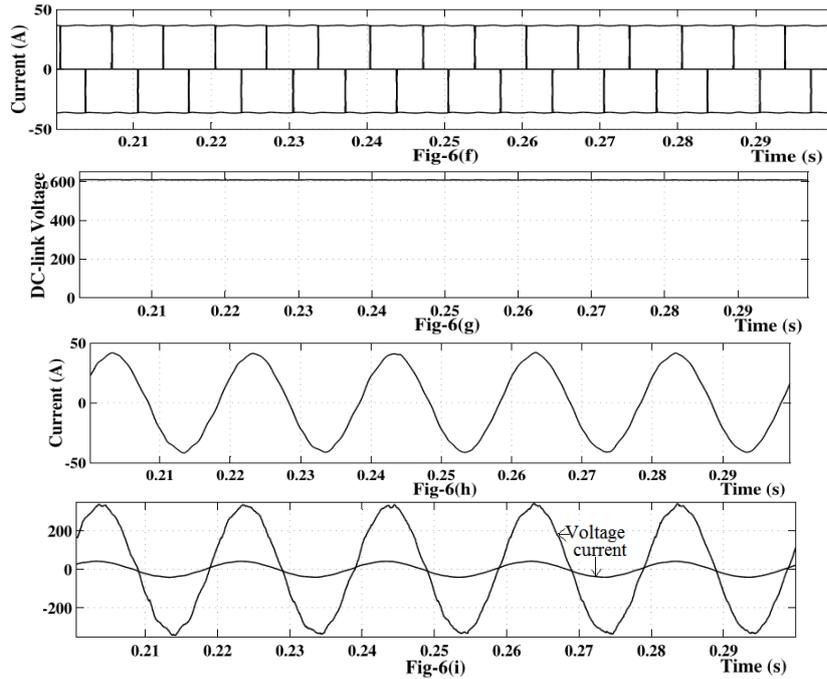
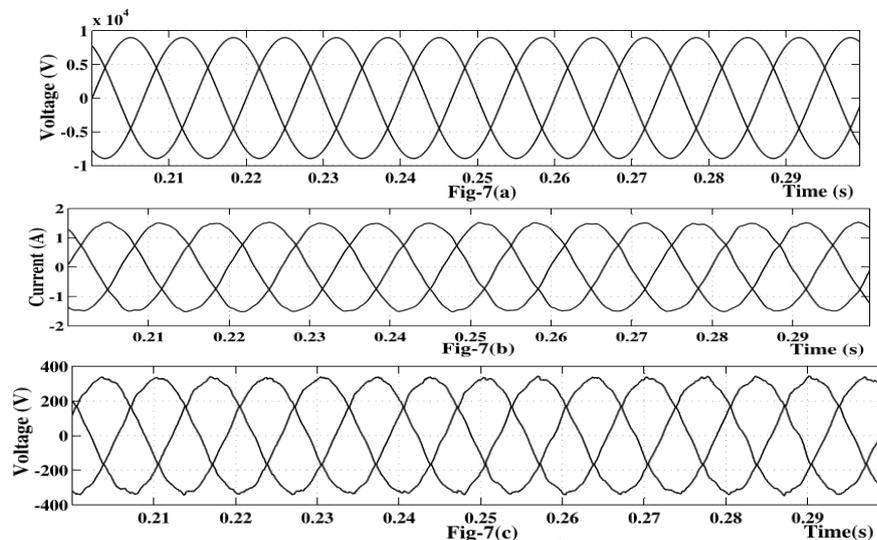


Figure 6 (a)Source voltage wave form, (b). Source current wave form, (c).voltage waveform at the PCC (Bus-2), (d). Current waveform at the PCC (Bus-2), (e).Load voltage wave form, (f).Load current wave form, (g).DC-link voltage form, (h). Per phase representation of current wave form at the PCC, (i). Voltage and current wave form, under nonlinear load with PI controlled D-STATCOM

5.3 D-STATCOM with Fuzzy logic controller

The SRF controlled D-STATCOM is connected at the PCC, the D-STATCOM is injecting the required amount of reactive power for compensation of current and voltage harmonics at the PCC and it also compensating the source current harmonics. The DC-link voltage is regulating with fuzzy logic controller. By comparing with PI controller the fuzzy logic controller provided better harmonic and reactive power compensation and its corresponding waveforms as shown in Figure-9.The compensated source and PCC current waveforms as shown in Figure-7(b) & 7(c). Using R-C filter load and PCC Voltage is compensated. Figure-7(d) shows the compensated and regulated voltage waveform at the PCC. Load voltage is also compensated as shown in Figure-7(e). Figure 7(f) shows the load current waveform. The DC-link voltage, per phase representation of the current wave form wave form as shown in Figure-7(g) & 7(h).



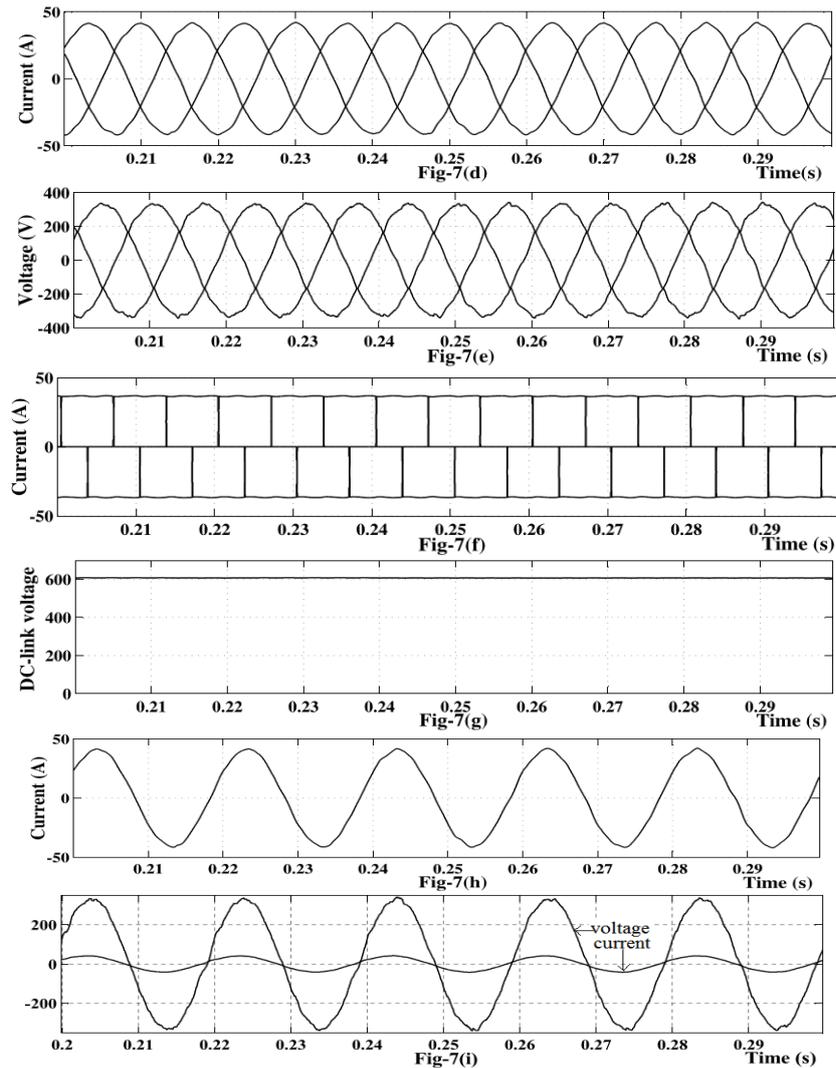


Figure 7 (a) Source voltage wave form, (b). Source current wave form, (c). voltage waveform at the PCC (Bus-2), (d). Current waveform at the PCC (Bus-2), (e). Load voltage wave form, (f). Load current wave form, (g). DC-link voltage form, (h). Single phase representation of current wave form at the PCC, (i). Voltage and current wave form, with Fuzzy logic controlled D-STATCOM

5.4 Total Harmonic Distortion analysis

The total harmonic distortion at the source PCC and PCC for the proposed system with and without DSTATCOM is discussed here. And also the analysis is done with PI and Fuzzy logic controller.

5.4.1 Non Linear load without DSTATCOM

The total harmonic distortion of the system by connecting nonlinear load without DSTATCOM as shown in Figure 8. The Current and voltage harmonic distortion at the PCC is observed that 19.20% and 18.39% as shown in Figure 8(a) & 8(b). The source current harmonic distortion 19.11% as shown in Figure-8(c).

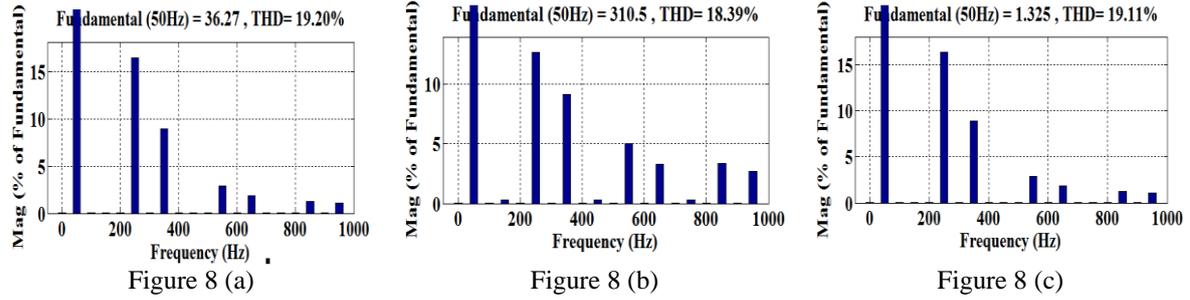


Figure-8. (a) Current THD at PCC, (b). Voltage THD at PCC, (c). Source current THD (BUS-1)

5.4.2. DSTATCOM with PI Controller

BY Connecting DSTATCOM to the proposed system, the current and voltage harmonic distortion at the PCC is reduced to 2.41% and 2.60% as shown in Figure 9(a) & 9(b).the source current harmonic distortion is reduced to 2.13%, as shown in Figure 9(c).

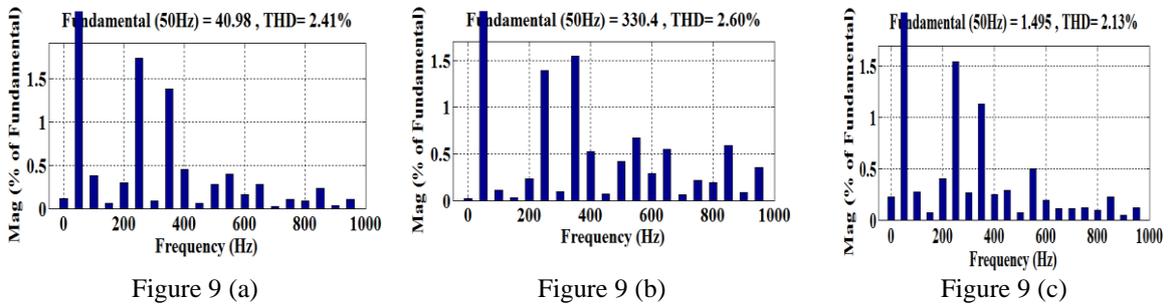


Figure 9 (a) Current THD at PCC, (b). Voltage THD at PCC, (c). Source current THD (BUS-1)

5.4.3. DSTATCOM with Fuzzy logic Controller

With fuzzy logic controlled DSTATCOM the current and voltage harmonic distortion at the PCC is reduced to 1.49% and 2.07% as shown in Figure 10(a) & 10(b). And the source current harmonic distortion is reduced to 1.41%, as shown in Figure 10(c).

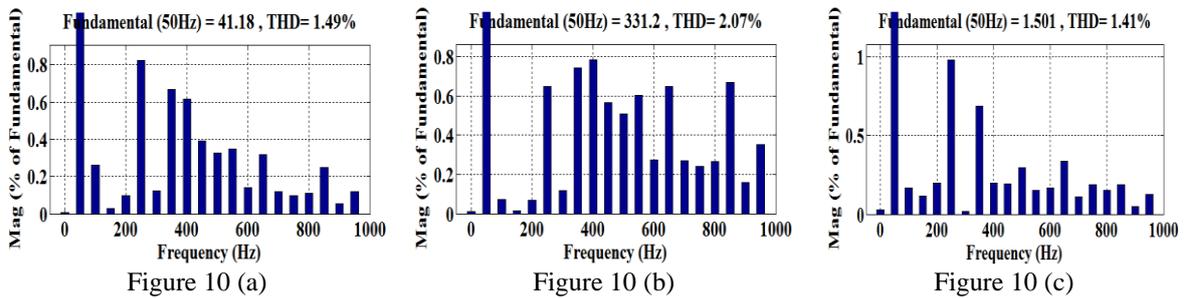


Figure 10. (a) Current THD at PCC, (b). Voltage THD at PCC, (c). Source current THD (BUS-1)

By observing from the above total harmonic distraction analysis the fuzzy logic controller DSTATCOM is providing better harmonic distortion as compare to the PI controller DSTATCOM.

5.5. Comparison of DC-link voltage

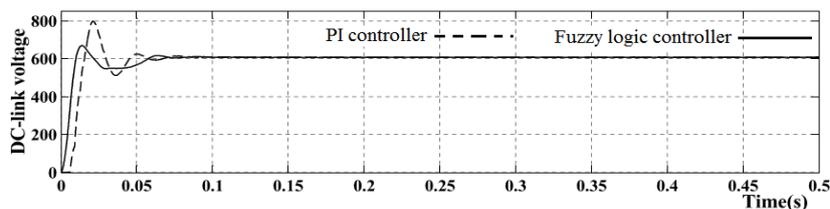


Figure-11. Comparison of DC-link voltage variation with PI and Fuzzy logic controller

The dc-link voltage comparison with PI and Fuzzy logic controller as shown in Figure-11. It is observed that the transient time and settling of fuzzy logic controller is better as compare to the PI controller.

6. CONCLUSIONS

In this paper, the performance of VSI based DSTATCOM has been analyzed using the synchronous reference frame control algorithm for the compensation of reactive power, harmonic and load balancing in three phase three wire nonlinear load distribution system. The control algorithm used here is simple and easy to implement and found to be satisfactory for nonlinear load distribution system. The simulation results for the proposed system is analysed and compared separately with PI and FUZZY logic controller. The dc bus voltage is maintained constant under all disturbance conditions for both controllers. Both controllers provide good compensation, but fuzzy logic controller provides superior performance under nonlinear load conditions. The simulation results obtained in MATLAB/SIMULINK software show that the fuzzy logic controlled DSTATCOM provides better system dynamic response and hence it improves transient behaviour and power quality as compared to the PI controlled DSTATCOM.

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Appendix A

Supply voltage	11Kv
Step-down Transformer	11/0.4Kv
Nonlinear load	Three phase bridge rectifier with R-L load. R=15Ω, L=60mH
DC-link capacitance	3000Uf
DC-link voltage	600V
Ripple filter	$L_f=0.3\text{mf}$, $C_f=30\text{uf}$
DC-bus PI controller	$K_{p1}=0.667$, $K_{p2}=0.0078$
AC-bus PI controller	$K_{p1}=6.07$, $K_{p2}=0.067$