Grid Connected Inverter for Current Control by Using Anti-Islanding Technique

B V Rajanna¹, Dr K S Srikanth²

¹Department of Electrical and Electronics Engineering, K L E F, Vaddeswaram, Guntur, A.P., India ²Department of Electrical and Electronics Engineering, K L E F, Vaddeswaram, Guntur, A.P., India

Article Info ABSTRACT

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The inverter with critical loads should be able to provide critical loads with a stable and seamless voltage during control mode change as well as clearing time. The indirect current control has been proposed for providing stable voltage with critical load during clearing time and seamless control mode transfer of inverters. However, the islanding detection is difficult since with the indirect current control the magnitude and frequency of voltage do not change when the islanding occurs. The conventional anti-islanding method based on the magnitude and frequency of voltage variation cannot apply to the indirect current control. This paper proposes an islanding detection method for the indirect current control. The proposed islanding detection and observation when the frequency and magnitude of voltage don't vary during clearing time. In order to verify the proposed anti-islanding method, the experimental results of a 600W three-phase inverter are provided.

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Corresponding Author:

B V Rajanna Department of Electrical and Electronics Engineering, Koneru Lakshmaiah Education Foundation(K L E F), Green fields, Vaddeswaram, Guntur, Andhra Pradesh, India. Email: rajannabv2012@gmail.com

1. INTRODUCTION

The micro-grid is considered as a possible solution to the reliability limitation of the traditional power system [1]. The micro-gird consists of energy storage systems (ESS) and distributed generation (DG) systems such as photovoltaic (PV), wind power and a cluster of critical load. Figure 1 shows the circuit diagram of the grid-connected inverter for ESS with a critical load. When an islanding occurs the inverter for ESS should detect the islanding within 2 seconds and disconnect the inverter from the grid according to IEEE 1547 and 929 standards [2-5]. During the clearing time the magnitude and frequency of voltage across the critical load may vary as shown in Figure 2, since the voltage is determined by the amount of the injected power and load condition. Further, a large transient may occur on the critical load at actual turn off of the switch since the inverter based on the conventional control method changes from current control to voltage control modes. Regarding this concern, an indirect current control was proposed in [6-10] for seamless transfer during the clearing time as well as at turn-off of the switch. However, since the indirect current control does not make the magnitude and frequency of voltage across the load vary when an islanding occurs, the conventional anti-islanding method such as the reactive power variation (RPV), the magnitude and frequency positive feedback and the active frequency drift (AFD) based on the magnitude and frequency of voltage variation cannot be applied to the indirect current control [11-15]. The harmonic injection antiislanding method can detect the islanding, but it may cause a severe distortion in current and voltage of the critical load [16-20].

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Figure 1. Grid connected inverter with critical load

Figure 2. Simulation of mode transfer with Conventional control

In this paper, an islanding detection method for the indirect current control is proposed. The proposed islanding detection method can detect the islanding using reactive power perturbation and observation in the inverter being operated based on indirect current control, which does not cause a change in magnitude and frequency of critical load voltage. The inverter detects reactive power mismatch between inverter reactive power Q and reactive power reference Q^* , leading to detection of islanding condition. Therefore in the inverter based on indirect current control the proposed antiislanding method can detect islanding. Finally the experimental result from a 600W three-phase inverter verifies that the proposed antiislanding method can detect the islanding without NDZ.

2. PROPOSED ANTI-ISLANDING

2.1. Conventional RPV anti-islanding method

According to IEEE 1547 and 929 standards, an islanding test scheme for verification of the antiislanding method is shown in Figure 3. The islanding test scheme consists of a grid connected inverter, a RLC load, a switch, a recloser and the grid. Quality factor of the RLC load is 2.5, and resonant frequency f_{res} is set to be the same as grid frequency. Then, values of the RLC load is determined by (1) and (2).

$$Q_f = R_{\sqrt{\frac{c}{L}}} \tag{1}$$

$$f_{res} = \frac{1}{2\pi\sqrt{LC}} \tag{2}$$

Active and reactive powers of the RLC loads can be expressed, respectively, as follows:

$$P_{Load} = 3 \frac{V_{PCC}^2}{R} \tag{3}$$

$$Q_{Load} = 3\left(\frac{V_{PCC}^2}{2\pi f L} - 2\pi f C. V_{PCC}^2\right) \tag{4}$$

where V_{PCC} and f represent voltage and frequency of PCC, respectively. By combining (1), (2), (3) and (4), Q_{Load} can be rewritten as

$$Q_{Load} = P_{Load} Q_f \left(\frac{f_{res}}{f} - \frac{f}{f_{res}} \right)$$
⁽⁵⁾

Equation (5) shows that the load reactive power is zero when frequency equal to the resonance frequency. In Fig. 3(a) showing the grid-connected mode, the load reactive power Q_{Load} is the sum of inverter reactive power Q and grid reactive power ΔQ :

$$Q_{Load} = Q + \Delta Q \tag{6}$$



Figure 3. Islanding test scheme (a) Grid connected mode, (b) Islanding mode





Figure 4. Conventional RPV anti-islanding method

Figure 5. Reactive Power P&O anti-islanding method

When the grid is connected the load reactive power is zero, and the inverter supplies reactive power to the grid. In the conventional Reactive Power Variation (RPV) method the inverter changes reactive power periodically, as shown in Figure 4. Then, when the islanding occurs the RPV method forces the frequency of V_{PCC} to exceed the frequency thresholds, leading to detection of islanding condition. In Figure 3(b) showing the islanding mode, the inverter reactive power is equal to the load reactive power:

$$Q_{Load} = Q \tag{7}$$

Because the load reactive power Q_{Load} is determined by frequency of V_{PCC} , the inverter changes frequency of V_{PCC} for reactive power output. Therefore, the frequency of V_{PCC} exceeds the frequency thresholds and the islanding is detected.

2.2. Proposed Reactive Power P&O Anti-islanding method

The conventional RPV method detects variation in the frequency of V_{PCC} . However, with the indirect current control the frequency of V_{PCC} does not change when the islanding occurs. Therefore, the conventional RPV method cannot be applied to the indirect current control. The proposed antiislanding method use reactive power variation, but an islanding detection indicator is not frequency but reactive power. In Fig. 5, the proposed islanding method detects reactive power mismatch between inverter reactive power Q and reactive power reference Q^* . If the frequency of V_{PCC} is limited by allowable maximum frequency f_{max} and maximum frequency f_{min} according to IEEE 1547 and 929 standards, the inverter reactive power range is defined as

$$P_{Load}Q_f\left(\frac{f_{res}}{f_{min}} - \frac{f_{min}}{f_{res}}\right) \le Q(=Q_{Load}) \le P_{Load}Q_f\left(\frac{f_{res}}{f_{max}} - \frac{f_{max}}{f_{res}}\right)$$
(8)

If reactive power reference Q^* exceeds reactive power range of equation (8), the inverter cannot generate the reactive power reference when the islanding occurs, resulting in the islanding detection. Therefore, the proposed anti-islanding method based on the reactive power can be applied to the indirect current control.

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Figure 6. Proposed control algorithm



Figure 7. Key waveform of the proposed P&O antiislanding method

Figure 6 illustrates the control block diagram of the proposed anti-islanding method for the indirect current control. The proposed anti-islanding method consists of a reactive power perturbation for changing reactive power reference Q^* periodically and a reactive power observation for measuring reactive power Q. The islanding detector detects the mismatch between inverter reactive power Q and reactive power reference Q^* . The indirect current control consists of an outer current control loop for regulating current and an inner voltage control loop for controlling capacitor voltage. Both current control and voltage control loops are used for the grid connected mode, and a single voltage loop is used for the standalone mode. Current command iL_g d_q^* can be obtained by power calculation, respectively:

$$i_{Lg}^{q^*} = \frac{\frac{2}{3} \left(p^* v_g^{q} + Q^* v_g^{d} \right)}{\left(v_g^{d} \right)^2 + \left(v_g^{q} \right)^2} \tag{9}$$

$$i_{Lg}^{d^*} = \frac{\frac{2}{3}(P^*V_g^d - Q^*V_g^q)}{(V_g^d)^2 + (V_g^q)^2}$$
(10)

Capacitor voltage reference V_{Cf^*} can be obtained by adding nominal value $V_{Cf,nom^{dq}}$ to output $i_{Lg,PI^{dq}}$ of the current controller. The outputs of the current controller and PLL are restricted by the limiters. Figure 7 shows key waveforms of the reactive power P&O anti-islanding method based on the indirect current control. In the grid connected mode inverter reactive power Q is regulated to follow the reactive power reference Q^* , and load reactive power Q_{Load} is zero. When the islanding occurs, the magnitude and frequency of the capacitor voltage do not change limiters, and inverter reactive power Q isn't regulated, resulting in the islanding detection. It is seen that the transient voltage across the load is negligible at actual turn-off of the switch since the voltage control is well maintained.



Figure 8. Simulation results of the proposed islanding detection method under resistive load condition. When the grid is connected, (a) P=1kW, $P_{Load} = 500W$, $\Delta P = -500W$ (b) P=250W, $P_{Load} = 500W$, $\Delta P = 250W$

3. SIMULATION RESULT

In order to validate the proposed anti-islanding method, simulations have been done under the condition of resistive load, reactive load and RLC load with the quality factor of 2.5, respectively. The system specification used in the simulation is as follows: $P_o = 500W$, $V_{DC} = 225V$, $V_{grid} = 64V$, $L_i = 1.68$ mH, $L_g = 3$ mH and $C_f = 3$ _F. Figure 8(a) and (b) show the islanding detection results under the resistive load condition. When the grid is connected the inverter reactive power Q changes periodically, and the load reactive power Q_{load} is zero. When the islanding occurs, the inverter reactive power Q becomes zero since the inverter reactive power Q_{load} , and then the islanding is detected due to reactive power mismatch between the inverter reactive power Q and the reactive power reference Q^* . Figure 9 and 10 show the islanding detection results under the reactive power Q, but cannot regulate reactive power Q since f is limited by frequency limiter, resulting in the islanding detection.



Figure 9. Simulation results of the proposed islanding detection method under reactive load condition. When the grid is connected, (a) $P = P_{Load} = 500$ W, $\Delta P = 0$ kW, $Q_{Load} = -30$ var (b)) $P = P_{Load} = 500$ W, $\Delta P = 0$ kW, $Q_{Load} = 30$ var



Figure 10. Simulation results of the proposed islanding detection method under RLC load(quality factor=2.5) condition



Figure 11. Experimental results of the proposed islanding detection method showing the mode transfer from stand-alone mode to grid-connected mode when islanding occurs. When the grid is connected, (a) $P=P_{Load}$, $\Delta P = 0$ W, $\Delta Q = 0$ W (b) $P = P = P_{Load} + 300$ W, $\Delta P = -300$ W, $\Delta Q = 0$ W

4. EXPERIMENT RESULT

A 600W three-phase inverter is built and tested to verify the proposed anti-islanding method, and the experimental results are provided. The system specification used in the experiment is as follows: $P_o = 600W$, $V_{DC} = 225V$, $V_{grid} = 64V$, $L_i = 1.68$ mH, $L_g = 3$ mH and $C_f = 3$ _F, RLC Load (quality factor = 2.5), Figs. 11(a) and (b) show the islanding detection within 2 seconds and the seamless mode transfer from gridconnected mode to stand-alone mode when the islanding occurs.

5. CONCLUSION

The conventional anti-islanding method cannot be applied to the indirect current control since the magnitude and frequency of voltage do not change even after the islanding occurs. An issue of detecting the islanding arose for the indirect current control method. In this paper, a new islanding detection method that can be applied to the indirect current control is proposed. The proposed islanding detection method can detect the islanding using reactive power perturbation and observation in the inverter being operated based on indirect current control, which does not cause a change in magnitude and frequency of critical load voltage not only during clearing time but at the moment of mode change. In order to verify the proposed islanding detection method, the experimental results of a 600W three-phase inverter with RLC load (qfactor= 2.5) are provided. The experimental results show that the proposed islanding detection method is effective in detect islanding within 2 seconds according to IEEE 1547 and 929 standards, while providing stable and seamless voltage for critical load.

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