# **Implementation of non-isolated three-port converter through** augmented time response

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ABSTRACT

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The work is concerning a multi-port dc-dc converter with improved time response and steady state output. Here the converter carries bare amount of switches for managing the power with mono inductance. The inductance and along with that the switched capacitance are pre owned to bring large voltage gain. This paper put forwarded an appropriate controller for the closed loop monitored high-gain converter with three ports. Higher is that the conversion rate. This converter is also a good interface between DC-source and load that aims to progressing time response with FLC and PI controller in the closed loop system. The converter with the PI controller and FLC is look over and the fast responses are compared with time domain specifications. The simulation outcome indicates that the FLC based converter brings most excellent time domain response.

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#### **INTRODUCTION** 1.

The use of electricity is increasing day by day. As non-renewable energy sources would run out in few years many research developments are going on renewable energy sources to generate power. PV source is one of primesources of renewable energy [1]-[3]. Therefore, batteries are used for continuous supply of power to load. Figure1 shows a photo voltaic system with three port converter and a battery backup. The limitation of conventional boost converter is that it should activate at tremendous duty cycle ratios to attain required voltage gains which may results in extreme conduction losses, severe reverse recovery losses. Other method of increasing the gain of voltage is by converters with switched capacitor or switched inductor this method is complex when achieving the conversion ratio. Due to high voltage ratings devices undergo more conduction losses in this technique [4], [5].

To boost the voltage gain in an inaccessible dc-dc converter use an inductor which is coupled. The above-mentioned method in boost converters can supply high voltage gain skiping a severe duty cycle operation [6], [7]. Also, efficiency is boosted with soft switching in high power applications. As we need high voltage gain zero voltage switching (ZVS) is used rather than zero current switching (ZCS) as this technique boosts the voltage and decrease the current passing through the device that is discussed in [8]-[10]. Also, in this paper the simulation results of PI and a fuzzy closed loop controller is compared. In the recent years, FLC has good approach of actual mathematical model of the controlled system [11], [12]. The high gain converter enhanced with fuzzy logic controller shows considerable improvement.



Figure 1. Block diagram of non-isolated three-port converter

### 2. PROPOSED THREE PORT CONVERTER

The proposed converter topology has the power switches, energy storage elements, two capacitors, and a pi filter circuit to filter the ripples as shown in Figure 2. To the bidirectional port circuit [13]-[18] the primary input source is  $V_{pv}$  and secondary source is  $V_{bt}$ .



Figure 2. Proposed converter topology

# 3. DESIGN PROCEDURE

The precise value of capacitance and inductance makes a right choice of preferred output. The desired values of energy storage elements inductance and capacitance can be obtained using the following formulae.

Let X<sub>L</sub> be the inductive reactance and X<sub>C</sub> be the capacitive reactance.

$$X_c = 1/2\pi fc$$
  
 $X_L = 2\pi fl$ 

The design of output filter circuit is expressed as follows, Assume ripple factor(r)=10^-4

 $R = \sqrt{3/w^2}lc$ 

The losses can be calculated for the devices

For Mosfet =  $v_{d*}i_d$ For Diode =  $v_{d*}i_d$ 

Total losses=losses in mosfet+losses in diode.

#### 4. SIMULATION DIAGRAM FOR THE PROPOSED CONVERTER

# 4.1. Simulink model for proposed converter with R load

The simulink model for the projected elevated step up three port converters with the resistive load is shown in Figure 3. In this model the power switches, the diodes, the coupled inductor and the pi filter circuits

are connected. The pulse generator triggers the power mosfets with resistive load. The scope is used to measure the voltage and current across each and every device.



Figure 3. Simulation diagram for proposed high step up converter with R load

The input voltage is taken from the photo volataic source. In the above simulink model for the proposed converter circuit the dc input voltage of 12V is given by connecting the photovoltaic module and is shown in Figure 4



Figure 4. Input voltage

To drive the controller circuit effectively the switching pulses are generated. The switching pulses generated for Vg3 and Vg1 of the projected high step up three port converters is shown in Figure 5. The switching pulses and the drain to source voltage across Vg3 and Vds are shown in Figure 6.



Figure 5. Switching pulse for high step up converter

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Figure 6. Switching pulse & drain to source voltage

The simulation output voltage of the converter using simulink is shown above in Figure 7. The main objective is to build a renewable energy-based converter solar photo voltaic system. For the input voltage of 12 V we can get the output voltage of 105 V.



Figure 7. Output voltage

Ripple voltage is the leakage of alternating voltage in DC output voltage. The output ripple voltage generated in the anticipated three port converter circuit with the combination of resistive load is shown in Figure 8.



Figure 8. Output ripple voltage

#### 4.2. Simulink model for proposed converter with motor load

In the below simulink model the resistive load is replaced by the motor load. Figure 9 shows the simulation diagram of proposed converter circuit connected with the motor load. The scope is connected at the load side to check the speed of the motor in rpm and Torque in N-m.



Figure 9. Simulation diagram of converter with motor load

Similarly, when the motor load is allied at output terminal of the converter with an input voltage of 12V we can see that motor runs at a speed of 1500 rpm is represented in Figure 10. The speed of the motor is progressively growing for a period of instance subsequent to that it maintains constant speed.





The preliminary torque developed is soaring very high. The torque developed in the motor rapidly increases to its maximum value and settle down to its steady state value. The torque developed in the motor load is shown in Figure 11.



Figure 11. Torque in N-m

#### 4.3. Simulink model for proposed converter of closed loop system with R load

Figure 12 shows the simulation diagram of proposed three port converter of closed loop system using PI controller with resistive load. The input supply of 12 V and 60 W boosting converter is used for simulation. The value of  $K_p = 0.018$  and  $K_i = 6$ . The Ton and Toff ratio is given to the transfer function model to obtain the output value.

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Figure 12. Simualtion diagram of closed loop system with R-load

In PI controller when the disturbance is given at particular instant of time at the input, the time taken to settle that is to come to a constant voltage will be long and is shown in Figure 13. Similarly the output current is shown in Figure 14 and the output power is shown in Figure 15.



Figure 15. Output power

# 4.4. Simulink model for proposed converter of closed loop system with motor load

Figure 16 shows the simulation diagram of proposed three port converter of closed loop system using PI controller with motor load. The input supply of 12 volts and 60 watts boosting converter is used for

simulation. The value of  $K_p = 0.018$  and  $K_i = 6$ . The Ton and Toff ratio is given to the transfer function model to obtain the output value.



Figure 16. Simualtion diagram of closed loop system with motor load

Figure 17 shows the input supply of closed loop system connected with the motor load. Under this loaded condition the torque developed in the motor and the corresponding speed of the motor is shown in Figure 18 and Figure 19.



Figure 19. Motor torque

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# 5. PROPOSED CLOSED LOOP SYSTEM WITH FUZZY LOGIC CONTROLLER

The Figure 20 shows the fuzzy logic based automatic control system. Here the mandani method is used for fuzzification process and centroid method is used for defuzzification process so as to get the crisp output.



Figure 20. Fuzzy logic controller

#### 5.1. Simulink model for proposed converter using fuzzy logic controller with R load

Figure 21 shows the simulation diagram of fuzzy logic based closed loop system with resistive load. The disturbance is created at the input terminal by giving the time response at 0.3 ms and the amplitude value is about 0.5 V both the time response and the amplitude values are not exact values.



Figure 21. Simualtion diagram of fuzzy based closed loop system R load

The fuzzy logic controller which is used as closed loop controller will take the signal from the feedback element. The feedback element will take the signal from the load and it will compare the results with the reference valve. From that feedback signal the output will go through the derivative block where it will check run time error and give signals to the fuzzy logic controller. Here we are using centroid method rule based membership function. The fuzzy logic controller will act depending on the the time response that is given to the pulse generators. The changes due to disturbance can be reflected at 0.3 seconds in output voltage shown in Figure 22. The output can be maintained constant after 0.3 seconds.



Figure 22. Output voltage

Figure 23 shows that ouput current across the resistive load of 1.1 A. The output power is improved drastically and 1106 W is achieved. The output power yielded under the resistive loaded condition is shown in Figure 24.





Figure 24. Output power

#### 5.2. Simulink model for proposed converter using fuzzy logic controller with motor load

Figure 25 shows the simulation illustration of fuzzy logic controller based closed loop system with motor load. Depending on the output range from the fuzzy controller the pulses will be generated to the mosfet switches. According to this signals the mosfets will either on or off. The range for the switches are operated under180 degrees modes of operation.

The rotating speed of the motor is shown in Figue 26. The snubber protection is provided to diminish the voltage spikes. In this among all the three switches the second switch will act as regenerative snubber switch which will be used to reduce spike voltage near the switches [19]-[21].

Figure 27 shows the torque developed in the motor and settle down at 0.3 sec as shown in. The below data is the difference between the PI and fuzzy logic controllers [22]-[25]. When compared to the PI and fuzzy logic controller output results beter performance in fuzzy logic control.

There is a large amount of decrease in both time domain specifications and error. So it is best to go for the fuzzy logic controller for closed loop purpose when compared to the other controllers. In the time response analysis of the fuzzy logic based converter the output ripple voltage isreduced from 0.25 V to 0.002 V. Pi filter produces minimum ripple voltage.  $T_r$  reduced from 0.33s to 0.30s,  $T_p$  reduced from 0.35 s to 0.31 s,  $T_s$  reduced from 0.44 s to 0.32 s and the steadty state error is reduced from 1.2s to 0.09 s.



Figure 25. Simualtion diagram of fuzzy based closed loop system R load



Figure 27. Motor torque

### 6. CONCLUSION

Overall, we can see that with the usage of the proposed non isolated three port fuzzy logic based converter various time domain parameters are enhanced while operation. Also in this paper we can see that fuzzy logic contoller gives better results when compared with pi controller compared with all the mechanical characteristics. In this multi port fuzzy based converter the number of switches are considerably reduced. The time response of this system also enhanced significantly.

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