Three-phase photovoltaic grid inverter system design based on PIC24FJ256GB110 for distributed generation

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Article Info	ABSTRACT
Article history:	In recent years, the demand for energy generated by photovoltaics has
Received Dec 21, 2018 Revised Jan 21, 2019 Accepted Mar 4, 2019	increased because of the cheapness of this energy, especially in the Middle East. This study focuses on the design and implementation of a three-phase photovoltaic inverter system for distribution of generator applications. The three-phase inverter used in this study was designed using GWM 100-01X1 as a three-phase full bridge MOSFET package. The MOSFET full bridge of
<i>Keywords:</i> DG GWM 100-01X1 Hall sensors	the three-phase inverter is driven using IR2104 gate driver IC, and the Hall sensors of the voltage and current are used to measure the AC and DC voltage and current. The SPWM signal is generated using a PIC24FJ256GB110 as a microcontroller and interfaced to the full bridge package. Simulation and experimental results are presented to validate the three-phase inverter design.
PIC24FJ256GB110 Three-phase inverter SPWM	Copyright © 2019 Institute of Advanced Engineering and Science. All rights reserved.

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1. INTRODUCTION

The first solution to photovoltaic power generation is how to convert solar radiation energy to electrical energy. The principle of solar photovoltaic cell power generation is based on the photovoltaic effect, which is a conversion device that converts solar radiation energy into electrical energy. A plurality of such devices are assembled into a photovoltaic cell assembly, and a plurality of components are combined into a photovoltaic array as needed and matched with the devices for energy storage, measurement, control, and inverter, thereby forming a photovoltaic power generation system [1-7].

To meet the static and dynamic requirements of the load on the distributed generation system, the three-phase photovoltaic inverter is generally designed as a closed-loop feedback control system, and the starting point for the analysis and control of the closed-loop control system involves correctly reflecting the basic relationship between input and output [8-12].

As shown in Figure 1, the basic principle is that the three-phase photovoltaic array inverter is connected to grid, and the generated solar energy is transferred to a DC power source, and then passed through the DC filter to become an alternating current through the inverter. The fundamental wave is a sine wave, which is inductively filtered and merged into the grid, which incorporates the current. The frequency and phase are in phase with the grid voltage. Photovoltaic grid-connected inverters use voltage source input and current source output control. The output current is controlled using voltage control [13-16].

The main hardware of the full system in this study is a part of the control system that uses PIC24FJ256GB110 as a microcontrol core [17, 18], three-phase full bridge MOSFET module (GWM 100-01X1) [19], and IR2104 gate driver IC as an isolation gate drive circuit [20, 21]. The Hall sensors of the voltage and current are used to measure the AC and DC voltage and current [22-26].

This paper is organised as follows: in Section 2, a mathematical model of the three-phase photovoltaic inverter is provided. Section 3 discusses the hardware circuit structure. Section 4 presents the simulation and practical results of the full system, and finally, Section 5 discusses the conclusions.

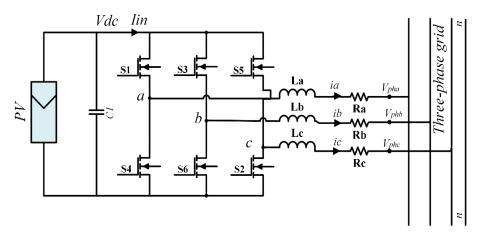


Figure 1. Three-phase photovoltaic inverter system

2. MATHEMATICAL MODEL OF THREE PHASE PHOTOVOLTAIC INVERTER

For the distributed generation of the three-phase grid, the main topology of the three-phase photovoltaic grid-connected power circuit inverter is shown in Figure 1. In the figure, Vdc is the DC voltage across the DC link capacitor, C is the DC bus capacitance, and LC is a filter in the AC side before being connected to the grid. Additionally, Vpha, Vphb, and Vphc are the phase voltages of the three-phase grid. According to Figure 1, the three-phase grid is assumed to be symmetrical. According to Kirchhoff's law, the mathematical model of the inverter in the three-phase stationary coordinate system can be expressed in (1).

$$L\frac{d}{dt}\begin{bmatrix}ia\\ib\\ic\end{bmatrix} + R\begin{bmatrix}ia\\ib\\ic\end{bmatrix} = \begin{bmatrix}V_{an}\\V_{bn}\\V_{cn}\end{bmatrix} - \begin{bmatrix}V_{pha}\\V_{phb}\\V_{phc}\end{bmatrix}$$
(1)

Where,

 V_{an} , V_{bn} and V_{cn} are the output voltage of the output Leg's A, B, and C of the three-phase photovoltaic inverter relative to the neutral point of the grid. i_a , i_b and i_c are the three-phase output current of the inverter. $(L_a = L_b = L_c = L)$ and $(R_a = R_b = R_c = R)$

In the two-phase stationary coordinate system, the grid voltage $\alpha\beta$ component is changed. When predictive control is performed, if the change is not considered, the system performance will be affected, especially when the switching frequency is small. If the variation of the grid voltage $\alpha\beta$ component is considered, the complexity of the control algorithm will increase. In the synchronous rotating coordinate system, the d-axis and q-axis components of the three-phase symmetric grid are approximately constant, so the control strategy of this study is synchronous. Implemented in a rotating coordinate system. After the dq coordinate transformation, the mathematical model of the inverter in the two-phase synchronous rotating coordinate system can be presented in (2).

$$L\frac{d}{dt}\begin{bmatrix}i_d\\i_q\end{bmatrix} + \begin{bmatrix}R & -wL\\wL & R\end{bmatrix}\begin{bmatrix}i_d\\i_q\end{bmatrix} = \begin{bmatrix}V_d\\V_q\end{bmatrix} - \begin{bmatrix}e_d\\e_q\end{bmatrix}$$
(2)

Where,

 i_d and i_q are grid-connected currents, V_D and V_Q are inverter bridge output voltages, and e_d and e_q are grid voltages.

The angle between the synchronous rotating coordinate system and stationary coordinate system $\theta = \theta_1 + 2\pi f_1 t$, f_1 is the grid frequency, θ is the starting angle, and θ_1 can be any value. Only the grid

frequency needs to be detected, which reduces the control difficulty. For ease of analysis, coupling is performed by (3).

$$\begin{bmatrix} V_d'\\ V_q' \end{bmatrix} = \begin{bmatrix} V_d\\ V_q \end{bmatrix} + \begin{bmatrix} wLi_d\\ -wLi_q \end{bmatrix}$$
(3)

If we ignore the influence of line resistance R, the final mathematical model can be obtained in (4).

$$L\frac{d}{dt}\begin{bmatrix}i_d\\i_q\end{bmatrix} = \begin{bmatrix}V_d'\\V_q'\end{bmatrix} - \begin{bmatrix}e_d\\e_q\end{bmatrix}$$
(4)

According to the instantaneous power theory, instantaneous active power P and reactive power Q output from the three-phase grid-connected inverter are present in the synchronous rotating coordinate system, as shown in (5).

$$\begin{bmatrix} P\\Q \end{bmatrix} = \frac{3}{2} \begin{bmatrix} e_d & e_q\\e_q & -e_d \end{bmatrix} \begin{bmatrix} i_d\\i_q \end{bmatrix}$$
(5)

3. HARDWARE CIRCUIT STRUCTURE

The main hardware part of the control system uses PIC24FJ256GB110 as a microcontrol core, three-phase inverter back package, isolation gate drive circuit, and current and voltage sensors (AC and DC). The PIC24FJ256GB110 control unit mainly completes the A/D conversion function and the processing of the pulse width modulation control signals.

3.1. The PIC24FJ256GB110 control unit

The PIC24FJ256GB110 microcontroller is the main part used to control and implement the threephase inverter. The PIC24FJ256GB110 is programmed using a MPLAB X IDEV3.5 software compiler for programming, and using C language [6][9][10] and Explorer 16 kit for the microchip. The SPWM control signals that are generated by the PIC24FJ128GA010 microcontroller are used as inputs to the three-phase full bridge driver circuit. The PIC24FJ256GB110 is selected because it is highly preferred, offers low power consumption, and is used in industries with high-voltage automotive applications. The properties of the microcontroller are shown in Table 1, and the Explorer 16 kit is presented in Figure 2.

	Table 1. The properties of PI	C24FJ256GB110
Figure 2. The Explorer 16 kit with PIC24FJ256GB110 [18]	Specification	Detail
	Device	PIC24FJ256GB110
	No. of Pins	100
	Program Flash Memory (Kbyte)	256
	RAM (Kbyte)	16
	Timer 16-bit	5
	Capture Input	9
	Compare Std. PWM	9
	ADC	DC, 3
	UART	4
	SPI	3
	$12C^{TM}$	2
	Enhanced CAN TM	2
	I/O Pins (Max)(2)	44
	Package	PF, PT

3.2. Three-phase MOSFET inverter package

Figure 3 presents the three-phase full bridge MOSFET module (GWM 100-01X1) obtained from the IXYS company for this study. The unit can withstand a maximum current of 90A and a maximum voltage of 100V. The SPWM signals generated using the PIC24FJ256GB110 are driven by the drive circuit to drive the MOSFET. The MOSFET is driven by the integrated circuit and IR2104 module, which is isolated by an optocoupler.

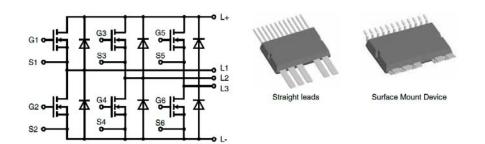


Figure 3. Three-phase full bridge MOSFET module (GWM 100-01X1) []

3.3. Voltage and current sensor circuit

The three-phase voltage sensor circuit shown in Figure 4a is used to measure a phase AC voltage that is less than 250 VAC. The current acquisition is performed using a Hall sensor. The three-phase Hall sensors of the inverter in Figure 4b are widely used in variable frequency speed control devices, inverter devices, UPS power supply devices, and power grid monitoring systems because of their high precision, good linearity, wide frequency bandwidth, fast response time, small size, and strong anti-interference ability.



Figure 4. The sensors: (a) Voltage sensors and (b) Current sensors

3.4. Gate driver circuit design

The MOSFET full bridge of the three-phase inverter is driven by the hybrid integrated module IR2104 gate driver IC. This driver is used as a MOSFET driver with independent to another power supply because it is a high-voltage, high-speed output channel. The IR2104 drive has many applications – DC-DC, AC/DC, DC/AC – for high-power density and efficiency and motor drive applications such as home appliances and LED drivers. A half-bridge gate driver circuit based on IR2104 is shown in Figure 5 with two MOSFET transistors.

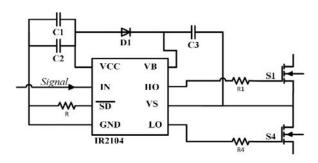


Figure 5. IR2104 gate driver circuit

4. SIMULATION AND PRACTICAL RESULTS

This section is divided into two parts: the first part discusses the MATLAB/Simulink results for the three-phase photovoltaic inverter, and the second part discusses the practical system's hardware and results.

4.1. Three-phase simulation results

Figure 6 presents the full system of the three-phase PV inverter with feedback and full control. This system was built using MATLAB/Simulink. The simulation parameters are $V_dc=500V$, L=0.3mH, 400 VAC with 50Hz and the three-phase power grid with a carrier frequency of 5 kHz. Figure 5 shows the simulated waveforms of the grid-connected current and voltage. The grid-connected current sinusoidality of the improved predictive control method is high and stable, and the inductance value fluctuation is small and stable, and the expected control target is achieved. Because the grid is symmetrical, the voltage and current waveforms of phase A are selected to present the current and voltage.

The simulation results of the three-phase photovoltaic inverter for DG distribution in Figure 6 are presented in Figure 7. Figure 7a shows the DC link voltage. The output voltage of the three-phase inverter is shown in Figure 7b, and Figure 7c presents the output current of the three-phase inverter. Finally, the voltage and current of phase A are shown in Figure 7d. Figure 7d presents the voltage and current of phase A in the same sequence.

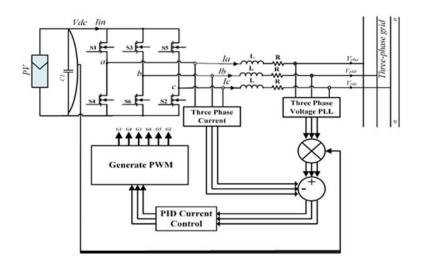
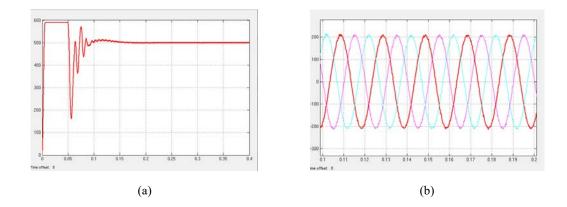


Figure 6. Three-phase photovoltaic inverter with full control circuit



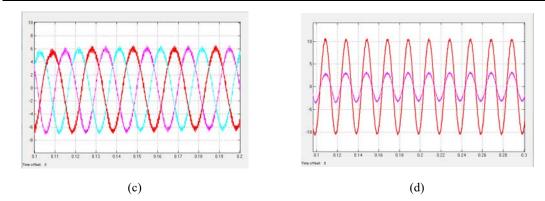


Figure 7. MATLAB/Simulation results: (a) DC link voltage across DC link bus capacitor, (b) Three-phase voltage (phase to ground), (c) Three-phase inverter current (phase current), and (d) Voltage and current results for Phase A

Figure 8 presents the THD of the three-phase currents THD (phase A) = 3.34%, THD (phase B) = 3.40% and THD (phase C) = 2.753%. The average value of THD for the full system is equal to 3.16%, which is acceptable because the IEEE standard accepts a THD $\leq 5\%$.

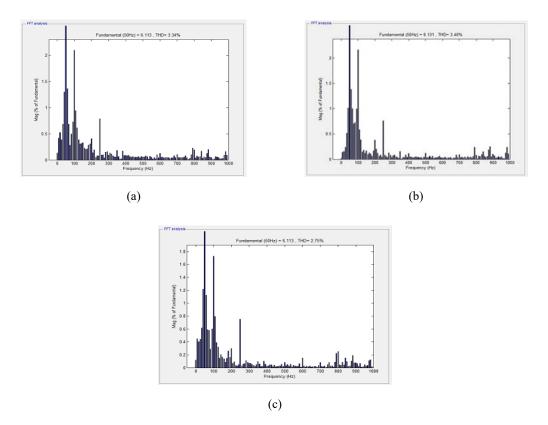


Figure 8. THD of three-phase current: (a) THD (phase A), (b) THD (phase B), and (c) THD (phase C)

4.2. Three-phase practical results

Figure 9 present the demo hardware of the full system of the three-phase inverter built in the lab. It shows the isolated driver circuit, measuring voltage and current circuit, three-phase bridge inverter, and Explorer 16 kit with PIC24FJ256GB110 microcontroller.



Figure 9. Full hardware circuit of the three-phase photovoltaic inverter system

The output current and voltage of the practical system of the three-phase inverter is shown in Figures 10(a) and (b) respectively. Figure 10(c) presents the positive results of phase A's current and voltage in the same phase.

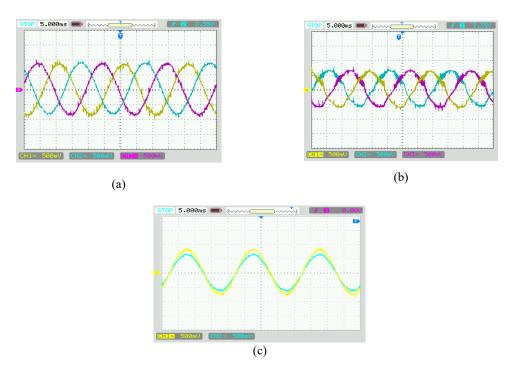


Figure 10. The practical results: (a) Three-phase output voltage, (b) Three-phase output current, and (c) Voltage and current in the same phase in Phase A

5. CONCLUSION

This study involved the design and implementation of a three-phase photovoltaic grid-tied inverter controlled by PIC24FJ256GB110 based on the distributed generation system. The microcontrol (PIC24FJ256GB110 chip) core used an advanced and powerful microchip with high reliability and high efficiency. The Hall sensor of the voltage and current was used to measure the AC and DC voltage and current. This practical design was made in the lab mainly by using the basic structure of the three-phase photovoltaic inverter system, selecting the appropriate main control chip, designing the three-phase inverter power supply realised using the PWM modulation method, and using the current and voltage feedback loops mode in the instantaneous value method to ensure that the output of the inverter was stable and that the L filter was used to maintain the quality of the output power. The simulation and experimental results verified the rationality of the mathematical model and control strategy and provided a theoretical basis for engineers and technicians. This modelling and control method can also be extended to other types of PWM changes.

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