ISSN: 2088-8694, DOI: 10.11591/ijpeds.v10.i3.pp1529-1537

Battery management system on electric bike using lithium-ion 18650

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Article Info

Article history:

Received Sep 29, 2018 Revised Jan 27, 2019 Accepted Mar 13, 2019

Keywords:

Active Balancing, Battery Management System Battery Pack

ABSTRACT

Electric bike (E-Bike) is a bicycle driven using an electric motor and uses batteries as the energy source. It is environmentally friendly as no exhaust gas is resulted during its operation. More than one battery is normally required, being arranged in series or in parallel connection. Over limit or overloaded conditions of battery usage will reduce the lifecycle of battery, speed up its replacement and add to the maintenance cost of electric bike. This paper proposes the prevention of such degrading condition using a tool to manage the battery usage both during the charging and discharging process. The proposed electronic Battery Management System (BMS) serves to regulate, monitor, and maintain the condition of batteries to prevent any possible damage. The resulted BMS design could provide a well balancing action in a battery system consisting of 13 cells utilizing the cell-to-cell active balancing method. The test results showed that the proposed BMS could monitor the individual cell voltage with an average error of 0.032 V (0.824%), while reading the charge and discharge current with an average error of 0.04 A (6.25%), and the battery pack temperature with an average error of 1.21°C (2.9%). Additionally, the BMS could offer a functional battery pack protection system from conditions such as undervoltage, overvoltage, overheat, and overcurrent.

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1. INTRODUCTION

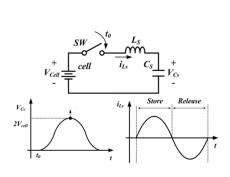
Battery which is normally used to compose the battery pack in an electric bike (E-Bike) is lithiumion 18650 [1-8]. It has a nominal voltage of 3.7 V and a capacity which varies between 2000 mAh - 3000 mAh. The main reason for the use of lithium-ion battery is due to its high energy density [9]. Battery packs as an energy source on electric vehicles such as E-Bike are composed of a number of Li-ion batteries arranged in series and parallel to obtain high voltage and capacity [10-15]. For safety consideration and to reach a long life-time, each cell in a battery pack needs to be in the same energy condition [16, 17]. Once there is a cell with a prohibited condition (overvoltage, undervoltage, overcurrent, overheat), the charging and discharging process must be stopped immediately. In addition, all cells in the battery series must be kept at the same voltage level, which is called as 'balance'. If they are in 'unbalance' condition, the protection system will stop the charging or discharging process. When only one cell of the battery pack is in an overvoltage or undervoltage condition while the other cells are in a safe condition, the overall battery pack performance will decrease [18-21]. To overcome this problem, the balancing process is carried out on the battery pack, using two basic methods, either passive or active balancing [21]. The passive method, which is cheap and whose mechanism is easy even though not efficient, is implemented by releasing the energy from each cell into a resistor until the voltage reaches the equal value as the smallest cell's voltage [22-24]. The active method is

realized by releasing the energy of the cell with the largest voltage into the one with the smallest voltage until reaching the same voltage on each cell. This method is more expensive because it requires complex components, complicated mechanisms, but with more energy efficiency [24-27].

BMS (Battery Management System) is a device employed to monitor battery's voltage and current, and it performs battery pack balancing system to protect the battery from damage [3, 6, 7, 16, 17]. The BMS presented in this paper uses the active balancing method with a cell-to-cell balancing mechanism and series capacitor inductor circuit (resonant LC) as energy transfer media. The resonant LC circuit contains inductors and capacitors arranged in series, utilizing the charging and discharging process of the capacitors as temporary energy storage and the inductors as balancing current suppressors [11]. The resonant frequency of the LC circuit is the frequency that causes the maximum current and voltage amplitudes in the circuit because the inductor's and capacitor's impedance values are the same but at different phases ($Z_L = Z_C$).

2. BMS DESIGN

The active balancing method with a cell-to-cell balancing mechanism and series capacitor inductor circuit (resonant LC) as energy transfer media to be adopted in the BMS design is shown in Figure 1 [11], whereas the related schema consisting of several sub-modules is illustrated in the block diagram of Figure 2.



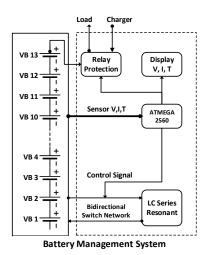


Figure 1. Resonant LC series circuit

Figure 2. Device block diagram

The BMS design is to be implemented on a battery pack consisting of 52 Li-Ion batteries in a 13-series, 4-parallel arrangement with a nominal voltage of 48.1 volts, a maximum voltage of 54.6 volts, and a total capacity of 10 Ah.

2.1. Design of voltage sensor

The voltage sensor circuit design shown in Figure 3 uses the principle of a voltage divider. A potentiometer of $100k\Omega$ is added with an N-Mosfet 2N7000 as a battery selector and a CD74HC 16 analogue multiplexer to connect to the analog-to-digital conversion (ADC) pin contained in the ATMega 2560 microcontroller. By knowing the total voltage of the battery pack at each level, the voltage of each cell can be determined by the reduction of it.

The voltage divider circuit uses a linear *trimpot* with a value of $100k\Omega$, which is installed on each cell of battery series, as specified in Figure 3. The ratio of potentiometer values is calculated so that the maximum V_{out} of each battery pack level is not more than 4.5 volts in order not to exceed the V_{in} limits of multiplexer and ADC. The calculation results are given in Table 1.

$$R_{pot} = \frac{4.5 \, volt \, x \, 100 k\Omega}{V_{\text{max} \, batt}} \tag{1}$$

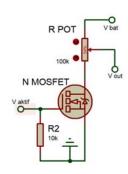


Table 1. Calculation results of potentiometer resistance						
Battery	V _{batt} (volt)	Vout (volt)	R _{pot} (kΩ)			
1	4.2					
2	8.4	4.5	53.6			
3	12.6	4.5	35.7			
4	16.8	4.5	26.8			
5	21	4.5	21.4			
6	25.2	4.5	17.9			
7	29.4	4.5	15.3			
8	33.6	4.5	13.4			
9	37.8	4.5	11.9			
10	42	4.5	10.7			
11	46.2	4.5	9.7			
12	4.2					
13	8.4	4.5	53.6			

Figure 3. Voltage sensor circuit

The algorithm used in the microcontroller utilizes the process of reading the voltage sensor as shown in the flowchart in Figure 4.

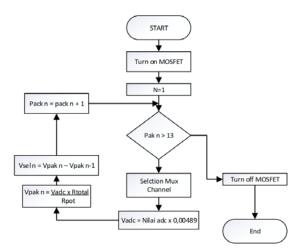


Figure 4. Flowchart of the voltage sensor reading

2.2. Design of current sensor

The current sensor used is IC ACS712 30A. This sensor can measure currents with a range of 30 A to -30 A with a sensitivity of 66mV/A and V_{out} at 0A current is 2.5 volts [5]. The output of this current sensor is connected to the ATMega 2560 ADC pin which has a resolution of 10 bits, and the reading process is programmed every 1 second to find out the battery capacity. The current sensor used is only 1 piece and is placed between the battery pack and the charger/load. The current sensor circuit used is shown in Figure 5.

2.3. Design of temperature sensor

The temperature sensor used is the LM35 IC with 10mV/°C sensitivity and the reading range is -50°C. This temperature sensor has 4 pieces which are placed on each side of the battery pack, and the sensor output pin is connected directly to the ATMega 2560 pin ADC microcontroller which has a 10-bit resolution.

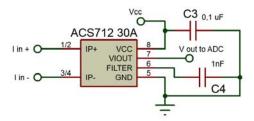
2.4. Design of the cells balancing circuit

The cell balancing circuit being used as an energy transfer medium is an LC series circuit with a switching mechanism at the LC resonance frequency in order to make the energy transfer more maximal. The inductor value is determined as small as possible, i.e. of 33 uH, so the capacitor value can be calculated using (2) with a determined value of 220uF in order to obtain an underdamping response. By using (3), the circuit resonance frequency of 1.8 kHz is obtained.

$$C \ge \frac{4L}{R^2} \tag{2}$$

$$f = \frac{1}{2\pi\sqrt{LC}} \tag{3}$$

The switching mechanism is carried out by connecting the cell with large voltage to an LC series circuit to store its energy, and then connecting the LC series circuit to the cell with low voltage to release the stored energy. The switching circuit uses a 2-way switch concept which means that the switch can flow in 2 directions, as shown in Figure 6.



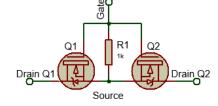


Figure 5. Current sensor circuit

Figure 6. Bidirectional switch N-Mosfet

As shown, the circuit uses two components of N-channel MOSFET IRF540 in series, except the uppermost cells series which uses P-channel MOSFET IRF9540. The PC817 optocoupler is used as the MOSFET driver, and the optocoupler input is connected to the ATMega 2560 microcontroller digital pin to select the cell to be balanced. The entire balancing circuit is shown in Figure 7.

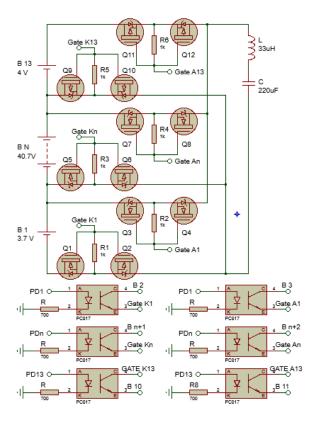


Figure 7. Balancing cell circuit

2.5. Design of protection circuit

The purpose of the protection circuit is to disconnect the power lines between the battery and the load using a 30 Ampere relay and fuse as shown in Figure 8. Fuse is useful for preventing component damage when a sudden overcurrent occurs. Fuse is used because it has a faster response time compared to relay. Relay on the other hand is used because it decides the path mechanically to prevent overvoltage, undervoltage, overheat, overcurrent conditions. Relay is activated through ATMega 2560 pin digital based on information from voltage sensors, current sensors and temperature sensors.

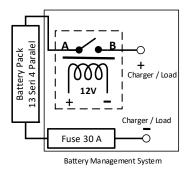


Figure 8. Protection circuit

2.6. Software development

The software development has been done using C-language (Arduino IDE). The sequence of operational instructions has been uploaded into the ATmega 2560 microcontroller.

3. RESULTS AND DISCUSSION

3.1. Voltage sensor testing

This test was aimed to measure the cell voltage of each battery in the battery pack which is composed of 13 series. The results are given in Table 2.

Table 2. Voltage sensor testing results

	** ***			** ***		1. **
Battery	V _{pack} VM	V _{pack} Sensor	$ \Delta V_{pack} $	$V_{cell} VM$	V _{cell} Sensor	$ \Delta V_{cell} $
1	3.94	3.93	0.01	3.94	3.93	0.01
2	7.85	7.85	0	3.91	3.92	0.01
3	11.88	11.87	0.01	4.03	4.02	0.01
4	15.71	15.68	0.03	3.83	3.82	0.01
5	19.69	19.63	0.06	3.98	3.95	0.03
6	23.68	23.66	0.02	3.99	4.03	0.04
7	27.51	27.46	0.05	3.83	3.8	0.03
8	31.47	31.33	0.14	3.96	3.87	0.09
9	35.46	35.39	0.07	3.99	4.07	0.08
10	39.23	39.15	0.08	3.77	3.76	0.01
11	42.9	42.79	0.11	3.67	3.64	0.03
12	46.8	46.75	0.05	3.9	3.96	0.06
13	50.7	50.63	0.07	3.9	3.89	0.01
	Avera	ge ΔV_{pack}	0.054	Avera	ge ΔV_{cell}	0.032

From Table 2, it is known that the pack voltage state had a maximum difference of 0.14 V for the 8th battery pack with an average difference of 0.054 V, and for the maximum cell voltage difference of 0.09 V for the 13th battery cell with an average difference of 0.032 V.

The reading error occurred because of the voltage drop on the MOSFET and multiplexer, the voltage reading error of 0 volt, and the accuracy of the potentiometer value used to calculate the voltage value in the program. The method of calculating cell voltage by knowing the voltage of the pack being followed with the calculation of the related difference was prone to errors, because the error reading voltage of the pack would affect the reading of the cell's voltage and also the following steps.

3.2. Current sensor testing

Current sensor testing was carried out during the charging process using a battery charger and the battery discharging process using a resistor.

The testing results of ACS712 sensor output voltage are given in Table 3. It indicates a linear characteristic to current with a sensitivity of 66mV/A and the output voltage is 2.5 V at the current of 0 A. Using the straight-line equation approximation, the sensor output voltage function has been obtained.

$$y = mx + c \tag{4}$$

$$V = 0.066I + 2.5 \tag{5}$$

$$I = 15V - 37.5 \tag{6}$$

The maximum difference of the ACS712 30A sensor current reading was 0.16 A, with an average error of 0.04 A. The error of the current sensor reading occurred because the sensor output error itself was 1.5% at 25° C and the ADC resolution was used.

Testing the reading time of current sensor was done using the millis function available on the microcontroller to ensure whether the current sensor reading had been performed every 1 second. From Table 4, it can be seen that the current sensor reading process was carried out every 1 second with a minimum error of 1 millisecond and a maximum error of 104 milliseconds.

Table 3. ACS712-30 testing results

Charging		
I Amp meter (A)	I ACS712 (A)	$\Delta I(A)$
4.02	4.05	0.03
Discharging		
I Amp meter (A)	I ACS712 (A)	$\Delta I(A)$
0.03	0.04	0.01
-0.75	-0.77	0.02
-1.46	-1.43	-0.03
-2.16	-2.16	0
-3.4	-3.34	-0.06
-4.53	-4.51	-0.02
-5.71	-5.68	-0.16
Averag	0.04	

Table 4. The results of of current sensor reading time

Data	Time	Current	Data	Time	Current
	(ms)	(A)		(ms)	(A)
1	999	0.15	34	34004	0.30
2	1999	0.30	35	35005	0.30
3	2999	0.30	36	35901	0.30
4	3999	0.30	37	36901	0.15
5	5000	0.15	38	37901	0.30
6	6000	0.30	39	38902	0.15

3.3. Temperature sensor testing

Testing the temperature sensor was done by comparing 4 temperature sensors with a digital thermometer TP3001, placed on the heater by conduction. Test results of 4 temperature sensors are shown in Table 5, whereas the related characteristic obtained is given in Figure 9.

From the results shown, it is known that the difference in measurement of the largest temperature occurred in sensor 2 of the 7th measurement which was equal to 5.7°C. The average sensor 1 difference was 1.58°C, sensor 2 was 2.1°C, sensor 3 was 1.88°C, and sensor 4 was 1.5°C. Figure 9 shows that there was a small difference between the characteristic curve of the sensors and that of the digital thermometer, with a maximum difference of 2.5°C of the 7th measurement.

Table 5. LM35 temperature sensor testing results

No Ther	Thermometer	LM35 Sensor				A	ΔΤ
	Thermometer	1	2	3	4	Average	$\Delta 1$
1	29.6	27.8	27.8	27.3	27.8	27.7	-1.9
2	35.2	34.2	34.2	33.7	34.2	34.1	-1.1
3	38.1	36.1	36.6	35.6	36.6	36.2	-1.9
4	42.9	41.5	42.9	41	42	41,8	-1.1
5	44.5	43.4	45.4	42.9	44.4	44	-0.5
6	53.3	51.2	55.6	51.7	55.6	53.6	0.3
7	61.6	62	67.3	62	64.9	64.1	2.5
8	71.2	68.3	74.7	67.8	72.2	70.8	-0.4
Average ΔT						1.21	

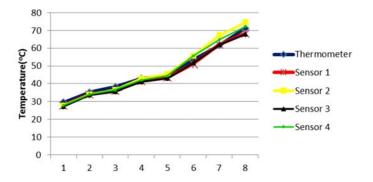


Figure 9. Temperature sensor characteristic

3.4. Cells balancing circuit testing

 $\Delta V(V)$

Testing the cell balancing circuit was carried out between the two highest-voltage batteries with the lowest-voltage battery. The results are given in Table 6.

 Table 6. Cells balancing circuit testing results

 Battery
 V
 Battery
 V
 Battery
 V
 Battery

 B1
 4.05
 B2
 3.77
 B3
 3.79
 B4

0.39

0.42

3.78 В5 3.77 В6 3.59 В7 3.73 B83.90 **B9** 4.04 B10 3.77 B11 3.77 B12 3.92 B13 V_{avg}=3.77 volt 3.85 $V_{max} : B1 = 4.05$ $V_{min}: B6$ 3.59 volt 2 4 5 6 Time (hour) $V_{max}(V)$ 4.04 4.03 4.02 4.015 4.011 4.005 3.661 3.666 3.669 $V_{min}(V)$ 3.62 3.64 3.65

0.37

0.354

0.345

0.336

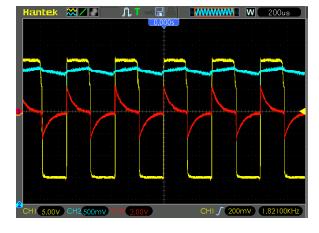


Figure 10. Output signal

The balancing process took 6 hours to reduce the difference between the strong and weak battery voltage from 0.46 V to 0.336 V with effective balancing current of 22.5 mA.

In Figure 10, the yellow signal indicates the signal of the MOFET switch which connected the higher voltage battery to the series LC circuit. The blue signal indicates the capacitor voltage signal, whereas the red signal was the current signal flowing in the capacitor. When the MOFET switch logic was High, the capacitor voltage roses to 4.04 V, which means there was charging on the capacitor with a current of 24 mA. When the MOFET switch logic was Low, the capacitor voltage dropped to 3.84 V, which means discharging the capacitor with a current of -24 mA.

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3.5. Protection circuit testing

This test was conducted to find out the protection system in the form of relay working according to the conditions of the battery. The conditions which needed to be protected were undervoltage, overvoltage, overheat, and overcurrent. Undervoltage and overvoltage tests were carried out using a variable voltage source, whose limits could be set. The undervoltage limit was 2.75 V while the overvoltage limit was 4.2 V. The observation during experiment showed that the undervoltage protection and overvoltage protection were working by interrupting the relay when it exceeded the voltage limit.

Overheat testing was done using a heat source which could be adjusted, with the temperature limit was under 45°C. The experiment brought to a conclusion that the overheat protection system worked by interrupting the relay when the temperature was above 45°C.

Overcurrent testing was carried out during charging and discharging conditions with a current limit set to 1A when charging and 4A during the discharging process. Charging process was tagged by a positive current, whereas during the process of discharging, the current flew negatively. From experiment, it could be concluded that the overcurrent protection system worked well during the charging and discharging process with the relay disconnected when it crossed the line.

4. CONCLUSIONS

The analysis of results obtained from the experiment in this research brings to some conclusions that the designed BMS could monitor the voltage of individual battery cell in a pack with an average error of 0.032 V (0.824%), the charging and discharging currents of battery pack with an average error of 0.04 A (6.15%), and the temperature of the battery pack with an average error of 1.21°C (2.9%). The constructed BMS also could provide the protection when an undervoltage, overvoltage, overheat, or overcurrent condition occured. The active balancing could be well achieved using the designed BMS, with the voltage difference between the strong battery and weak battery reaching 0.336 V within 6 hours.

It is also to be suggested that in the future the voltage readings use a differential ADC IC so that it would not require a resistor which could absorb energy. Addition of series LC circuit could also be made, so that two balancing processes could occur at once to accelerate the balancing time.

ACKNOWLEDGEMENTS

The authors would like to thank International Scientific Publication Office of Brawijaya University, Indonesia for the funding of this publication.

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Taufik received his Doctor of Engineering from Cleveland State University. He joined the Electrical Engineering department at Cal Poly State University in 1999 where he is currently a tenured Full Professor and the Director of Electric Power Institute. He received numerous teaching awards, most notably the 2012 Outstanding Teaching Award from the American Society of Engineering Education - Pacific Southwest Section. He is a Senior Member of IEEE and has industry experience with several international companies. His areas of research include power electronics, power systems, rural electrification, energy harvesting, renewable energy and smart grid. He has published over 200 technical papers and journals, reports, books, course readers, and served on the editorial review boards of several journals.