Three phase PUC5 inverter fed induction motor for renewable energy applications

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Article Info	ABSTRACT	
Article history:	This paper gives the overview three phase 5 level packed U cell (PUC5)	
Received Jul 17, 2019 Revised Sept 9, 2019 Accepted Oct 25, 2019	inverter fed Induction Motor feed from renewable energy sources for electric vehicles and water pump set in agriculture applications. PUC5 inverter requires a minimum number of semiconductor devices, one PV panel and only one capacitor in each phase. Maximum all possible switching sequences are analyzed and sensor less voltage balancing technique depending on	
Keywords: CHB NPC	simple switching modes was integrated as level shifted PWM controlle control the PUC5 inverter switches. Sensor less voltage balancing fixe capacitor voltage (Vc) at half of the PV source (Vdc) magnitude in phase, which results the symmetric five level wave in phase voltage o	
Power Quality PUC5	and seven level waveform in line voltage with less harmonics. This topology minimizes the complication in the control system which creates the PUC5 inverter most suitable for industrial applications and market requirements. It	
Multievel Inverter Single DC Source	count, voltage rating, voltage balancing and complexity etc. In order to test the performance proposed PUC inverter it is tested with rated load with Matlab/Simulink. The results obtained have shown good dynamic	

performance under different conditions.

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1. INTRODUCTION

The world power demand in the energy market leads to redesign of power converters. The power conversion quality is broadly divided into two categories. The first converters inject pollution into the system in term of power factor and harmonics, researchers are trying to enhance the power quality by using filters. Generally, this reduces the efficiency of power conversion and increase cost of installations. The main aim of the second one is to deliver a less cost, high energy efficient inverter without pollution, multilevel inverters belongs to this category [1]. The multilevel inverters role has been increasing in medium and high power applications in the power market, because of advantages, like their capacity to synthesize a waveform with more number of levels, better voltage profile, reduced switching stresses, minimized output filter size. Multilevel found the solution to extend the inverter operating voltage higher than the voltage limit of semiconductor devices [2]. NPC topology was first proposed by Mabae and Takahashi in 1981. Ten years later flying capacitor converter topology was introduced by Meynard and Foch, in this converter the two clamping diodes of NPC was replaced by capacitors [3]. Implicated cell multilevel topology has been explained in1993 and H-bridge cells in 1996. Which connect several inverters in cascade connection with

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separate DC sources. This converter can avoid clamping diodes or voltage balancing capacitors. During these years, several optimizations were proposed by researchers and showed very good performance, such as a large number of levels with with improved output waveforms, low total harmonic distortion and improved efficiency [4-6].

All these topologies have been introduced for multilevel converters that utilized the combination of active switches and DC sources to generate multilevel waveform at the output [7, 8]. The major challenges of these multilevel inverters are usage of more number of isolated DC sources and too many numbers of switching devices, these problems are eliminated in PUC converter which is invented by Al-Hadded in 2010 and several papers are published by Al-Haddad and Hani vahedi on the PUC inverter with R and RL loads [9]. This paper proposes a three phase five level PUC inverter fed induction motor feed from the PV source through DC/DC converter, compared all popular multilevel topologies and simulate the test system using Matlab/Simulink. This topology is interesting in hybrid electric vehicles and agriculture pump sets by utilizing solar and wind energy [10-12]. Five level PUC inverter is established as a solution to the seven level PUC inverter issues. Switching modes of PUC5 converter are controlled by sensor less voltage balancing techniques, this technique is recommended because of simple design and does not require any sensor, which increase the reliability of the system and full fill the industrial requirements.

2. PUC5 CONVERTER

Three phase PUC5 inverter fed induction motor is shown in Figure 1. It consists of four low voltage switches two high voltage switches in each phase. A 1.8 kW PV array connected to each phase of three phase PUC5 inverter at V_{DC} through DC/DC boost converter. The PV array uses six sun modules (SRP-305E-WHT-D) in each phase. Total 18 modules (6*3*305 = 5.49 kW) with open circuit voltage of 64.2V and short circuit current of 5.96A at 1000 w/m² sun irradiance and 25°C ambient temperature [13-15]. The DC/DC converter uses incremental conductance and integral regulator MPPT technique is used to extract maximum power from the panel under irregular weather conditions. Boost converter boosts the voltage to 440V and connected to PUC5 inverter at V_{DC}. If this V_{DC} is 2E volts, then another source V_C must be E volts [16]. Previously published papers on 7 level PUC inverter with different modulation techniques show some limitations with voltage balancing procedure in terms of modulation index [17]. These limitations are eliminated in sensor less voltage balancing technique and do not require sensors or controllers to maintain required voltage across capacitor V_C [18, 19]. In the design consideration power industry use identical devices in the multilevel inverters. It is difficult to have equal voltage range switches. But PUC converter shown in Figure 2(a), has a voltage of 2E volts across S1, S3 and remaining all switches subjected to E volts, by adding two identical switches in series in the S1, S2 of inverter it could be designed with identical voltage rating switches as shown in Figure 2(b). Another design consideration about switches S4, S5 and S6 are working complementary to S1, S2 and S3, So three main pulses devise S1, S2 and S3 while their NOT sent to S4, S5 and S6 [20].



Figure 1. Three phase PUC inverter fed induction motor



Figure 2. PUC5 inverter with (a) unequal voltage rating switches (b) equal voltage rating switches

3. SENSOR-LESS VOLTAGE REGULATING TECHNIQUE

Table 1 shows that the capacitor may be charged or discharged in any one half cycle. Only to maintain the capacitor voltage constant the switching sequence of the capacitor is designed in such a way that it should be discharged in the negative half cycle and charged during the positive half cycle. Because of the switching sequence of capacitor and output waveform frequency the capacitor charge is increased to half of the main source voltage (V_{dc}). As a result this capacitor voltage (V_C) increases when it is connected in series with the load and V_{dc} source, decreases when it is connected in series with the load. The capacitor charges in switching states 2 and 7, similarly capacitor discharge in switching states 3 and 6. The capacitor voltage (V_C) during charge/discharge is $\pm E$ volts as shown in Table 1. This charging and discharging is written into the equation (1).

$$V_1 = V_2 + V_{out} \Longrightarrow \begin{cases} 2E = V_2 + E\\ -2E = V_2 - E \end{cases} \Longrightarrow |V_2| = E$$
(1)

If the source voltage magnitude V_{dc} is fixed at 2E, to produce the required load voltage, the capacitor voltage V_c must be maintained at E. So the charging and discharging time keeps the capacitor voltage at $\pm E$. Therefore, to maintain equal charging and discharging times, switching state 2 charge the capacitor in the positive half cycle and switching state 6 discharge the capacitor in negative half cycle [21, 22]. Charging and discharging of capacitors depends on the type of load only, not on the output frequency or switching frequency, the size of the capacitor depends on the type of load.

The sensor less voltage regulating technique is integrated with modulation technique [23-25]. The level shifted Multi carrier switching technique is used as modulation technique [26]. For implementation of five level PWM inverter scheme, four carrier waveforms (Cr1, Cr2, Cr3, and Cr4) and reference sine wave are used shown in Figure 4.



Figure 4. Five-level PWM scheme using four level shifted carrier waves

Hear four triangular signals are vertically shifted for modulating the reference sine wave. This modulation output pulse is associated with the switching states 1, 2, 5, 6 and 8 shown in Table 1. The algorithms for generating the firing pulses are presented in Figure 5. This proposed algorithm generates the

five level output on PUC5 inverter after setting the capacitor voltage at a correct value without any feedback sensor. The capacitor and output voltages don't depend on the type of the system model (e.g. Average modelling), modulation index, feedback sensors, grid frequency and switching frequency.

It can operate the system voltage to any arbitrary value. The PV source voltage may be flacuvates because of changes in climatic conditions. This sensor less voltage balancing perfectly work with any changes in source voltage. We can change the fundamental frequency as well as induction motor speed, by changing the V_{Ref} signal frequency.



Figure 5. Proposed sensor-less voltage regulating approach integrated into switching technique

Table 1. Sv	vitching states,	Capacitor	charging/	discharging	states and	l output vo	oltage of	five	levels	PUC
				inverter						

niverter								
State	S1	S2	S3	V output	V out value	Capacitor voltage status		
1	1	0	0	V _{DC}	+2E	No Effect		
2	1	0	1	V_{DC} - V_{C}	+E	Charging		
3	1	1	0	$V_{\rm C}$	+E	Discharging		
4	1	1	1	0	0	No Effect		
5	0	0	0	0	0	No Effect		
6	0	0	1	- Vc	- <i>E</i>	Discharging		
7	0	1	0	V_C - V_{DC}	- <i>E</i>	Charging		
8	0	1	1	- V _{DC}	-2E	No Effect		

4. COMPARISON BETWEEN PUC5 AND OTHER TOPOLOGIES

This PUC5 topology is separately compared with full bridge, CHB and NPC/T3 converters in terms of component count, switching frequency and voltage rating. Obviously FC inverter has more number of components to control since this topology was not considered. However, NPC and T3 have same principle concept, they are compared in the same subsection.

4.1. Comparison between single phase PUC and FB inverters

Full bridge inverter shown in Figure 6(a) is the most common inverter topology in the market because of simple structure. It has one isolated DC source, four switches. However, each switch suffers 2E volts if V₁ would be equal to PUC5 inverter DC source. Based on an equal voltage rating design the switches cost of PUC5 would be lower than full bridge at equal voltage/power rating. However full bridge produces three level output voltage while PUC5 inverter has better THD results with five level voltage with added cost of one capacitor. More ever the quasi-sine wave at the output, reduce the harmonics which would diminish the filter size at least by half ratio and overall size and cost of inverter would be reduced.

4.2. Comparison between single phase PUC and CHB

A five level CHB consists of two full bridge cells connected in series as shown in Figure 6(b). Where as the output voltage is the sum of two cells, so it includes two isolated DC sources of each E volts and eight switches with an identical voltage rating of each E volts. Accordingly, it has exactly the same design as the PUC5 topology as shown in Figure 2(a). These two converters generate five level voltage at the output. So the THD and output filter design would be same. Only the difference between five level CHB and PUC5 is one DC source. Two DC sources of Magnitudes E are used in CHB to generate five level waveform. The main question is that using two DC sources with each E volts is better or using a single DC source with

2E volts? Normally in renewable energy conversion system only one bus available from the PV or wind farms that should be connected to the inverter. Therefore CHB with more than one isolated DC source require more splitting in the PV panel or wind turbine into different arrays with separated connections to make more isolated DC buses. Such type configuration requires power balancing between DC sources, particularly during any renewable energy generation level variation caused by environmental factors. However, replacing the existing two level topology with those inverters that have more isolated DC sources would be more difficult due to change the DC side structure. Because of the above discussed merits, PUC5 inverter with single DC source is the best solution.



Figure 6. (a) Full bridge inverter, (b) Five level CHB inverter

4.3. Comparison between single phase PUC and NPC/T3

NPC and T3 are the most popular in applications as a three level inverter, while line voltage will generate five levels of output when two legs and one DC source are used in single as shown in Figure 7(a) and 7(b). An NPC contains eight switches and four diodes which is considerably more than PUC5 components. T3 inverter has eight switches as shown in Figure 7(b). Out of eight S1, S2, S3 and S4 are required to bear the whole DC bus voltage 2E volts. This unequal voltage rating switches is the main disadvantage in T3 inverter. Moreover, both NPC and T3 inverters require two capacitors to split the single DC source voltage, it is one extra capacitor than PUC5 inverter capacitors. Eventually the voltage balancing also difficult in NPC/T3 when compared with PUC5.



Figure 7. (a) Five level NPC inverter, (b) Five level T3 inverter

4.4. Comparison between three phase PUC and NPC/T3

In both NPC and T3 inverters only one leg added to the single phase two leg structure shown in Figure 7 (a) and (b) and another advantage with three phase NPC is only one DC source and two capacitors are required for all three phases. But the voltage balancing is much more complex in these two inverters compared to PUC5. So many methods have been appeared in literature to balance the capacitor voltage at DC source such as linear PI regulator, nonlinear controllers and zero component injection. The simplest

controller used in NPC or T3 is space vector controller, this can be integrated into space vector modulation technique. However, in three phase PUC5 inverter simple sensor less voltage balancing is integrated into very simple modulation that is PWM modulation to regulate the capacitor voltage. Furthermore, the three phase NPC/T3 inverter produces three level waveform in each phase, further increase in levels require an extreme number of switches and isolated sources. On the other hand three phase PUC5 produces up to nine levels in each phase by using the same components used in three level PUC.

4.5. Comparison between three phase PUC and CHB

Three phase five level CHB consists of two sources and six switches in each phase that is total six sources and 24 switches. Only the difference is three isolated switches which will increase the manufacturing cost and size of the inverter. Remaining all characteristics, including voltage rating, voltage level, applications and THD value are same in both CHB and PUC inverters.

Table 2 shows the component count comparison for single phase five level inverters. All above comparisons proved that the PUC inverter is the best topology for replacing existing two, three and five level inverters on the market.

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Inverter type	DC Sources	Capacitors	Diodes	Switches	Total components
CHB	2	0	0	8	10
NPC without voltage control	4	0	6	8	18
FC	1	3	0	8	12
PUC5	1	1	0	6	8

5. SIMULATION RESULTS AND DISCUSSION

Proposed a three phase five level PUC5 inverter is built in Matlab/Simulink with the given parameters. The performance is tested with sudden load change at 0.5 Sec. It was simulated in Matlab/Simulink software and the results shows the performance of three phase PUC5 inverters fed induction motor. It is simulated for one second. The test system simulation parameters are listed in Table 3. Figure 8 shows the three-phase line to line voltage output of inverter with seven levels. Figure 9 shows capacitor voltage which is zero at starting and increasing up to 220 volts and then it is constant, maintained at half of the V_{DC} throughout the simulation. Figure 10 shows the phase to ground voltage output with the five levels.

Figure 11 shows the three phase inverter load current which is almost pure sine wave, Figure 12 shows the load current in B phase, which is very high at starting because of induction motor load after 0.1 seconds it settles to normal values, Figure 13 shows the THD spectrum of load current analyzed by FFT analysis. It is done at 0.5 sec as starting time and evaluated for three cycles. It shows the THD value 20.23%. Similarly, current wave showing the THD value 1.87%. Which is showing very good performance. Figure 14 shows the speed of the drive, which is almost constant, at 0.5 Sec sudden load change is applied.



Figure 8. Three-phase line to line voltage output of inverter with seven levels



Phase voltage (V)

-500

100

-100

0

0.05

0.1

Current (A)

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Figure 11. Three phase load current

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Figure 10. Phase to ground voltage output

Time (Sec)

Load current

0.15

Time (seconds)

0.2

0.25

0.3





Figure 12. Load current in phase B

Figure 14. THD spectrum of line voltage



Figure 13. Speed of the induction motor (Pu)

6. CONCLUSION

In this paper sensor less voltage controlling technic is suggested for PUC converter fed induction motor with a PV source along with boot converter and a single capacitor in each phase with minimum number components. Compared to all other existing systems, it requires only six switches in each phase and does not require any sensor which will reduce the cost and complexity of the system. All the demerits of neutral clamped inverters and flying capacitor inverters are eliminated in the PUC5 inverter. This system is simulated in Matlab/Simulinl, the results showing good dynamic performance and power quality is also improved. It has less weight because of less components and more suitable for moving electric vehicles. By varying the V_{ref} sine wave signal frequency, we can change the speed of the motor. This inverter is best suitable for interface the renewable energy source with induction motor.

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