

Simplified cascade multiphase DC-DC buck power converter for low voltage large current applications: part I --- steady-state analysis

Anand Bannet Ganesen, Nungky Prameswari, Falah Kharisma Nuraziz, Jihad Furqani, Arwindra Rizqiawan, and Pekik Argo Dahono

School of Electrical Engineering and Informatics, Institute of Technology Bandung, Indonesia

Article Info

Article history:

Received May 27, 2021

Revised Jul 1, 2021

Accepted Jul 23, 2021

Keywords:

Buck

Cascade

DC-DC Converter

Large Current

Multiphase

ABSTRACT

This paper presents a new simplified cascade multiphase DC-DC buck power converter suitable for low voltage and large current applications. Cascade connection enables very low voltage ratio without using very small duty cycles nor transformers. Large current with very low ripple content is achieved by using the multiphase technique. The proposed converter needs smaller number of components compared to conventional cascade multiphase DC-DC buck power converters. This paper also presents useful analysis of the proposed DC-DC buck power converter with a method to optimize the phase and cascade number. Simulation and experimental results are included to verify the basic performance of the proposed DC-DC buck power converter.

This is an open access article under the [CC BY-SA](https://creativecommons.org/licenses/by-sa/4.0/) license.



Corresponding Author:

Anand Bannet Ganesen

School of Electrical Engineering and Informatics

Institute of Technology Bandung

Jalan Ganesa no. 10, Bandung 40132, Indonesia

Email: anandbannetganesen@gmail.com

1. INTRODUCTION

A step-down DC-DC power converter is commonly used in DC power supplies, DC voltage regulators, DC welders, and cathodic protection systems. In some applications, the required output voltage is very low at very large output current. Moreover, the allowed output current ripple should be very low to not damage the internal components. In general, a step-down DC-DC power converter can be classified as isolated and non-isolated DC-DC power converter [1]. As efficiency is very important, the non-isolated one is the most commonly used. Several DC-DC power converters with very low voltage ratios have been proposed in the literature [2]-[10]. The most common method to achieve a very low voltage ratio is by cascading several conventional DC-DC buck power converters. However, the number of components is increased when the cascaded converters are increased. Different methods to simplify cascade DC-DC buck power converters have been proposed in the literature [11], [12].

To achieve large output current rating with reduced ripple content, the multiphase technique is commonly used [13]-[21]. If several multiphase DC-DC buck power converters are connected in cascade then, both very low voltage ratio and very small ripple content can be achieved. In this case, however, the required active and passive components number will be very high. This paper presents a new simplified cascade multiphase DC-DC buck power converter for very low output voltage and large output current applications. Very low voltage ratio is obtained by cascading several DC-DC buck power converters. In order

to reduce the required components, the simplified version of cascade single-phase DC-DC buck power converter is used. In order to increase the current capability and to reduce the ripple content, several simplified cascade single-phase are connected in parallel and operated as a cascade multiphase DC-DC buck power converter. In order to reduce further the required number of components, the resulting cascade multiphase DC-DC buck power converter is then simplified. Output voltage analysis of the proposed converter is analyzed by considering voltage drops across active and passive components. The obtained output voltage expressions are useful to determine the conduction losses and, therefore, the efficiency. Based on the conduction losses, the required phase number and cascaded number of the proposed DC-DC buck power converter can be determined. Several calculated and experimental results are included to show the basic performance of the proposed DC-DC buck power converter.

This paper is arranged into five sections. In section 2, the proposed DC-DC buck power converter topologies are derived and explained in detail. Section 3 theoretically analyzes the converter where the output voltage, together with the power loss calculation, are discussed. The optimization of the phase number and the cascade number is also presented in this section. The proposed converter chosen design and the experimental results are presented in section 4 to prove the analysis in section 3. Finally, the conclusion is drawn in the last section.

2. PROPOSED DC-DC BUCK POWER CONVERTER

This section discusses the derivation of the proposed DC-DC buck power converter. The discussion is started by discussing the conventional DC-DC buck power converter and cascade DC-DC buck power converter. The discussion is then followed by simplified cascade multiphase DC-DC buck power converter towards the proposed DC-DC buck power converter. Here, the discussion neglects the effects of voltage drops across the active and passive components.

2.1. Conventional DC-DC buck power converters

Figure 1 (a) shows a conventional single-phase DC-DC buck power converter. Though a MOSFET has been used as the active switching device, in practice we can use other active switching devices depending on the application. Under continuous conduction mode, the voltage ratio is (1),

$$\frac{V_o}{E_d} = \alpha \quad (1)$$

with

$$\alpha = \frac{T_{ON}}{T_S} \quad (2)$$

where V_o is the output voltage, E_d is the input voltage, and α is the duty cycle, T_{ON} is the ON-period of the switch and T_S is the switching period of the transistor, respectively. The duty cycle α can be varied from zero to unity and, therefore, the output voltage can be varied from zero to E_d . In practice, however, the duty cycle cannot be made too low due to the minimum turn-off time of the switching device. Thus, the minimum voltage ratio is limited.

In order to increase the output current rating and to decrease the ripple content, several single-phase DC-DC buck power converters can be connected in parallel and operated as a multiphase DC-DC buck power converter, as shown in Figure 1 (b). If the phase number is N , both current rating and ripple frequency will increase N times compared to single-phase DC-DC buck power converter.

2.2. Cascade DC-DC buck power converters

In order to improve the voltage-ratio capability, several DC-DC buck power converters can be connected in cascade as shown in Figure 2 (a), in which the grey-colored shows the cascade configuration. If all transistors are operated at the same duty cycle, the voltage ratio is (3),

$$\frac{V_o}{E_d} = \alpha^M \quad (3)$$

where M is the number of cascaded converters. For the same duty cycle, the obtained voltage ratio will be smaller compared to a single-stage DC-DC buck power converter. The method in Figure 2 (a) can be extended into the multiphase one and the result is shown in Figure 2 (b). This topology is named conventional cascade multiphase (CCM) DC-DC buck power converters. The CCM DC-DC buck power

converters have the same average output voltage as (3), with each switch is activated by similar carrier signals on phase difference of $2\pi/N$, where N is the number of phases. Nevertheless, the phase numbers of each stage do not have to be the same. Moreover, the duty cycle of each stage does not have to be the same. Both cascade number and phase number can be optimized to achieve a certain criterion.

2.3. Simplified cascade multiphase (SCM) DC-DC buck power converters

In order to reduce the required active switching devices, a simplified version of cascade single-phase DC-DC buck power converter shown in Figure 3 (a) was proposed [11]. This converter can also be configured into the multiphase one, as shown in Figure 3 (b). The obtained output voltage expression is still the same as given by (3). This converter can produce a very low voltage ratio with very low ripple content.

Even though the component number of active switching devices in Figure 3 (b) is lower than the one in Figure 2 (b), the requirement of electrolytic capacitors is higher. It has been shown in the literature [22]-[25] that electrolytic capacitors are the most unreliable component in power electronic system and, therefore, the used of electrolytic capacitors must be minimized.

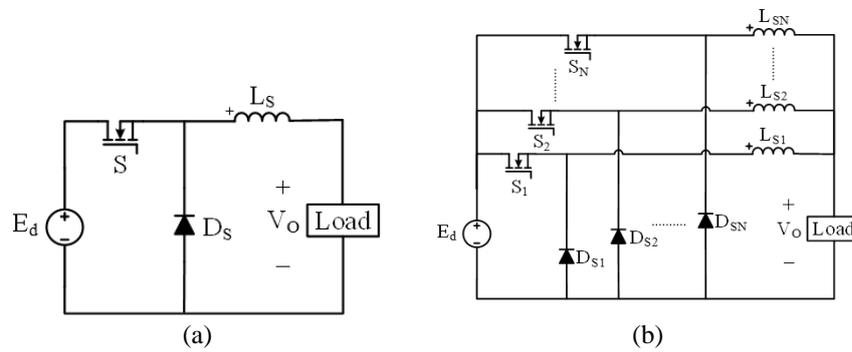


Figure 1. (a) conventional single-phase DC-DC buck power converter, (b) conventional multiphase DC-DC buck power converter

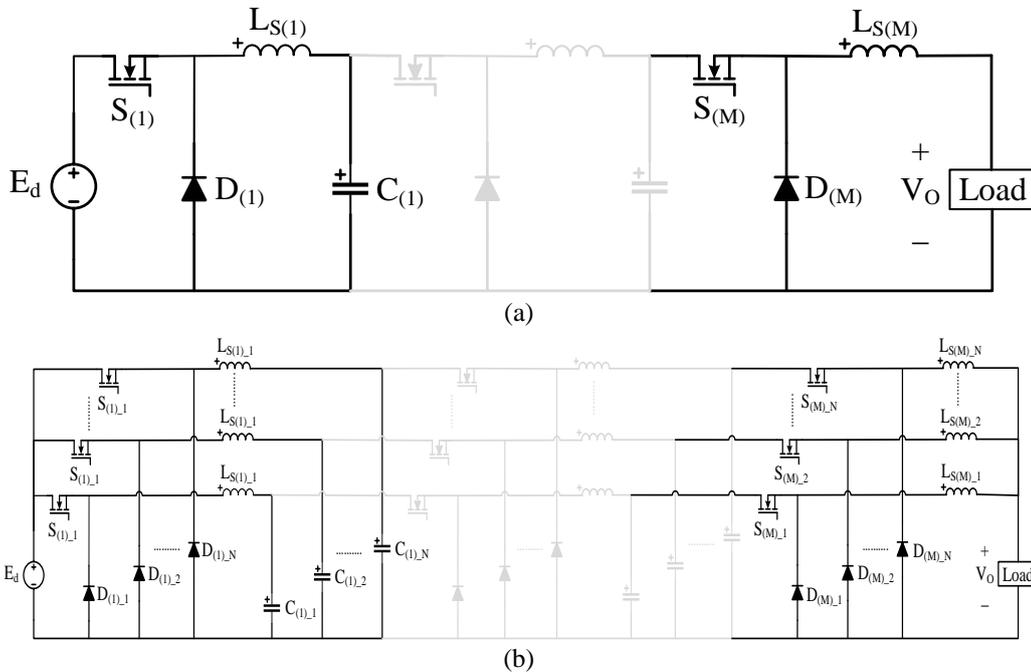


Figure 2. (a) conventional cascade single-phase DC-DC buck power converter, (b) conventional cascade multiphase (CCM) DC-DC buck power converter

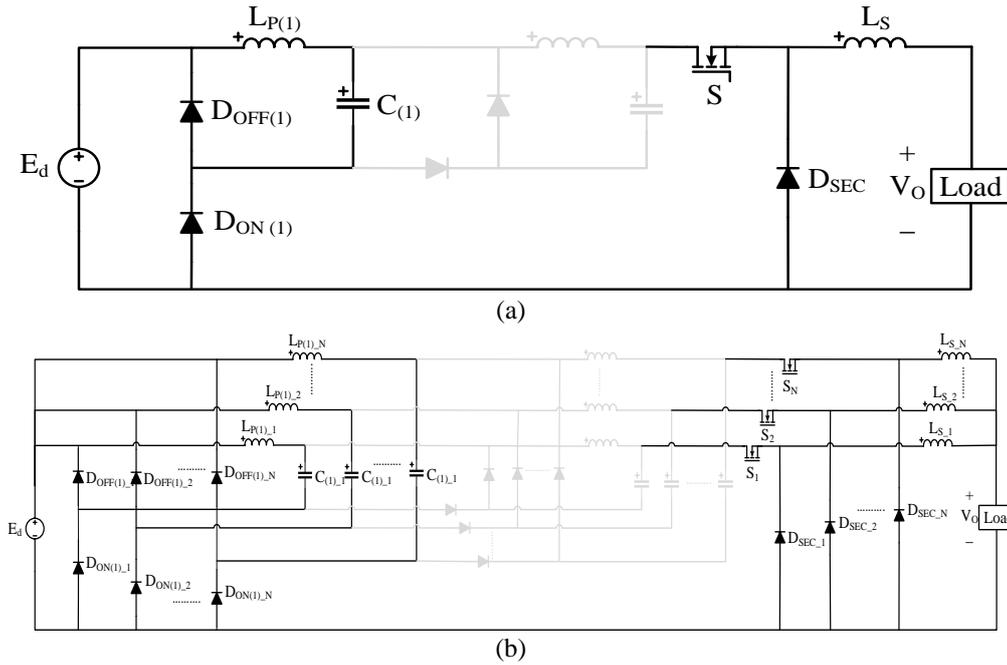


Figure 3. (a) simplified cascade single-phase DC-DC buck power converter, (b) simplified cascade multiphase (SCM) DC-DC buck power converter

2.4. New simplified cascade multiphase (NSCM) DC-DC buck power converters

Since the duty cycle of switches per phase is equal, all average voltages of capacitors on Figure 3(b) will be equal. When the voltages are identical, all capacitors that are installed in parallel can be replaced with only a single capacitor. Consequently, all diodes D_{ON} , diodes D_{OFF} and inductors L_P can also be replaced with only a single component each. This simplified scheme is named as a new simplified cascade multiphase (NSCM) DC-DC buck power converter, which is shown in Figure 4. The voltage ratio of the new scheme is shown by (4),

$$\frac{V_o}{E_d} = N^{M-1} \alpha^M \tag{4}$$

where N is the number of phases. In this case, the maximum duty cycle is limited to $1/N$.

NSCM DC-DC buck power converters have switching mechanisms as follows:

- 1) When switch S_i is *ON* (other switches are *OFF*), all diodes D_{ON} and secondary diodes (excluding D_{SECi}) will be forward biased, meanwhile the remaining diodes will be reverse biased. Current will flow in two directions: from DC source E_d to inductor L_P and from diode D_{ON} to capacitor C . Then, both currents sum up and charge inductor L_{Si} , before reaching the load. Other secondary inductors will discharge their currents to the load.
- 2) When switch S_i is *OFF* (other switches are also *OFF*), all diodes D_{OFF} and secondary diodes will be forward biased. Diodes D_{ON} will be reverse biased. The load will only receive discharged currents from the secondary inductors. Both mechanisms will be repeated for other switches.

A comparison of components among conventional cascade multiphase DC-DC buck power converters/CCM (Figure 2 (b)), simplified cascade multiphase DC-DC buck power converters/SCM (Figure 3(b)), and the proposed cascade multiphase DC-DC buck power converters/NSCM (Figure 4) is represented in Table 1. The proposed converter has the least number of components compared to two other cascade multiphase topologies. It should be noted that this converter is the dual of the converter that has been proposed in [26].

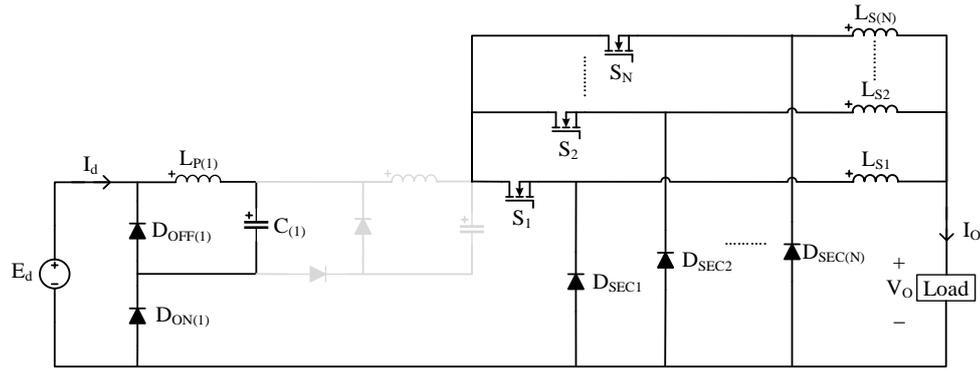


Figure 4. New simplified cascade multiphase (NSCM) DC-DC buck power converter

Table 1. Comparison of number of components in each cascade multiphase converter (2-cascade *N*-phase)

Converter Name	No. of Components			
	Inductors	Capacitors	Diodes	Switches
CCM	2 <i>N</i>	<i>N</i>	2 <i>N</i>	2 <i>N</i>
SCM	2 <i>N</i>	<i>N</i>	3 <i>N</i>	<i>N</i>
NSCM	<i>N</i> +1	1	<i>N</i> +2	<i>N</i>

3. CONVERTER ANALYSIS

This section discusses the output voltage analysis of the cascade multiphase DC-DC buck power converters topologies, which are conventional cascade multiphase (CCM), simplified cascade multiphase (SCM), and the new simplified cascade multiphase (NSCM) DC-DC buck power converters. The discussion is then followed by deriving the conduction losses of NSCM DC-DC buck power converters. This section ends after the optimization of the number of cascades and the number of phases desired for the NSCM DC-DC buck power converter, based on conduction losses.

3.1. Output voltage analysis

All output voltage equations in section 2 have been derived by neglecting voltage drops across the diodes, inductors, and switching devices. In fact, the voltage drops across switching devices, diodes, and inductors will reduce the maximum output voltage that can be obtained.

In the derivation of average output voltage, it is assumed that the voltage drop across the transistor during conduction can be represented as (5)

$$v_Q = V_Q + R_Q i_Q \tag{5}$$

and the voltage drop across the diode during conduction as (6)

$$v_D = V_D + R_D i_D \tag{6}$$

where V_Q is the on-state drop voltage of the active switch, R_Q is the on-state drain-to-source resistance, i_Q is the current flowing in the switch, V_D is diode forward voltage, R_D is the diode internal resistance and i_D is the current flowing in the diode. The resistances of inductors are assumed the same as R_L . Note that all capacitors are assumed ideal with no parasitic components.

By using the state-space averaging method, the average output voltage under continuous conduction mode can be determined. The results are shown in Table 2, where it is assumed that the number of stages is equal to two. This concept can be extended easily for cascaded number more than two.

Output voltage expressions in Table 2 are then plotted in Figure 5. It is assumed that the active switching devices are MOSFETS (model FCH023N65S3). According to the datasheet, the resistance of FCH023N65S3 is 18 mΩ. All diodes are model MUR1560G with a constant voltage drop of 0.6 V and a resistance of 18.4 mΩ. Inductor resistances are assumed equal to 30 mΩ. It is assumed that the DC input voltage is constant at 312 V.

Figure 5 shows the output voltage as a function of output current. As the load increases, NSCM converter has higher steepness compared to SCM and CCM, indicating that NSCM produces high conduction

losses. This steepness decreases as the number of phase decrease. The average output voltage of NSCM decreases faster than the others because it was assumed that all inductors are the same. In practice, the current rating of the primary inductor (L_p) of NSCM must be lower and, therefore, has the smallest resistance.

Based on the same voltage ratio, Figure 6 shows that NSCM has smaller duty cycle compared to two other figures. As the number of phases increases, the maximum duty cycle decreases. Fortunately, a small duty cycle is enough to achieve the desired low output voltage. This small duty cycle can be increased by adjusting the switching technique, which is left for future investigation.

Table 2. Output voltage expressions of CCM, SCM, and NSCM

Converter Name	Output Voltage Expression
CCM	$V_o = \alpha^2 E_d - (1 - \alpha^2) V_D - (\alpha^2 + \alpha) V_Q$ $-I_o \frac{[(\alpha^2 + 1)R_L + (\alpha^3 + \alpha)R_Q + (1 - \alpha + \alpha^2 - \alpha^3)R_D]}{N}$
SCM	$V_o = \alpha^2 E_d - (1 + \alpha - 2\alpha^2) V_D - \alpha V_Q$ $-I_o \frac{[(\alpha^2 + 1)R_L + \alpha R_Q + (1 - \alpha^2)R_D]}{N}$
NSCM	$V_o = N\alpha^2 E_d - (1 + \alpha - 2N\alpha^2) V_D - \alpha V_Q$ $-I_o \frac{[(N\alpha^2 + 1)R_L + \alpha R_Q + (1 + N\alpha^2 - 2N^2\alpha^3)R_D]}{N}$

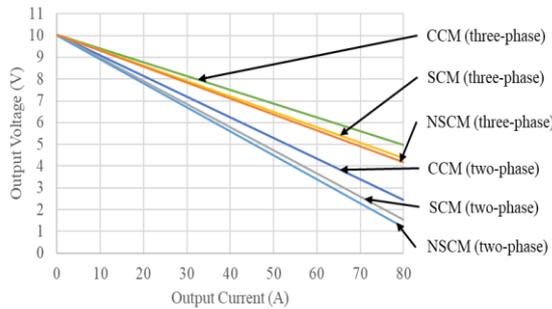


Figure 5. Output voltage as a function of output current

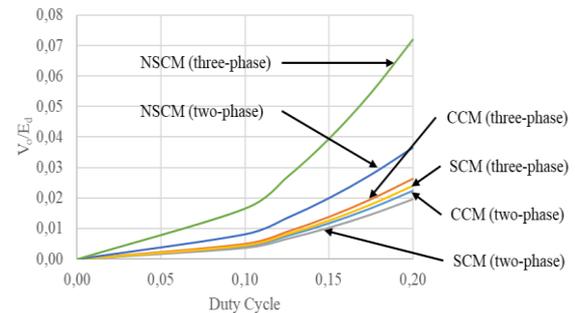


Figure 6. Voltage ratio as a function of duty cycle

3.2. Converter power loss analysis

3.2.1. Conduction losses

The output power of a DC-DC buck power converter is mentioned in (7)

$$P_o = V_o I_o \quad (7)$$

where P_o is the output power, V_o is the average output voltage, and I_o is the average output current of the converter.

By using the output voltage expression in Table 2 and (7), the output power of the new simplified cascade multiphase (NSCM) DC-DC buck power converter, as shown in Figure 6, is (8)

$$P_o = N\alpha^2 E_d I_o - (1 + \alpha - 2N\alpha^2) V_D I_o - \alpha V_S I_o$$

$$-I_o^2 \frac{[(N\alpha^2 + 1)R_L + \alpha R_S + (1 + N\alpha^2 - 2N^2\alpha^3)R_D]}{N} \quad (8)$$

The input current (I_d) of the DC-DC power converter is written in (9)

$$I_d = N\alpha^2 I_o \quad (9)$$

By substituting (9) into (8), the (10) is obtained

$$P_o = E_d \dot{i}_d - (1 + \alpha - 2N\alpha^2) V_D I_o - \alpha V_S I_o - I_o^2 \frac{[(N\alpha^2 + 1)R_L + \alpha R_S + (1 + N\alpha^2 - 2N^2\alpha^3)R_D]}{N} \quad (10)$$

The power loss (P_{loss}) of the converter is the difference between the input and output power, which is shown in (11)

$$P_{loss} = P_i - P_o \quad (11)$$

Meanwhile, the converter input power (P_i) is mentioned in (12)

$$P_i = E_d I_d \quad (12)$$

By using (10)-(12), the (13) is obtained

$$P_{loss} = (1 + \alpha - 2N\alpha^2) V_D I_o + \alpha V_S I_o + I_o^2 \frac{[(N\alpha^2 + 1)R_L + \alpha R_S + (1 + N\alpha^2 - 2N^2\alpha^3)R_D]}{N} \quad (13)$$

The (13) shows that voltage drops across the inductors, diodes, and power switches cause the conduction losses to occur. Figure 7 plots the comparison of conduction losses between CCM, SCM, and the NSCM. NSCM has higher conduction losses compared to two other cascade multiphase topology, due to larger number of used diodes. In practice, the resistance of the primary inductors (L_p) of the proposed converter is lower than the others because the current rating is higher and the required inductance is smaller. Thus, the total conduction losses will be just the same as the others, or even lower.

3.2.2. Switching losses

Switching losses of the converter depend on some parameters, which are: input voltage (E_d), switching frequency (f_s), current flow on the switch (I_{sw}), and the internal switch characteristics (rise time/ t_r and turn-off crossover time/ t_{cf}).

Based on the literature review [27], [28], the switching losses equation for CCM, SCM and NSCM is shown in (14). In this expression, K_1 is the sum of t_r and t_{cf} , while K_2 is the sum of current-linear dependent rise time and current-linear dependent turn-off crossover time.

$$P_{sw} = \frac{1}{2} E_d f_s (K_1 I_{sw} + K_2 I_{sw}^2) \quad (14)$$

The (14) shows that all DC-DC buck power converters have the same switching losses.

3.3. Converter optimization

Comparison between the number of phases and cascades is done to find the optimum topology of the proposed converter. Table 3 shows the variants to be optimized. The optimization is done based on the requirements of the converter as in Table 4, with the specification of the components in Table 5.

The graph in Figure 8 shows the comparison of conduction losses in each variation of cascades and phases under full load and half load conditions. As the number of cascades increases, the losses also increase. Meanwhile, when the number of phases increases, the losses decrease. Amongst the variants, the 2-cascade 4-phase topology has the lowest conduction losses. This topology will then be experimented further.

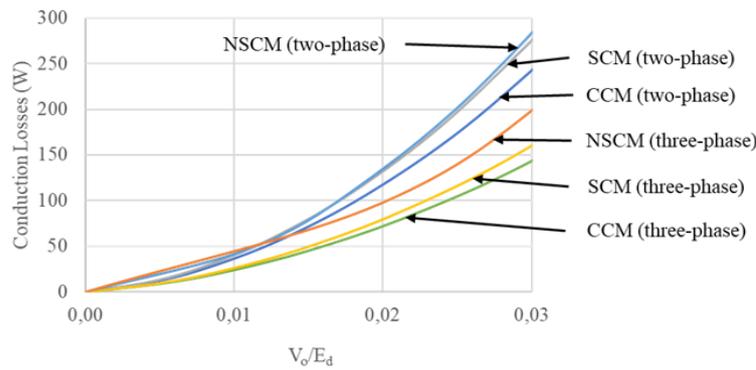


Figure 7. Conduction losses

Table 3. Six variations to find the optimum topology

No.	No. of Cascades	No. of Phases
1.		2
2.	2	3
3.		4
4.		2
5.	3	3
6.		4

Table 4. Converter specifications

No.	Converter' Specification	Value
1.	Input Voltage	(311.127 ± 10%) V
2.	Output Voltage	10 V
3.	Output Current	50 A
4.	Output Power	500 W

Table 5. Converter components

Components	Value
Inductors	$L = 1 \text{ mH}; R_L = 30 \text{ m}\Omega$
Switches	$R_S = 18 \text{ m}\Omega$
Diodes	$V_D = 0.85 \text{ V}; R_D = 1.84 \text{ m}\Omega$
Capacitor	$470 \mu\text{F}; 400 \text{ V}_{DC}$
Switching Frequency	10 kHz

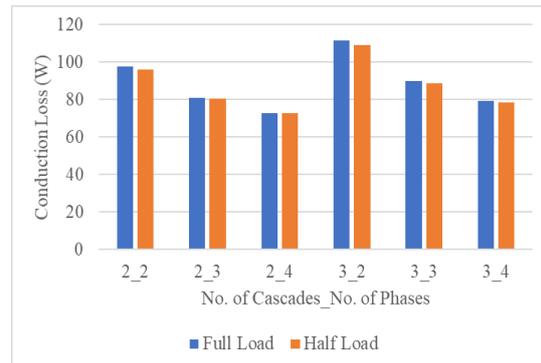


Figure 8. Conduction losses of the six analyzed variants

4. EXPERIMENTAL RESULTS

The chosen schematic design from section 3 can be seen in Figure 9. This schematic is then experimented, with the setup as shown in Figure 10. The primary inductor (L_p) has an inductance of 2.6 mH with an internal resistance of 0.9 Ω , while the secondary inductor on each phase (L_{S1}, L_{S2}, L_{S3} , and L_{S4}) is 1.03 mH with an internal resistance of 0.5 Ω . MOSFETs (FCH023N65S3) with an internal resistance of 18 m Ω are used as active switches. To drive the MOSFETs, four drivers model TLP350 are used. Ultrafast diodes MUR1560G are used in the topology, which has 0.6 V as the drop voltage with an internal resistance of 18.4 m Ω . The switching frequency of 10 kHz is used in all the experiments. The input voltage was downscaled to 100 V, with fixed aluminum resistors as the load resistance. No attempts have been done to select better inductors to improve converter performance.

Figure 11 shows the voltage ratio of the proposed converter, in which the measured results are very close to the calculations. Figure 12 shows the output voltage as a function of output current, which is obtained from changing the load resistance on fixed duty cycle of 0.125. Measured results are also close to the calculation results. Figure 13 shows the conduction losses of the converter as the function of the converter voltage ratio. Overall, these figures have shown that the calculation obtained from the formulas in Section 3 have been proven right. The measured results have small differences with the calculation ones due to inaccuracy of measuring inductors' resistance. Figure 14 shows output current waveform with the ripple waveform. These figures prove the converter claim for large output current with low output ripple.

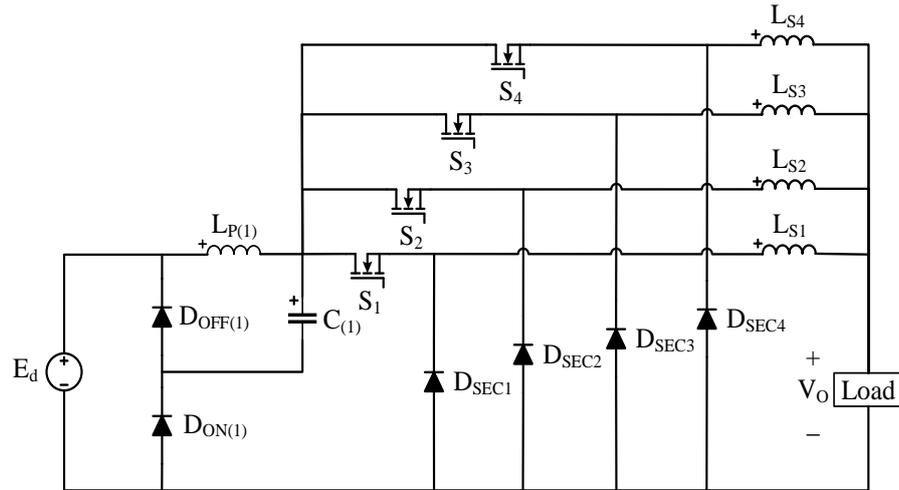


Figure 9. Two-cascade four-phase schematic design

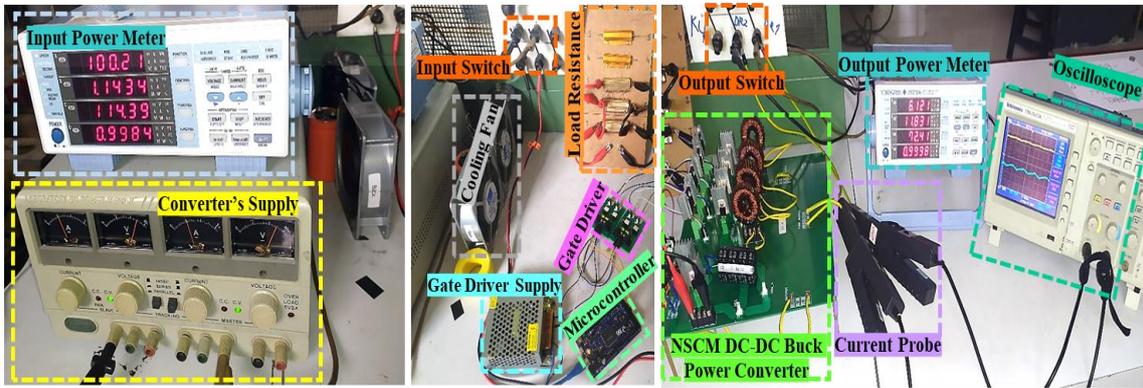


Figure 10. Experimental setup

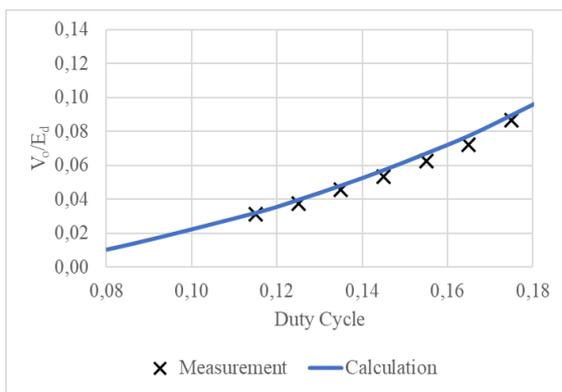


Figure 11. Voltage reduction ratio as a function of duty cycle

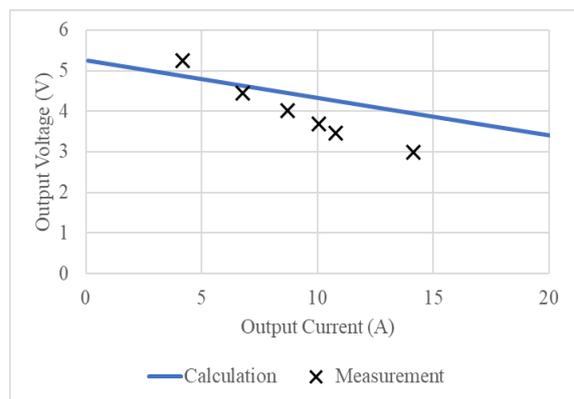


Figure 12. Output voltage as a function of output current

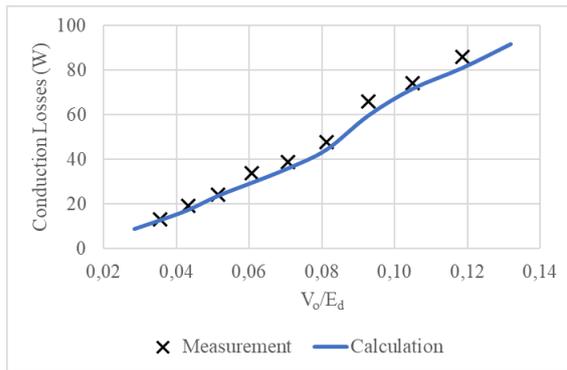


Figure 13. Conduction losses as a function of voltage ratio

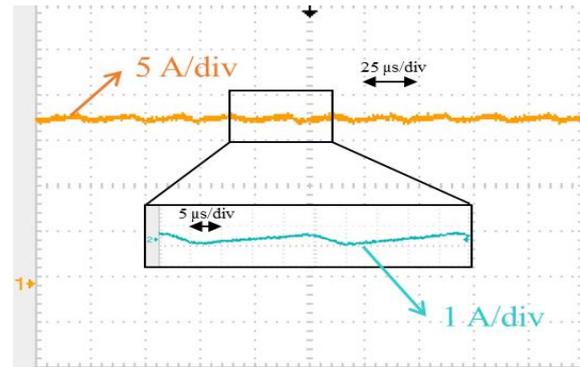


Figure 14. Output current waveform (orange-colored) with the enlarged output ripple waveform (blue-colored)

5. CONCLUSION

A new simplified cascade multiphase DC-DC buck power converter for low voltage large current applications has been proposed in this paper. The proposed converter has very high voltage reduction ratio with smaller number of components. Output voltage analysis, useful to estimate the conduction losses, is also presented. Even though conduction losses of the proposed converter are higher than the conventional cascade multiphase DC-DC buck power converter, the switching losses is lower. Optimum number of cascades and number of phases is presented in this paper. Experimental results showing the basic performance have been included. Selection of the optimal components for the proposed converters is under investigation.

ACKNOWLEDGEMENTS

The authors wish to thank the Korea Midland Power Company for partial funding of this research. Experimental facilities provided by the School of Electrical Engineering and Informatics, Institute of Technology Bandung, are greatly appreciated.

REFERENCES

- [1] R. Swarnkar, H. K. Verma and R. N. Patel, "Comparative analysis of isolated and non-isolated bi-directional DC-DC converters for DC microgrid," *2019 3rd International Conference on Recent Developments in Control, Automation & Power Engineering (RDCAPE)*, 2019, pp. 557-562, doi: 10.1109/RDCAPE47089.2019.8979027.
- [2] M. Amiri, H. Farzanehfard, and E. Adib, "A nonisolated ultrahigh step down DC-DC converter with low voltage stress," *IEEE Transactions on Industrial Electronics*, vol. 65, no. 2, pp. 1273-1280, 2018, doi: 10.1109/TIE.2017.2733478.
- [3] R. Aguilar-Najar, F. Perez-Pinal, G. Lara-Salazar, C. Herrera-Ramirez and A. Barranco-Gutierrez, "Cascaded buck converter: A reexamination," *2016 IEEE Transportation Electrification Conference and Expo (ITEC)*, 2016, pp. 1-5, doi: 10.1109/ITEC.2016.7520211.
- [4] R. Tymerski and V. Vorperian, "Generation, classification and analysis of switched-mode DC-to-DC Converters by the use of converter cells," *INTELEC '86 - International Telecommunications Energy Conference*, 1986, pp. 181-195, doi: 10.1109/INTLEC.1986.4794425.
- [5] D. Maksimovic, and S. Cuk, "Switching converters with wide DC conversion range," *IEEE Transactions on Power Electronics*, vol. 6, no. 1, pp. 151-157, 1991, doi: 10.1109/63.65013.
- [6] P. C. Heris, Z. Saadatizadeh, and N. Rostami, "Transformerless quadratic-based high stepdown DC-DC converter with wide duty cycle range," *IET Power Electronics*, vol. 12, no. 3, pp. 368-382, 2019, doi: 10.1049/iet-pel.2018.5504.
- [7] Z. Saadatizadeh, P. C. Heris, M. Sabahi, and E. Babaei, "Two new transformerless high step-down DC-DC converters," *IET Power Electronics*, vol. 12, no. 5, pp. 1205-1219, 2019, doi: 10.1049/iet-pel.2018.5484.
- [8] A. Asghari, "Ultra-high step-down ZVS synchronous buck converter with low switch voltage stress," *IET Power Electronics*, vol. 13, no. 10, pp. 1-10, 2020, doi: 10.1049/iet-pel.2019.1113.
- [9] D. Cheshmdehnam, E. Adib and H. Farzanehfard, "Structure improvement of active-clamp to achieve high step-down conversion," *2016 24th Iranian Conference on Electrical Engineering (ICEE)*, 2016, pp. 670-675, doi: 10.1109/IranianCEE.2016.7585606.
- [10] Y. T. Yau and K. I. Hwu, "Ultra high step-down converter," *2014 International Power Electronics Conference (IPEC-Hiroshima 2014 - ECCE ASIA)*, 2014, pp. 3392-3396, doi: 10.1109/IPEC.2014.6869983..
- [11] T. Wu, "The origin of converters," *2013 1st International Future Energy Electronics Conference (IFEEC)*, 2013,

- pp. 611-617, doi: 10.1109/IFEEC.2013.6687578.
- [12] M. K. Kazimierczuk, *Pulse-Width Modulated DC-DC Power Converters*. 2015.
- [13] M. Amiri, and H. Farzanehfar, "A high-efficiency interleaved ultra-high step-down DC-DC converter with very low output current ripple," *IEEE Transactions on Industrial Electronics*, vol. 66, no. 7, pp. 5177–5185, 2019, doi: 10.1109/TIE.2018.2869348.
- [14] C. T. Pan, C. F. Chuang, C. C. Chu, and H. C. Cheng, "A novel transformer-less interleaved four-phase high step-down DC converter with low switch voltage stress," *2014 International Power Electronics Conference (IPEC-Hiroshima 2014 - ECCE ASIA)*, vol. 61, no. 10, pp. 3379–3385, 2014, doi: 10.1109/IPEC.2014.6869981.
- [15] O. Kirshenboim, and M. M. Peretz, "High-efficiency nonisolated converter with very high step-down conversion ratio," *IEEE Transactions on Power Electronics*, vol. 32, no. 5, pp. 3683–3690, 2017, doi: 10.1109/TPEL.2016.2589321.
- [16] M. Esteki, B. Poorali, E. Adib, and H. Farzanehfar, "Interleaved Buck converter with continuous input current, extremely low output current ripple, low switching losses, and improved step-down conversion ratio," *IEEE Transactions on Industrial Electronics*, vol. 62, no. 8, pp. 4769–4776, 2015, doi: 10.1109/TIE.2015.2397881.
- [17] P. A. Dahono, S. Riyadi, A. Mudawari, and Y. Haroen, "Output ripple analysis of multiphase DC-DC converters," *Proceedings of the IEEE 1999 International Conference on Power Electronics and Drive Systems. PEDS'99* (Cat. No.99TH8475), 1999, pp. 626-631 vol.2, doi: 10.1109/PEDS.1999.792732.
- [18] M. Z. Hossain, N. A. Rahim, and J. a/l Selvaraj, "Recent progress and development on power DC-DC converter topology, control, design and applications: A review," *Renewable & Sustainable Energy Reviews*, vol. 81, no. July 2017, pp. 205–230, 2018, doi: 10.1016/j.rser.2017.07.017.
- [19] M. Ishwarya, and R. Dhanalakshmi, "Investigations on multiphase modified interleaved buck converters for high step down voltage," *2017 International Conference on Innovative Mechanisms for Industry Applications (ICIMIA)*, 2017, pp. 491-496, doi: 10.1109/ICIMIA.2017.7975663.
- [20] Taufik, R. Prasetyo, D. Dolan, and D. Garinto, "A new multiphase multi-interleaving buck converter with bypass LC," *IECON 2010 - 36th Annual Conference on IEEE Industrial Electronics Society*, 2010, pp. 291-295, doi: 10.1109/IECON.2010.5675013.
- [21] A. R. Babu, Raghavendiran, B. P. Gladly, and G. T. S. Rajan, "Novel non-isolated high step-down three phase interleaved DC-DC converter with capacitor voltage division techniques," *2017 International Conference on Circuit, Power and Computing Technologies (ICCPCT)*, 2017, pp. 1-7, doi: 10.1109/ICCPCT.2017.8074399.
- [22] B. Kiriskanen, and H. F. Ugurdag, "Cost-benefit approach to degradation of electrolytic capacitors," *2014 Reliability and Maintainability Symposium*, 2014, pp. 1-6, doi: 10.1109/RAMS.2014.6798436.
- [23] A. Lahyani, P. Venet, G. Grellet, and P. J. Viverge, "Failure prediction of electrolytic capacitors during operation of a switchmode power supply," *IEEE Transactions on Power Electronics*, vol. 13, no. 6, pp. 1199–1207, 1998, doi: 10.1109/63.728347.
- [24] E. Aeloiza, Jang-Hwan Kim, P. Enjeti and P. Ruminot, "A real time method to estimate electrolytic capacitor condition in PWM adjustable speed drives and uninterruptible power supplies," *2005 IEEE 36th Power Electronics Specialists Conference*, 2005, pp. 2867-2872, doi: 10.1109/PESC.2005.1582040.
- [25] V. A. Sankaran, F. L. Rees, and C. S. Avant, "Electrolytic capacitor life testing and prediction," *IAS '97. Conference Record of the 1997 IEEE Industry Applications Conference Thirty-Second IAS Annual Meeting*, 1997, pp. 1058-1065 vol.2, doi: 10.1109/IAS.1997.628992.
- [26] P. A. Dahono, "Simplified cascade multiphase dc-dc boost power converters for high voltage-gain and low-ripple applications," *International Journal of Power Electronics and Drive Systems (IJPEDS)*, vol. 12, no. 1, pp. 273–285, 2021, doi: 10.11591/ijpeds.v12.i1.pp273-285.
- [27] P. A. Dahono, Y. Sato, and T. Kataoka, "Analysis of switching and conduction losses in hysteresis current-controlled inverters," *IEEE Transactions on Industry Applications*, vol. 113, no. 10, pp. 1216–1225, 1993, doi: 10.1541/ieejias.113.1216.
- [28] J. H. Rockot, "Losses in high-power bipolar transistors," in *IEEE Transactions on Power Electronics*, vol. PE-2, no. 1, pp. 72-80, Jan. 1987, doi: 10.1109/TPEL.1987.4766334.

BIOGRAPHIES OF AUTHORS



Anand Bannet Ganesen, was born in Medan, Indonesia in 1998. He received his bachelor's degree in Electrical Engineering from Institut Teknologi Bandung, Indonesia, in 2020. His fields of research are power electronics and drive systems. He has interest in power systems and power qualities. At present, he is a graduate fast-track student at Institut Teknologi Bandung, Indonesia.



Nungky Prameswari, was born in Bandung, Indonesia in 1996. She received the bachelor's degree in Electrical Engineering in 2018 from Universitas Diponegoro, Indonesia. Her fields of research are power electronics and drive systems. She has interest in power engineering and power electronics. At present, she is a graduate student at the Institut Teknologi Bandung, Indonesia.



Falah Kharisma Nuraziz, was born in Bandung, Indonesia in 2000. His field of research is power electronics. He has an interest in electrical power engineering and power electronics. At present, he is an undergraduate student at the Institut Teknologi Bandung Indonesia.



Jihad Furqani, was born in Malang, East Java, Indonesia in 1990. He received B.S. degree in Electrical Power Engineering from Bandung Institute of Technology in 2012. He received M.S. degree in Electrical Engineering from Bandung Institute of Technology in 2013. He received Dr. Eng degree in Electrical and Electronic Engineering from Tokyo Institute of Technology in 2019. He has been studying multilevel and multiphase motor drive, noise reduction in switched reluctance motor, power electronic converter for renewable energy application, and electric motor for vehicle application. He was Visiting Researcher of University of Akron in 2017 and Visiting Lecture of Tokyo Institute of Technology in 2021. Currently, he is lecturer in Electrical Power Engineering, School of Electrical Engineering and Informatics, and National Center for Sustainable Transportation Technology, Bandung Institute of Technology. He received IEEE Star Reviewer in 2019.



Arwindra Rizqiawan, He received his bachelor and master's degree from Insitut Teknologi Bandung, Indonesia, in 2006 and 2008, respectively, and doctoral degree from Shibaura Insitute of Technology, Japan, in 2012, all in the field of electrical engineering. His current main interests are power engineering, power electronics, and renewable energy. He is currently serving as assistant professor in School of Electrical Engineering and Informatics, Institut Teknologi Bandung, Indonesia. He is certified professional engineer (IPM) in Indonesia by the Institution of Engineers Indonesia (PII), and ASEAN Engineer by ASEAN Engineering Register.



Pekik Argo Dahono, He got the Insinyur (Ir) degree, from the Institut Teknologi Bandung, Indonesia, in 1985, the Master and Doctor Engineering degrees from Tokyo Institute of Technology, Japan, in 1992 and 1995, respectively, all in electrical engineering. He is registered as a Professional Engineer in Indonesia and ASEAN. He is a senior member of IEEE. He is cofounder of Indonesia Smart Grid Initiatives and Indonesia Power Quality Initiatives. At present, he is a professor in the School of Electrical Engineering and Informatics, Institut Teknologi Bandung. He has interest in power electronics, power systems, and power quality.