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The performances of partial shading adjuster for improving photovoltaic emulator

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ABSTRACT

A photovoltaic (PV) emulator (PVE) is essential equipment for the research and diagnostic of PV generation. It is a convenient, highly efficient, and low-cost approach when compared to controllable light sources. Nonetheless, the implementation of the partial shading capability in a PVE is highly limited in terms of efficiency, computation burned, number of power converters, and flexibility to change in the ambient condition. This paper proposes a partial shading adjuster for a PVE that can overcome the aforementioned limitations. The adjuster is applicable to the conventional PVE since it is based on an algorithm that can be added to the controller of the PVE. By adding the adjuster, the conventional PVE can emulate partial shading. The partial shading adjuster is added into a PVE that uses the direct referencing control strategy with the buck controller regulated by the proportional-integral controller. The results show that the PVE maintains its accuracy and produces a stable output voltage and current during the load changes when the adjuster is added. In conclusion, the proposed partial shading adjuster able to improve the capability of the PVE by providing a real-time partial shading capability.

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1. INTRODUCTION

Photovoltaic (PV) emulator (PVE) has become essential equipment for the research and development of the PV generation-based system. The common control strategy for the PVE is the direct referencing method. The control strategy consists of the standard PV model (PVM), proportional-integral (PI) controller, and buck converter. It is a simple control strategy that does not require any additional algorithm to operate since the operating point is determined by the transient response of the PVE. Nevertheless, this PVE does not consider the partial shading effect.

Partial shading occurs when the PV modules do not receive similar irradiance. This is due to shadows cast by clouds or buildings or the PV modules is not cleaned properly which leads to the dust layer. Computing partial shading is a difficult task especially if it is implemented into the PVE. As a result, the majority of the research does not include partial shading capability. There are 2 approaches that can be used to emulate partial shading for the PVE, which are the hardware and software approaches. For the hardware approach, the amplifier circuit combined with the PV cell is a simple PVE with partial shading capability [1]. This method amplifies the current-voltage (I-V) characteristic of the PV cell, which allows high-power emulation at a lower cost. Nonetheless, the method is highly inefficient since it operates in the linear region

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and is limited to the type of PV cell used. Another method is to build the PVE based on the transistor [2]. This method uses the PVM electrical circuit model as a reference and the diode presented in the PVM is replaced with the transistor. Although this method is simple and it requires a low cost to build, it has several limitations. This method requires multiple power sources, has low efficiency, and has a high ripple when emulating the certain region on the I-V characterised curve. Another method used to emulate partial shading is to combine several PVE [3]. This approach is suitable for a simple series configuration since the digital controller used needs to compute multiple PVM in real-time, which burden the digital controller and may affect the performance of the PVE. This method is also unable to emulate properly at a certain region of the I-V characteristic curve.

For the software approach, the control strategy is based on the common direct referencing method. The modification is done on the PVM to allow partial shading capability. If the PVM output is added together based on the PVM input, these PVMs are able to produce partial shading [4], [5]. This control strategy is called the PVM adder method. It is nearly similar to the multiple PVE method with the advantage of using only a single power converter. However, the digital controller stills need to compute multiple PVM to emulate the partial shading. To reduce the computation burden, the computation of the PVMs is conducted offline and the data is collected and placed in the look-up table (LUT) for the real-time operation [6]–[9]. Although this method solves the computation problem, it has a flexibility problem. This means that it is difficult to change the irradiance and temperature during real-time operation since the I-V curve is generated externally during the offline period.

The literature shows that emulating partial shading is a difficult task. When it comes to PVE with the partial shading capability, Recently, the single-based PVM with partial shading capability is developed, which significantly reduce the processing burden [10]. Therefore, this model is suitable to solve the limitation faced by the PVM adder method. This paper presents a new method for the PVE with the ability to produce partial shading based on the software approach. It uses a new adjuster that can produce partial shading by only adjusting the input of the standard PVM. This PVM is based on the single diode model with a series resistor or 1D1R model. The common direct referencing method is chosen as the control strategy. The continuous current mode buck converter is selected for the PVE and controlled using the common PI controller. The PVE is simulated using the MATLAB/Simulink simulation platform. The next section discusses the control strategy used in the PVE. Then, the design of the MPPT converter is shown in the following section. The results and discussion are covered in the next section. The paper ends with a conclusion.

2. PROPOSED PHOTOVOLTAIC EMULATOR

The PVM used for the PVE is a simplified version of the PVM adder method. Instead of using multiple standard PVM, the irradiance (G), module temperature (T), and PV voltage (V_{pv}) are adjusted so that only one standard PVM able to be represented as multiple standard PVM for the partial shading. The adjustment algorithm is called the partial shading adjuster, as shown in Figure 1. During the partial shading, there are multiple G and T inputs for the PVM (G_{vec}) and T_{vec} . The V_{pv} input for the PVM is higher than the open-circuit voltage, V_{oc} , of a single PV module since it is V_{pv} in series (V_{pv_sr}) . The function of the partial shading adjuster is to select a suitable G and $T(G_i)$ and T_i for the one standard PVM. Then, the V_{pv} is reduced to for the use of the one PVM (V_{pv}) needs to be equal to or lower than V_{oc} for a valid PVM). The new V_{pv} is called adjusted V_{pv} , V_{pv_adj} . The final output for the PVM is the series PV current, I_{pv_sr} .

The I_{pv_sr} becomes the reference current, I_{ref} , for the PI controller. The I_{ref} is compared with the output current, I_o , from the buck converter to obtain the error for the PI controller. The PI controller uses the error to produce the corresponding duty cycle, d. The pulse width modulation, PWM, use the d and produce the switching pulse, sp, for the buck converter. The buck converter produces the I_o according to the I_{ref} . The corresponding output voltage, V_o , from the buck converter is sent to the partial shading adjuster to become V_{pv_sr} .

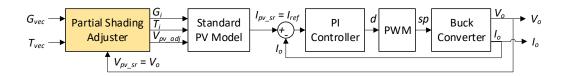


Figure 1. The block diagram of the proposed PVE with partial shading capability by adding the partial shading adjuster

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2.1. Photovoltaic model

The PVM consists of two components, which are the standard PVM and the partial shading adjuster. The purpose of the standard PVM is to compute a standard I-V characteristic curve without partial shading. While the function of the partial shading adjuster is to receive multiple irradiance and temperature input, select one irradiance and one temperature, and sent it to a single standard PVM. The selection allows the production of the partial shading characteristic.

2.1.1. Standard photovoltaic model

The mathematical characteristic equation for the standard PVM is presented using (1), which calculate the photovoltaic current, I_{pv} [11], [12]. It is based on the single diode model with a series resistor, which is widely used in the PVE application due to the low computation burden [13]–[15]. Commonly, the parallel resistor, R_p , is considered in the single diode PVM. Nonetheless, it is not applicable for the partial shading adjuster [10]. The Amerisco 80J-B PV module is chosen for the simulation, in which the number of PV cells in a module, n_s , is 72 [16]. The K_{tv} and temperature coefficient short circuit current, K_{ti} , are -0.16 V/°C and 0.002 A/°C, respectively. The PV module has the maximum power point voltage and current at the STC (V_{np_stc} and I_{np_stc}) at 35.8 V and 2.23 A, respectively. The open-circuit voltage and short circuit current at the STC (V_{oc_stc} and I_{sc_stc}) are 44.4 V and 2.32 V, respectively.

$$I_{pv} = I_{ph} - I_s \left[exp\left(\frac{V_{pv} + I_{pv}R_s}{AV_t}\right) - 1 \right]$$

$$\tag{1}$$

Where I_{ph} is the photo-generation current, V_{pv} is the PV voltage, I_s is the saturated current, V_t is the thermal voltage, and A is the ideality factor.

2.1.2. Partial shading adjuster

The partial shading adjuster is an essential part of the PVE to produce partial shading. The algorithm starts by loading the parameters of the PV module and constants, as presented in Figure 2. The I_{ph} for the first G and T (G_I and T_I) is calculated. Then, the V_t and I_s are evaluated, respectively. Then, the I_{ph} for the following G and T are calculated until the n_s is achieved. At the same time, the critical PV voltage, V_{pv_cri} , is determined using (2) and (3). Then, the sum of PV voltage, V_{pv_sum} , is calculated using (4). The V_{pv_sr} is compared with the V_{pv_sr} . If the V_{pv_sr} is smaller than V_{pv_sum} , the V_{pv_sum} is updated by subtracted with V_{pv_cri} and the V_{pv_adj} is calculated using (5). This condition occurs when the operating point of the PVE is located on the I-V characteristic curve for the highest G.

$$V_{pv\ cri}(i) = ABV_t(T(i)) - I_{ph}(G(i), T(i))R_s$$

$$\tag{2}$$

$$B = In \left[\frac{I_{ph}(G(i), T(i)) - I_{ph}(G(i+1), T(i+1))}{I_{s}(T(i))} + 1 \right]$$
(3)

$$V_{pv_sum} = \sum_{i=1}^{i} [V_{pv_cri}(i)] + (i-1)^{i_{adj}} V_{pv_cri_adj}$$
(4)

$$V_{pv\ adj} = V_{pv\ sr} - V_{pv\ sum} \tag{5}$$

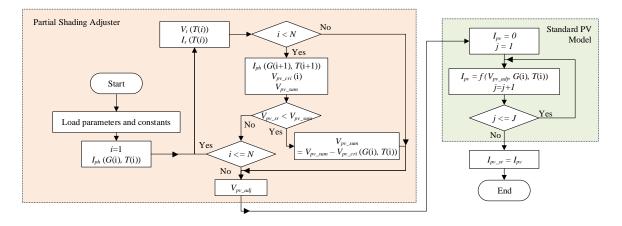


Figure 2. The flowchart of the PVM with the partial shading capability based on the partial shading adjuster method

If the V_{pv_sr} is equal to or larger than V_{pv_cri} , the operating point of the PVE is located on the I-V characteristic curve for the second-highest and lower G. In this condition, the algorithm checks whether the current partial shading adjusted, i, is equal to or lower than the N. If the condition is not true, this means that the computation is already at the lowest G and the V_{pv_adj} is calculated using (5). If the condition is true, this means that the computation is between the highest G and the lowest G. For this condition, the computation is repeated until the V_{pv_sr} is smaller than V_{pv_cri} .

After the computation of the partial shading adjuster is completed, the V_{pv_adj} , G_i , and T_i are produced and used by the standard PVM. Since the standard PVM is based on an implicit equation, the iteration method such as the Newton-Raphson method is needed. Using the Newton-Raphson method, (1) is iterated until the number of iterations for the standard PVM, J, is achieved. In the end, the I_{pv} becomes the I_{pv_sr} .

2.2. Buck converter

The PVE requires a power converter to work. The power converter used for the PVE is commonly the buck converter due to the low passive components requirement and high-power efficiency [6], [17], [18]. The equivalent circuit of the buck converter provided in Figure 3 contains parasitic resistors for a more realistic simulation. There are two parts in designing the buck converter, which is the design of the passive element and the controller for the buck converter.

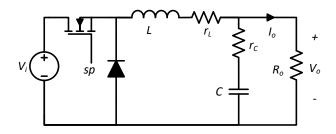


Figure 3. The topology of the buck converter with parasitic resistors

The design of the buck converter for the PVE is based on the conventional buck converter [19], [20]. The first parameter calculated is the input voltage, V_i using (6). The V_i is affected by the maximum V_{oc} , V_{oc_max} , and maximum d, d_{max} . Based on the simulation of the PVM, the V_{oc_max} is 195.6 V. While the d_{max} is set to 0.9. The calculated V_i is 217.3 V. Since the calculation does not consider the nonideality factor, a higher voltage needs to be chosen, which is 250 V.

$$V_{\rm i} = \frac{V_{oc_max}}{d_{max}} \tag{6}$$

The inductance, L, design is important to maintain continuous current operation. This is essential especially for the PI controller design since a different controller is needed if the buck converter operates in the discontinuous current mode. The V_{o_max} and the maximum output resistance, R_{o_max} , balance needs to be considered in the design. In the ideal condition, the R_{o_max} is infinity. Nonetheless, the buck converter cannot be controlled when the R_{o_max} is infinity [19]. Therefore, a certain limit is chosen, which is 700 Ω . This R_{o_max} is chosen because it covers the most area in the I-V characteristic curve. Based on the R_{o_max} , the output voltage corresponding to R_{o_max} , $V_{o_Ro_max}$, is obtained from the PVM. Using (7), the required L is 3 mH, which the switching frequency, f_s , is 20 kHz. The corresponding internal resistance of the inductor, r_L , is 0.9 Ω .

$$L = \frac{\left(1 - \frac{V_{o_Ro_max}}{V_i}\right) R_{o_max}}{2f_s} \tag{7}$$

The capacitance, C, is designed to operate the buck converter at the desired ripple factor. The chosen output voltage ripple factor, γ_{Vo} , for the PVE is 0.5% [21]. Based on (8), the C required is 36 μ F, which minimum D, d_{min} , is 0.01. The corresponding internal resistance of the capacitor, r_C , is 0.3 Ω .

$$C = \frac{1 - d_{min}}{8L\gamma V_O f_S^2} \tag{8}$$

To design the PI controller, the transfer function of the power converter is needed. The transfer function of the buck converter, $G_b(s)$, is needed, as provided in (9) [18], [22]. The $G_b(s)$ has the I_o as of the output since the current-controlled direct referencing method is used for this PVE. For the transfer function of the PI controller, $G_c(s)$, it can be presented as (10). Using the automatic tuning function provided by the MATLAB/Simulink, the proportional and integral gains (K_p and K_i) are 0.0001 and 39, respectively.

$$G_b(s) = \frac{\ell_o(s)}{d(s)} = \frac{V_i}{R_o} \frac{\frac{1}{LC}}{s^2 + \frac{1}{R_o C} s + \frac{1}{LC}}$$
(9)

$$G_c(s) = \frac{\hat{a}(s)}{\hat{e}_f(s)} = K_p + \frac{K_i}{s}$$
 (10)

3. RESULTS AND DISCUSSION

There is five performance analysis conducted for the proposed PVE. This performance analysis is conducted to ensure the direct referencing method control strategy able to cope with the partial shading adjuster modification. The accuracy, load, irradiance, stability, and efficiency tests are conducted using a resistive load. While the compatibility test is conducted using the MPPT converter as the load for the PVE, which the resistive load is removed from the simulation.

3.1. Accuracy test

The accuracy is an essential aspect when it comes to PVE. The PVE is considered accurate if the operating points fall on the I-V characteristic curve of the PV. Three types of accuracy tests can be conducted on the PVE. These can be either by comparing the V_o and I_o of the PVE with the V_{pv} and I_{pv} of the PVM [23], [24], PV module [25], or both PVM and PV module [11]. The comparison with the PV module evaluates the accuracy of the control strategy and PVM used in the PVE. While the comparison with the PVM only evaluates the accuracy of the control strategy used in the PVE. Since the paper focused on the accuracy of the control strategy and not the accuracy of the PVM, the V_o and I_o of the PVE are compared with the V_{pv} and I_{pv} of the PVM.

The comparison of the I-V characteristic curves produced by the PVE and PVM are shown in Figure 4. The result shows that the V_o and I_o produced by the PVE is on the I-V characteristic curve of PV. This show that the proposed PVE has high accuracy. The I-V characteristic of the PVE is in the discrete form because the accuracy of the PVE becomes lower if the variable load is sweep from zero to infinity. This is due to the slow transient response of the PVE. The PVE requires a certain time to achieve a steady-state. Therefore, the V_o and I_o of the PVE are recorded after the steady-state is achieved with multiple fixed loads. There is also no short-circuit (R_o is zero) and open-circuit (R_o is infinity) tests conducted on the PVE. This is because the buck converter cannot work properly during this condition. When the R_o is equal to zero, the V_o needs to be zero, which then produce zero I_o if a buck converter is used. However, the I_{pv} from the PVM is not zero when V_{pv} equals to zero, which means that the PVE fails to emulate the short-circuit test. If the R_o is infinity, the V_o cannot be regulated and operates near to V_i . A different type of converter is needed in order to operate at these points.

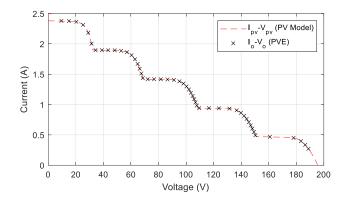


Figure 4. The comparison of the I-V characteristic curve of the PVM and PVE

3.2. Load test

The load test is conducted on the PVE to analyse the transience performance and stability during load changes. A good PVE can change to a new operating point without any oscillation at a fast rate. By referring to Figure 5 (a), the load starts at 30 Ω . At 0.2 s, the load is stepped up to 160 Ω for 0.2 s. Then the load is stepped down back to 30 Ω . The waveforms of the Vo and Io of the PVE are recorded in Figure 5 (a) and Figure 5 (b), respectively. The percentage overshoot, %OS, of the is observed and it is calculated using (11) [26]. A PVE needs to have zero or low %OS to avoid damaging the load or the components in the PVE. Another factor that needs to be considered is the settling time, ts, which is used to analyse the transient performance of the PVE, which is defined as the time taken for the Io to be within 2% of its steady-state value.

$$\%OS = \frac{I_{o(max)} - I_{o(ss)}}{I_{o(ss)}} \times 100\%$$
 (11)

Where $I_{o(max)}$ is the maximum output current overshoot and $I_{o(ss)}$ is the steady-state output current.

Based on Figure 5 (b), when the load is stepped down from 160 Ω to 30 Ω , there is a large I_0 overshoot occur. Using (11), the %OS is 147.7%. The large I_0 overshoot is caused by the C that try to maintain V_o after the load is stepped down. This overshoot cannot be avoided when the buck converter is used for the PVE. Nonetheless, the C is only 36 μ F and the I_o should be reduced quickly. However, the I_o waveform shows that it takes 43.4 ms to achieve a steady-state. The slow response is due to the constraint of the PI controller. To reduce the duration of the overshoot, a faster and more robust controller like fuzzy logic or shift controllers [20]. The result shows the t_s during the beginning of the operation is 44.1 ms. When the load is stepped up and down, the t_s is 80.5 ms and 43.4 ms, respectively. The results show that the t_s is faster when the load is stepped down compared to when the load is stepped up. The slow response during a high load condition is due to the limitation of the PI controller. The PI controller is designed at the lowest load to avoid unstable output. If the PI controller is designed at the higher load condition, the output becomes unstable when the load is low. Nonetheless, the PI controller alone does not affect the t_s for the PVE [20]. If the effect of PI controller alone is accounted, the t_s increases as the V_o increases. By referring to Figure 6, this relationship does not apply. This is because the control strategy used in the PVE affects the t_s for the PVE. Since the direct referencing control strategy is used for the PVE, the t_s becomes higher at the local and global MPP. The t_s becomes lower when the operation leaves the MPP and move to the constant voltage region. A robust PVE should maintain a constant t_s at the various operating point.

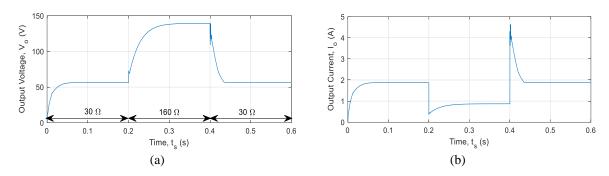


Figure 5. The time-domain response of the PVE when the load is stepped up from 30 Ω to 160 Ω at 0.2 s and stepped down from 160 Ω to 30 Ω at 0.4 s (a) output voltage waveform (b) output current waveform

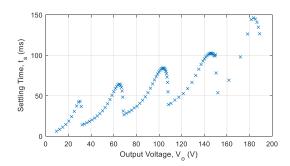


Figure 6. The settling time of the output voltage of the PVE at various load condition

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4. CONCLUSION

The PVM requires a high computation since it is based on an implicit equation that requires multiple iterations. To produce partial shading, several PVMs need to be used, which further increases the computation burden. Nonetheless, the partial shading adjuster allows partial shading capability with only a single PVM. This gives an advantage for the PVE because the PVE able to operate in real time and requires a low computation to work properly. The partial shading adjuster is a new concept and is not being tested in the PVE. Based on the simulation results, this adjuster is suitable for the PVE. The accuracy test shows the PVE can imitate the I-V characteristic curve produced by the PVM. This result not only shows combining the partial shading adjuster with the PVE has produced high accuracy results but also shows it is stable during the steady state operation. The result shows that the transient response becomes slower as the load increases. There is also a current spike that occurs during the load stepped down. A different power converter, closed-loop controller, and control strategy are needed to improve the PVE. In conclusion, the partial shading adjuster can work with the PVE since it is accurate. The transient response of the PVE requires improvement due to the slow response at a high load and a high current spike when the load decreases.

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