

Comparison of sliding mode controller application for buck-boost converter based on linear sliding surface

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ABSTRACT

In the utilization of photovoltaic (PV), the output voltage produced is unstable because the conditions of irradiation received by PV is not uniform. Therefore, a direct current (DC) voltage converter is needed as an output voltage regulator. In this research, buck-boost converter is proposed to regulate the desired output voltage. The proposed controller in this research is a sliding mode controller (SMC) and employ a linear sliding surface to maintain the regulated voltage stable. This research was conducted by determining the component parameters and state space model of the buck-boost converter. Proportional integral derivative (PID) controller with integral of time-weighted absolute error (ITAE) method is used as a comparison of the proposed method. The performance results were observed from the buck-boost converter by performing 3 fault scenarios, variation in supply voltage, resistor in load side, and the desired output voltage. The results obtained of SMC has a faster settling time than PID controller. The voltage deviation of buck-boost converter exhibits that SMC is smaller than the PID control. In addition, under some experiment conditions, the PID control could not or did not fit in some scenarios while the output control values of SMC matched the changes in the entire scenarios.

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1. INTRODUCTION

Utilization of renewable energy resources (RER) to be converted into electrical energy is currently growing rapidly. This is due to the increasing demand for electricity but the supply of fossil energy is decreasing. The potential resource is currently being widely applied is solar cells or photovoltaic (PV) [1]. PV is a device or component that works based on the photoelectric effect on a semiconductor material to convert light energy into electrical energy [2]. However, one of the obstacles in the utilization of PV is the unstable generated voltage. This is because the voltage generated depends on the condition of the intensity of sunlight received by the PV at all times. Whereas in general, electronic devices have specification limits for voltage variations, frequency variations, and harmonic distortion [3]. If the unstable voltage output continues to be ignored, it will result in less than optimal usage and can accelerate damage to electronic equipment. Most of the electricity generated by RER, such as PV, is direct current (DC) voltage. In order to accomplish this problem, developing a DC-DC converter to stabilize the regulated voltage is most important [4]. There are several types of basic converter that have different capabilities which are step-down converter (buck converter), step-up converter (boost converter), and buck-boost converter.

Buck-boost converter is DC voltage converter whose regulated voltage is invert value and is able to be set higher or lower than the supply voltage by using pulse width modulation (PWM). Currently, buck-

boost converter is commonly employed in various applications such as photovoltaic systems, fuel-cells, electric vehicles, and uninterrupted power supplies [5]–[10]. The difficulty in regulating the voltage for buck-boost converter is caused by the non-minimum phase structure in mathematical model. Inductor current regulation can indirectly adjust the generated voltage of DC converter. This converter is included in nonlinear system and performs in the time domain, such that the conventional linear adjustment technique is not suitable to be applied in controlling this converter. The suitable control technique for converters must overcome the intrinsic nonlinearity, variations in some disturbances, and confirm the stability under some operating conditions by observing the transient response [11]–[13].

Currently, there are various kinds of controls, one of which is the proportional–integral–derivative (PID) controller. According to a survey among tens of thousands of controller types, most controllers employed for regulatory purposes in the chemical, paper, and refining factories is PID controller [14]. In order to compensate the deviation or error between desired value and feedback value, applications of PID controller in power converter are widely used due to the effectiveness and the easiness [15]–[19]. Mostly PID controllers are effective when the system is linear, however these conventional PIDs are not able to compensate the response from non-linear systems or high-order systems [19]. Therefore, a suitable controller is required for a non-linear system as in buck-boost converter. Sliding mode controllers (SMC) are renowned for their durability and stability. This controller is a nonlinear control approaching that is suitable with the nonlinear characteristics of DC-DC converter especially buck-boost converter [11]. SMC has a simple implementation and it is quite resistant to interference. By using this sliding mode control method, it will process mathematical equations by reducing low frequency components which will suppress ripple variations due to imperfect switching processes [20].

The utilization of SMC with linear sliding surface and PID control with integral of time-weighted absolute error (ITAE) method in buck-boost converter are conducted in this research. The calculation of proposed method is carried out by employing state-space model as approaching plant model. The verification of the proposed controller is performed by changing the supply voltage, resistor in load side, and the desired output voltage. The proposed controller is intended to keep the generated voltage of the buck-boost converter as desired voltage.

2. THE PROPOSED METHOD

The proposed method is the application of a sliding mode controller to a buck-boost converter which is employed to improve the settling time. The application of a simple linear sliding surface is carried out. The proposed combination of sliding surface is compiled using inductor current and output voltage.

2.1. Buck-boost converter

The circuit of buck-boost converter is displayed in Figure 1, it is composed of two passive components and two electronic switches (MOSFET and diode) [21]. The dynamic model of this converter describes the characteristics of a system which is defined from two states, a closed switch (ON) and an open switch (OFF). The current passes via the source, inductor, and electronic switch when ON state and there is no current through the load resistor and capacitor [22]. Figure 1 (a) depicts the closed switch of buck-boost converter. By analyzing the voltage and the current based on Kirchhoff's law (KL), the following equation is obtained:

$$V_s = L \frac{di_L}{dt} \quad (1)$$

$$C \frac{dv_C}{dt} = -\frac{v_C}{R} \quad (2)$$

If the (1) and (2) are moved, then the (3) and (4) can be obtained.

$$\frac{di_L}{dt} = \frac{V_s}{L} \quad (3)$$

$$\frac{dv_C}{dt} = -\frac{v_C}{RC} \quad (4)$$

From (3) and (4) with $x_1 = i_L$ and $x_2 = v_C$, the equation below will be obtained:

$$x_1' = \frac{1}{L} V_s \quad (5)$$

$$x_2' = -\frac{x_2}{RC} \quad (6)$$

So, the matrix of state space in first state is:

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ 0 & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} V_s \quad (7)$$

In the second status Figure 1 (b), the current in inductor through the capacitor and load resistor because the diode is conducted. In this time, the inductor become voltage source in the circuit. The analysis is carried out by giving the output voltage V_o with a constant value [22]. The (8) and (9) are derived by considering the voltage in capacitor and the inductor current using KL.

$$-V_c = -L \frac{di_L}{dt} \quad (8)$$

$$-i_L = C \frac{dV_c}{dt} + \frac{V_c}{R} \quad (9)$$

If the (8) and (9) are moved, then the following equation can be obtained:

$$\frac{di_L}{dt} = \frac{V_c}{L} \quad (10)$$

$$\frac{dV_c}{dt} = -\frac{i_L}{C} - \frac{V_c}{RC} \quad (11)$$

From (10) and (11) with $x_1 = i_L$ and $x_2 = v_C$, the equation below will be obtained:

$$\dot{x}_1 = \frac{1}{L} x_2 \quad (12)$$

$$\dot{x}_2 = -\frac{1}{C} x_1 - \frac{1}{RC} x_2 \quad (13)$$

The state space matrix when the switch is opened is:

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 0 & \frac{1}{L} \\ -\frac{1}{C} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \end{bmatrix} V_s \quad (14)$$

This state space equation when the opened switch is the same as the buck converter when the switch is opened [22]. The state space averaging (SSA) is determined by deriving the state of PWM during the switch is opened and closed as described in,

$$\bar{A} = A_{(on)}d + A_{(off)}(1-d)$$

$$\bar{A} = \begin{bmatrix} 0 & 0 \\ 0 & -\frac{1}{RC} \end{bmatrix} d + \begin{bmatrix} 0 & \frac{1}{L} \\ -\frac{1}{C} & -\frac{1}{RC} \end{bmatrix} (1-d) = \begin{bmatrix} 0 & \frac{1-d}{L} \\ -\frac{1-d}{C} & -\frac{1}{RC} \end{bmatrix} \quad (15)$$

$$\bar{B} = B_{(on)}d + B_{(off)}(1-d)$$

$$\bar{B} = \begin{bmatrix} \frac{1}{L} \\ 0 \end{bmatrix} d + \begin{bmatrix} 0 \\ 0 \end{bmatrix} (1-d) = \begin{bmatrix} \frac{d}{L} \\ 0 \end{bmatrix} \quad (16)$$

By substituting the (15) and (16), the complete buck-boost converter model is defined as (17).

$$\begin{bmatrix} \dot{x}_1 \\ \dot{x}_2 \end{bmatrix} = \begin{bmatrix} 0 & \frac{1-d}{L} \\ -\frac{1-d}{C} & -\frac{1}{RC} \end{bmatrix} \begin{bmatrix} x_1 \\ x_2 \end{bmatrix} + \begin{bmatrix} \frac{d}{L} \\ 0 \end{bmatrix} V_s \quad (17)$$

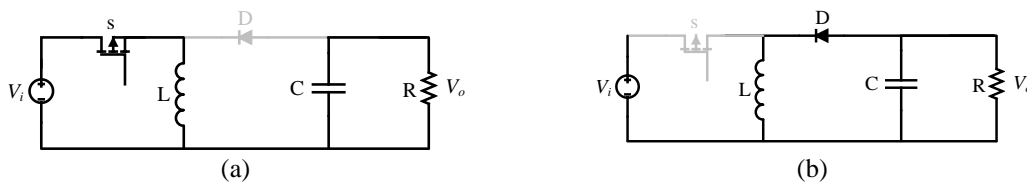


Figure 1. Buck-boost converter circuit (a) in ON state and (b) in OFF state

2.2. Sliding mode control design

The appropriate output voltage is regulated based on suitable parameter of SMC. The calculation of this parameter begins by determining the parameters to be used as an error equation or sliding surface equation [23]. In this study, the reference used is capacitor voltage and inductor current, so the sliding surface equation used is in accordance with (18).

$$S(x) = \alpha_1(x_1 - i^*) + \alpha_2(x_2 - v^*) \quad (18)$$

Where, α_1 and α_2 are sliding coefficients or parameters of the sliding surface, x_1 represents of the current via the inductor, x_2 is the output voltage of the buck-boost converter, i^* is the desired current, and v^* is the reference voltage. Determination of the duty cycle in SMC is defined by two parameters, U_{eq} and U_n which is shown in (19) [24], [25].

$$U = U_{eq} + U_n \quad (19)$$

The U_{eq} or continuous equivalent controller is obtained by calculating the first derivative of $S(x)$ which is equated to zero. By substituting the equations \dot{x}_1 and \dot{x}_2 to derivative of $S(x)$, the state space averaging buck-boost converter, becomes:

$$\alpha_1 \left(\frac{1-d}{L} x_2 + \frac{d}{L} V_{in} \right) + \alpha_2 \left(-\frac{(1-d)}{C} x_1 - \frac{x_2}{RC} \right) = 0 \quad (20)$$

The next step is to collect terms containing variable d (duty cycle) on the left and move terms that do not contain variable d to the right side. Therefore, the equation U_{eq} acquired is:

$$U_{eq} = \frac{-\alpha_1 RCx_2 + \alpha_2 L(Rx_1 + x_2)}{\alpha_1 RC(V_{in} - x_2) + \alpha_2 RLx_1} \quad (21)$$

Then the calculation of the duty cycle on SMC is continued by looking for the discontinuous normal controller U_n . The calculation of this U_n is based on Lyapunov function as a stability investigation.

$$V = \frac{1}{2} S(x)^2 \quad (22)$$

In order to satisfy that this plant have a stable system, the first derivative of the above equation must satisfy negative definite ($\dot{V} < 0$). The first derivative of the Lyapunov stability equation is:

$$\dot{V} = S\dot{S} \quad (23)$$

By substituting \dot{S} as the sliding surface in first derivation and control signal ($U_{eq} + U_n$), U_n can be obtained and it can be shown in (24).

$$U_n = -\alpha_3 \cdot \left(\left(\frac{\alpha_2 x_1}{C} + \frac{\alpha_2 x_2}{RC} - \frac{k_1 x_2}{L} - \frac{\alpha_1^2 C x_2 (x_2 - V_{in})}{L(\alpha_1 C (V_{in} - x_2) + \alpha_2 L x_1)} + \frac{\alpha_1 \alpha_2 (R x_1 + x_2)(x_2 + V_{in})}{R(\alpha_1 C (V_{in} - x_2) + \alpha_2 L x_1)} + \frac{\alpha_2 x_1 (\alpha_1 RCx_2 + \alpha_2 L(Rx_1 + x_2))}{RC(\alpha_1 RLC(V_{in} - x_2) + \alpha_2 RLx_1)} \right) : \left(\frac{\alpha_1 C V_{in} - \alpha_1 x_2 C + \alpha_2 x_1 L}{LC} \right) \right) \text{sign}(S) \quad (24)$$

Where, α is a positive sliding coefficient, in order to fulfill the negative definition of Lyapunov function as stability theory, and S is the sliding surface equation. The implemented signal control for the buck-boost converter U is derived by calculating the U_{eq} and U_n as written in (25).

$$U = U_{eq} + U_n$$

$$U = \left[\frac{-\alpha_1 RCx_2 + \alpha_2 L(Rx_1 + x_2)}{\alpha_1 RC(V_{in} - x_2) + \alpha_2 RLx_1} + \left[-\alpha_3 \cdot \left(\left(\frac{\alpha_2 x_1}{C} + \frac{\alpha_2 x_2}{RC} - \frac{\alpha_1 x_2}{L} - \frac{\alpha_1^2 C x_2 (x_2 - V_{in})}{L(\alpha_1 C (V_{in} - x_2) + \alpha_2 L x_1)} + \frac{\alpha_1 \alpha_2 (R x_1 + x_2)(x_2 + V_{in})}{R(\alpha_1 C (V_{in} - x_2) + \alpha_2 L x_1)} + \frac{\alpha_2 x_1 (\alpha_1 RCx_2 + \alpha_2 L(Rx_1 + x_2))}{RC(\alpha_1 RLC(V_{in} - x_2) + \alpha_2 RLx_1)} \right) : \left(\frac{\alpha_1 C V_{in} - \alpha_1 x_2 C + \alpha_2 x_1 L}{LC} \right) \right) \text{sign}(S) \right] \right] \quad (25)$$

3. RESEARCH METHOD

Investigation the response of the buck-boost converter using the proposed controller, some experiments of buck-boost converter are conducted in simulation and it is compared by PID controller using

ITAE tuning method. Table 1 gives several component parameters that require to be defined such that it is able to validate the proposed method. In order to verify the proposed method in buck-boost converter, it will be compared with PID controller in several scenarios, changing the supply voltage, load, and reference voltage. The constant value of α_1 , α_2 , and α_3 are listed in Table 2. The performance simulation results seen in recovery time (t_{rec}) and voltage deviation.

Table 1. Buck-boost converter parameters

Parameter	Value
Max supply voltage ($V_{i\ min}$)	15 V
Min supply voltage ($V_{i\ max}$)	30 V
Output voltage (V_o)	24 V
Resistor (R)	10 Ω
Switching frequency (f)	20 kHz
Inductor (L)	1000 μ H
Capacitor (C)	2200 μ F

Table 2. Proposed method parameters

Parameter	Value
α_1	1×10^{-8}
α_2	0.1387
α_3	1×10^{-3}

4. RESULTS AND DISCUSSION

4.1. Changing input voltage

For the first investigation, the supply voltage is changed by decreasing and increasing from the initial input voltage, but the desired voltages and load are set in specific value, for instance in PV systems, where the output generated is not constant because it is very dependent on irradiance. The changes made in the experiments are being as:

$$v_{in}(t) = \begin{cases} 7.5, t > 0.3 \\ 11.25, t > 0.3 \\ 18.75, t > 0.3 \\ 22.5, t > 0.3 \\ 26.25, t > 0.3 \\ 30, t > 0.3 \end{cases}$$

The results for the changing input voltage are shown in Table 3. Simulation of closed loop condition with a changing input voltage (initially 15 V) was carried out for 0.7 seconds and when it was 0.3 seconds the supply voltage changed. When the supply voltage decreases, the recovery time for a buck-boost converter with SMC is faster than PID controller as well as when the input voltage increases. Meanwhile, when the input voltage is very large, the recovery time of proposed controller is slower than the PID controller as presented in Figure 2. The largest voltage deviation occurs when the supply voltage set to 7.5 V with a PID controller which is 3.04 V and the smallest voltage deviation occurs when the supply voltage changes to 18.75 V with a SMC which is equal to 0.08 V. Based on Figure 3, it exhibits the voltage deviation proposed controller is lower than PID. The voltage deviation of PID controller is steeper, such that PID controller is not suitable for the system when experiencing changes in input voltage. Figure 4 shows the buck-boost converter with SMC produces a slight overshoot when a disturbance occurs in 0.3 seconds, the output voltage which was initially -24 V to -23.44 V then the voltage returns to its desired value in 0.3605 seconds. While the output voltage of PID controller indicates overshoot and it is able to follow the reference value in 0.3647 seconds.

Table 3. Experiment results when the input voltage changes

V_{in} (V)	V_{ref} (V)	R(Ω)	SMC		PID	
			t_{rec} (s)	$ \Delta V $ (V)	t_{rec} (s)	$ \Delta V $ (V)
7.5	-24	10	0.0605	0.56	0.0647	3.04
11.25			0.0493	0.16	0.0568	1.28
18.75			0.0404	0.08	0.0449	0.95
22.5			0.0488	0.12	0.0458	1.73
26.25			0.0592	0.15	0.0566	2.36
30			0.0664	0.18	0.0585	2.92

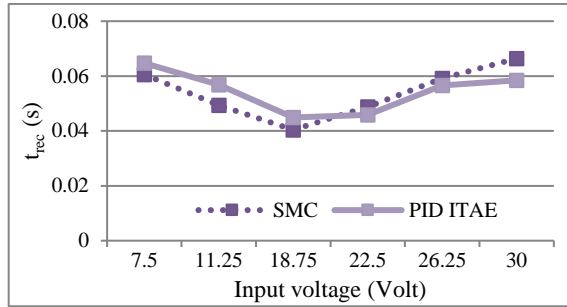
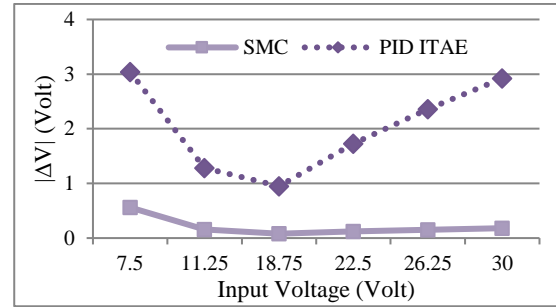
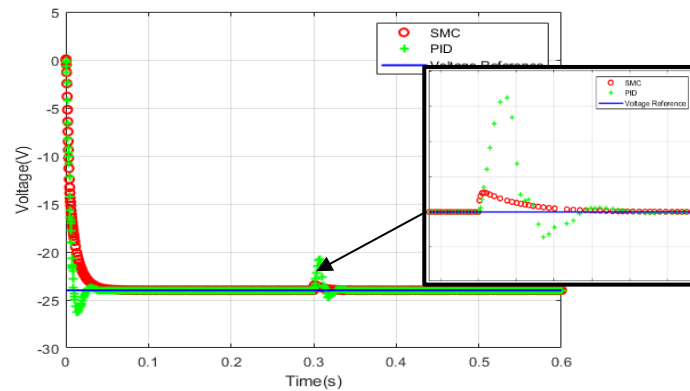
Figure 2. Comparison of t_{rec} for input voltage changeFigure 3. Comparison of ΔV for input voltage change

Figure 4. Output voltage when input voltage changes to 7.5 V

4.2. Changing load

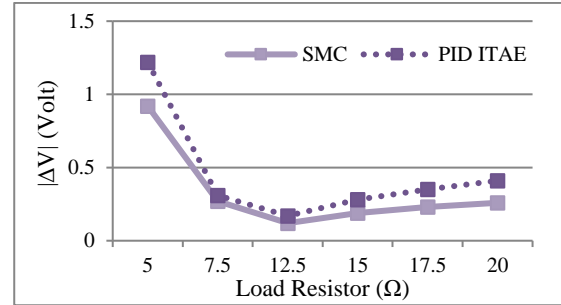
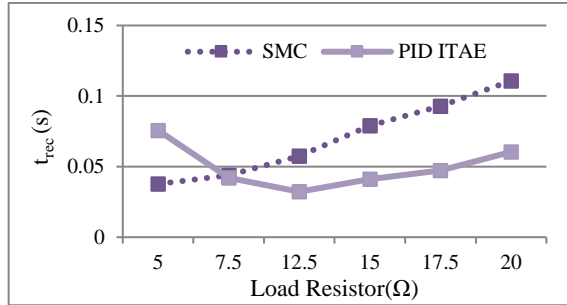
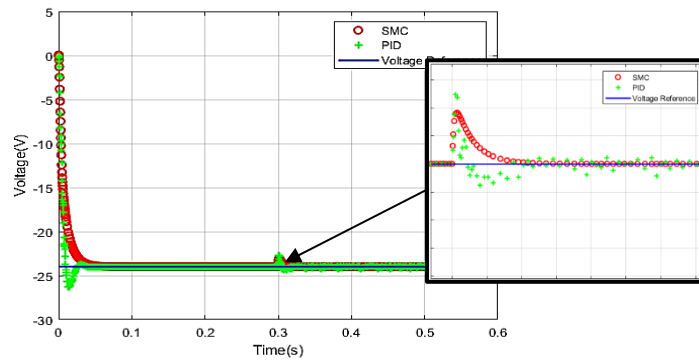
In the second experiment, the load resistor is set several values. The changed by decreasing and increasing the initial load resistor is conducted gradually, but the supply voltage is set in 15 V and the reference voltage is in -24 V. The load changes are defined as:

$$(t) = \begin{cases} 5, t > 0.3 \\ 7.5, t > 0.3 \\ 12.5, t > 0.3 \\ 15, t > 0.3 \\ 17.5, t > 0.3 \\ 20, t > 0.3 \end{cases}$$

The results for this case are stated in Table 4. In the first experiment, the load is reduced by 50%, the time required to return to the desired voltage of the proposed controller is faster than PID controller. This is because when the test occurred after the disturbance, the PID controller experienced oscillations. For further experiments, the recovery time for SMC is slower than PID controller. The longest recovery time occurs when the load changes to 20 Ω on the SMC, which is 0.1108 seconds. For more details, the relationship between load changes and recovery time of the buck-boost converter with proposed controller and PID can be seen in Figure 5. In voltage deviation parameter, SMC produces a smaller deviation compared to the PID controller as shown in Figure 6, furthermore, when the load decreases or increases. The largest voltage deviation occurs when the load changes to 20 Ω on PID controller is 1.22 V and the smallest voltage deviation occurs when the load changes to 12.5 Ω with an SMC is 0.12 V. When the load decreases, the result of voltage deviation is greater than when the load increases, so the SMC and PID controller is able to overcome this case. From Figure 7, the SMC response has overshoot when a disturbance occurs in 0.3 seconds, the output voltage become -23.08 V then it returns to reference value in 0.3377 seconds. In PID controller, the load changes to 5 Ω in 0.3 seconds generates -22.78 V then reach the reference value back in 0.3757 seconds and it yields oscillations until the end of the simulation.

Table 4. Experiment results when the load resistor changes

V_{in} (V)	V_{ref} (V)	$R(\Omega)$	SMC		PID	
			t_{rec} (s)	$ \Delta V $ (V)	t_{rec} (s)	$ \Delta V $ (V)
15	-24	5	0.0377	0.92	0.0757	1.22
		7.5	0.0439	0.27	0.042	0.31
		12.5	0.0574	0.12	0.0322	0.17
		15	0.079	0.19	0.0411	0.28
		17.5	0.0929	0.23	0.0472	0.35
		20	0.1108	0.26	0.0604	0.41

Figure 5. Comparison of t_{rec} for load resistor changeFigure 6. Comparison of ΔV for load resistor changeFigure 7. Output voltage when load resistor changes to 5 Ω

4.3. Voltage reference changing

In last scenario, the reference voltage is changed by decreasing and increasing from the initial reference voltage, but the input voltage and load resistor are kept constant is being as:

$$v_{ref}(t) = \begin{cases} -12, & t > 0.3 \\ -18, & t > 0.3 \\ -30, & t > 0.3 \\ -36, & t > 0.3 \\ -42, & t > 0.3 \\ -48, & t > 0.3 \end{cases}$$

The last experiment results for changing reference voltages are tabulated in Table 5 and indicate the required time to recovery for SMC is slower than the PID controller. Meanwhile, when the reference voltage is increased by 25%, the recovery time of SMC has rapid response than PID controller as shown in Figure 8. In this scenario there are different results, when the reference voltage increases above 25%, the simulation is not proceeded because the calculated K_p , K_i , and K_d parameters are not suitable for changes in this range. The voltage deviation that occurs in the PID controller means that there is always an overshoot at the time of settling time. Figure 9 shows the buck-boost converter with SMC has no overshoot when there is a disturbance at 0.3 seconds and at 0.3971 seconds the output voltage reaches the recent reference, which is -12 V. The output response of PID controller when the reference voltage changes to -12 V in 0.3 seconds generates an output voltage deviation of -9.961 V then the voltage follows the reference voltage slowly in 0.3604 seconds.

Table 5. Experiment results when the voltage reference changes

V_{in} (V)	V_{ref} (V)	$R(\Omega)$	SMC		PID	
			t_{rec} (s)	$ \Delta V $ (V)	t_{rec} (s)	$ \Delta V $ (V)
15	-12	10	0.0971	0	0.0604	2.039
	-18		0.0872	0	0.0679	1.31
	-30		0.0521	0	0.0714	1.8
	-36		0.0799	0	-	-
	-42		0.0823	0	-	-
	-48		0.0922	0	-	-

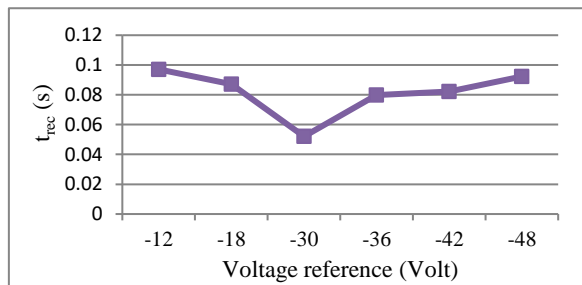
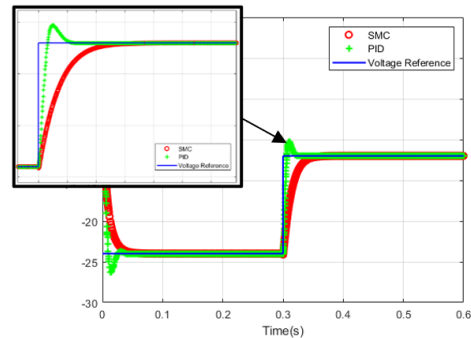
Figure 8. t_{rec} for voltage reference change in SMC response

Figure 9. Output voltage when voltage reference changes to -12 V

5. CONCLUSION

The performance results for buck-boost converter using SMC with linear sliding surface and PID controller can be observed by two parameters such as the voltage deviation and recovery time. The given disturbance scenario is conducted in three forms, modifying the supply voltage, varying load, and shifting the reference voltage. The proposed controller when given a disturbance requires a longer recovery time than PID controller. For SMC, the generated voltage deviation is twice smaller than PID controller. However, some conditions, the K_p , K_i , and K_d parameters of the PID are not able to continue the simulation due to the infinite calculation. If the voltage deviation is too large, such as that produced by the PID controller, it can damage the existing semiconductor switching components. In addition, SMC is also more adaptive to all changes that occur. This is proven by occurrence of disturbance in the plant, all simulations can run and the resulting output voltage can still return according to the reference.

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