Voltage regulation of DC micro grid system using PV and battery coupled SEPIC converter

Rajaboyana Narendra Rao¹, Meda Sreenivasulu², Busharaju Ramakrishna³

¹Department of Electrical Engineering, JNTUA College of Engineering, Andhra Pradesh, India
²Department of Electrical & Electronics Engineering, Vasavi College of Engineering, Hyderabad, India
³Department of Electrical and Electronic Engineering, Maturi Venkata Subba Rao (MVSR) Engineering College, Hyderabad, India

Article Info

Article history:

Received May 29, 2022 Revised Aug 28, 2022 Accepted Sep 7, 2022

Keywords:

Buck boost converter DC micro grid Photovoltaic PID controller SEPIC converter

ABSTRACT

This paper deals with the voltage regulation of DC micro grid system using photovoltaic and battery coupled single-ended primary-inductor converter (SEPIC) converter. SEPIC converter is a DC-to-DC boost converter, it can produce non pulsating DC current with less ripples when compared to buck and buck boost converters. Non pulsating DC current is the demanding condition required in maximum power point tracking (MPPT) applications and battery charging. This paper presents the simulation of the converters for both open and closed loop systems. Firstly, the proposed PV coupled SEPIC converter and the battery coupled buck boost converters are integrated to regulate the voltage in micro grid, and it is compared with the conventional methods in terms of output voltage, ripple voltage and power. The ripple voltage has been reduced to 0.1 V from 0.3 V in the proposed method. Secondly, PI and PID controllers are employed individually to SEPIC and buck boost converter for the voltage regulation of the DC micro grid system. The performance of the closed loop analysis is done in terms of time domain specifications, and it reveals that PID controller has the better response. The proposed system is simulated using MATLAB/Simulink and the prototype has been developed to verify the simulated results.

This is an open access article under the <u>CC BY-SA</u> license.

BY SA

Corresponding Author:

Rajaboyana Narendra Rao Department of Electrical Engineering, JNTUA College of Engineering Pulivendula 4th UAD Campus, Muddanur Rd, Pulivendula, Andhra Pradesh 516390, India Email: rnrao.jntueee@gmail.com

1. INTRODUCTION

Nowadays, grid connected photovoltaic systems are very popular due to constant demand of energy. Many countries are encouraging PV fed power generation systems by funding it. The efficiency of the PV panels to convert electrical power from sunlight is always less than 20%, and even less efficiency in case of load conditions and panel temperature. In order to improve efficiency, maximum power point tracking (MPPT) technique with power point tracker is employed to improve the efficiency. PV coupled single-ended primary-inductor converter (SEPIC) converter can able to generate or track more power from the solar panels using PWM controlled DC-DC converter [1]–[3]. The extraction of maximum power from PV panel without using microcontroller is given in [4]. The maximum power can be extracted from PV panels under all atmospheric conditions and the efficiency can be improved with controlled PWM technique [5]. The power range of low power and residential applications using grid connected single phase inverter are 10 KW and less than that. More switching losses, interference level and acoustic noise are due to high switching at three level inverters [6]. P-V and V-I characteristics of PV due to non-uniform insolation with partial shading was proposed in [7]. PV panels, interfacing circuits with load can extract maximum power from the PV

panels [8]–[17]. DC microgrids is the integration of local loads and generators operated in DC. The different advantages of DC microgrid over AC microgrid are i) efficiency of DC power system as it operates with fewer converters, ii) requirement of less components, so it is reliable, iii) low cost due to requirement of the lesser components, and iv) High survival and less complex when subject to disturbances [18]–[25]. The two different modes of DC microgrid are island mode and grid connected mode. In grid connected mode, it can supply the power to the grid in demand and absorbs the power from the grid in excess condition. The above literature has not discussed PV fed SEPIC converter system. System configuration and DC microgrid model is presented in section 2. Section 3 deals with the comparison of conventional and proposed DC microgrid system. PI and PID controlled Closed loop system are also discussed in this section. Hardware implementation of the proposed system is explained in the subsequent sections 4. Section 5 concludes the results.

2. SYSTEM DESCRIPTION

Figure 1 illustrates the schematic of the proposed system. The schematic diagram represents the PI and PID controlled SEPIC and buck boost converter. MPPT technique employed PV source is given to the SEPIC converter and source from battery is given to the buck boost converter through filter is integrated with the DC grid. Figure 2 shows the Integration of conventional buck boost and boost converter employed to the DC grid. The main objective of the buck boost and boost converter is to provide voltage regulation to the DC Microgrid. The modified and proposed SEPIC and buck boost converter is shown in Figure 3. Non pulsating DC current is the demanding condition required in MPPT applications and battery charging. The proposed PV coupled SEPIC converter and the battery coupled buck boost converters are integrated to regulate the voltage in microgrid.

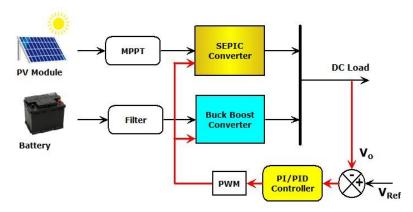


Figure.1. Schematic illustration of proposed system

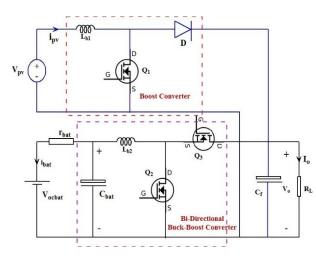


Figure 2. Conventional circuit diagram

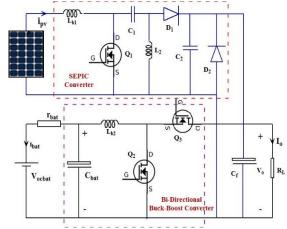


Figure 3. Circuit diagram of proposed system

3. MATLAB/SIMULINK SIMULATION RESULTS

In the circuit diagram of DC micro grid-based boost and buck boost converter is depicted in Figure 4. Output voltage of 70 V with 1 V ripple voltage is obtained across RL are depicted in Figure 5 and Figure 6. The output current of 0.7 A through R-L load is as depicted in Figure 7. The output power of 47 W is obtained from the R-L load of the converter is delineated in Figure 8.

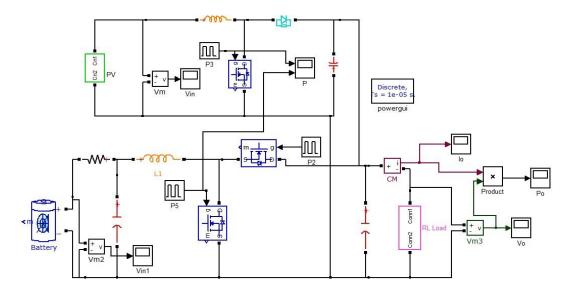


Figure 4. Schematic diagram of boost and buck boost converter for DC microgrid

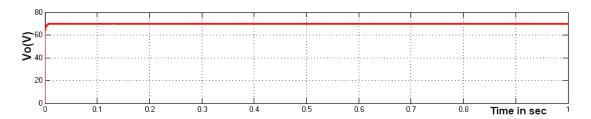


Figure 5. Voltage across RL-load of the converter

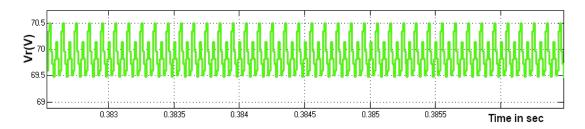


Figure 6. Ripple voltage across RL-load

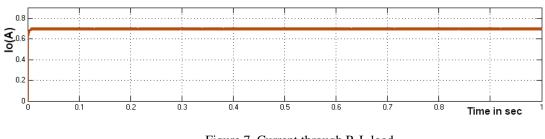


Figure 7. Current through R-L load

Voltage regulation of DC micro grid system using PV and battery coupled ... (Rajaboyana Narendra Rao)

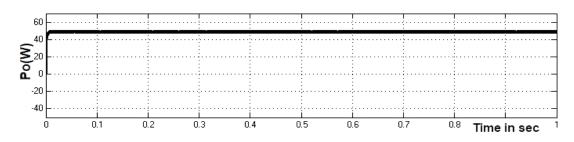


Figure 8. Output power of the converter

The Simulink circuit diagram of DC micro grid-based buck boost and SEPIC converters is depicted in Figure 9. Figure 10 and Figure 11 depict the output voltage of 95 V with 0.3 V ripple voltage across the RL. Figure 12 depicts the 0.98 A output current through RL-load of the converter and Figure 13 depicts the 90 W output power across the RL-load of the converter.

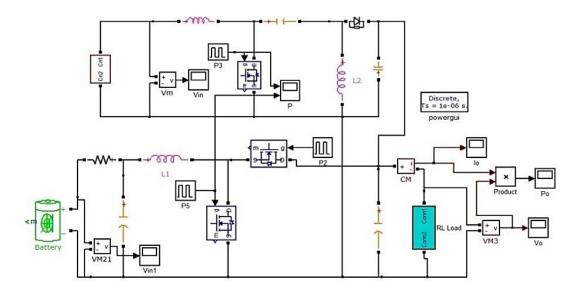
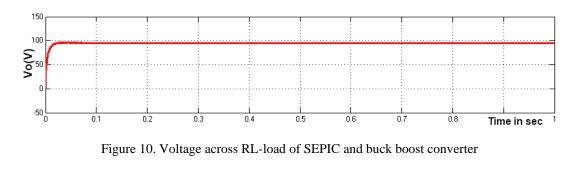


Figure 9. Circuit diagram of integrated SEPIC and buck boost converter for DC microgrid



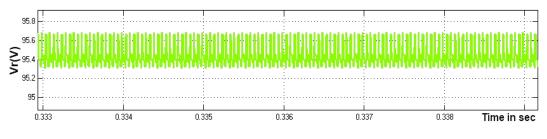
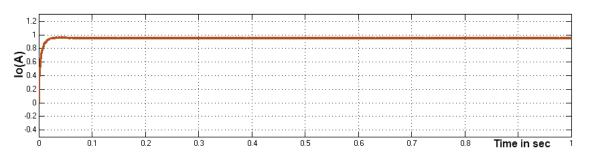
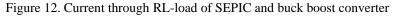
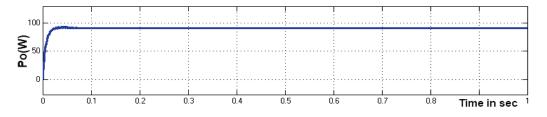


Figure 11. Ripple voltage across RL-load of SEPIC and buck boost converter







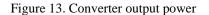


Table 1 shows the comparison of input and output parameters of the converters. The ripple voltage has been reduced to 0.1 V from 0.3 V in the proposed method. The circuit diagram of DC micro grid based SEPIC and buck boost converter with source disturbance is delineated in Figure 14 and its input voltage is 19 V. Output voltage of 110 V and output current of 1.18 A of the proposed converter with source disturbance are shown in Figure 15 and Figure 16 respectively.

Table 1. Comparison of input and output parameters of the converter									
	DC Micro grid	Vin(V)	Vo(V)	Vor(V)	Po(W)				
	Boost and buck boost converter	15	70	1.0	50				
	SEPIC and buck boost converter	15	95	0.3	90				

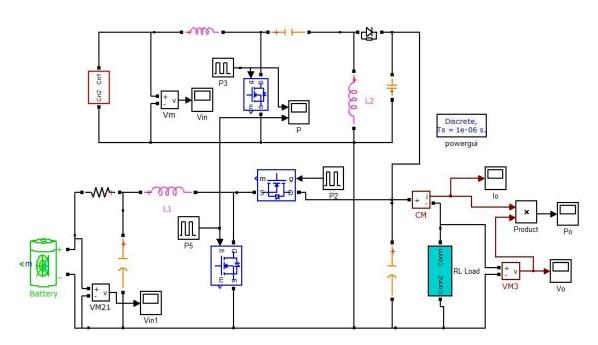


Figure 14. Circuit diagram of proposed converter with source disturbance for DC micro grid

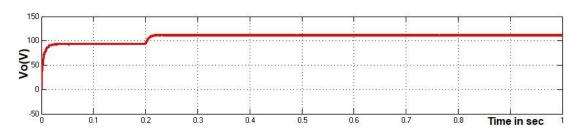


Figure 15. Voltage across RL-load of converter with source disturbance

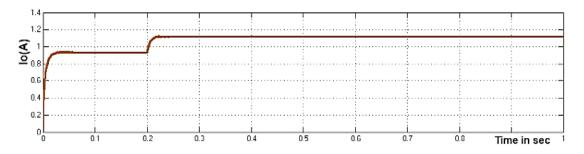


Figure 16. Current through RL-load of proposed converter with source disturbance

The circuit diagram of PI controlled proposed converter for DC micro grid is depicted in Figure 17 and its input voltage value is 19 V. Voltage across R-L load and current through RL-load of SEPIC and buck boost converter with PI controller are depicted in Figure 18 and Figure 19 respectively and its values are 100 V and 0.98 A. Output power is as shown in Figure 20 and its corresponding value is 99 W.

The Circuit diagram of PID controlled proposed converter for DC micro grid is depicted in Figure 21 and its input voltage value is 19 V. Voltage across R-L load and current through RL-load of proposed converter with PID controller are depicted in Figure 22 and Figure 23, and its values are 100 V and 0.98 A. Table 2 gives the comparison of time domain parameters for PI and PID controllers. Table 2 results show that PID controlled SEPIC and buck boost converter has the better performance.

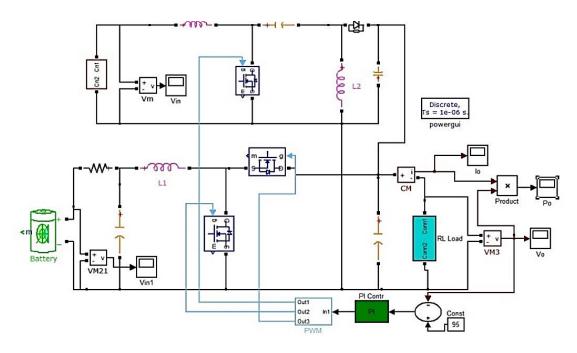
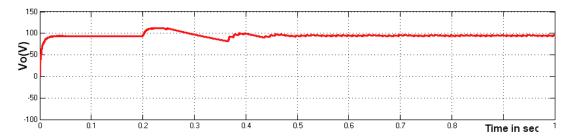
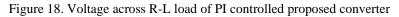
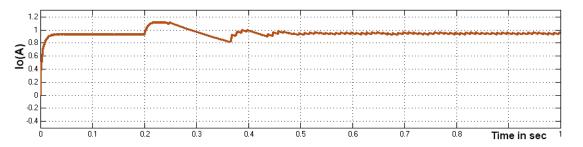
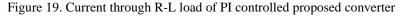


Figure 17. Circuit diagram of PI controlled proposed converter for DC microgrid









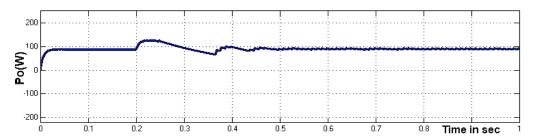


Figure 20. Output power of PI controlled proposed converter

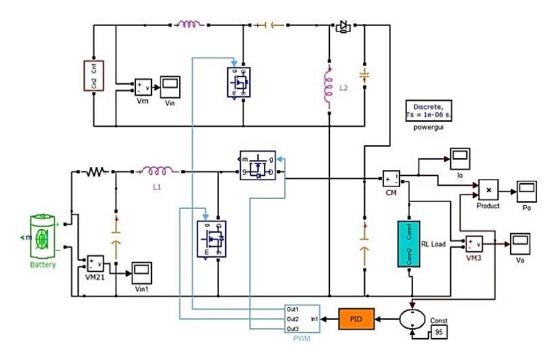


Figure 21. Circuit diagram of PID controlled proposed converter for DC micro grid

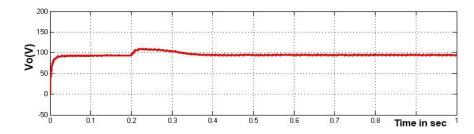


Figure 22. Voltage across PID controlled proposed converter

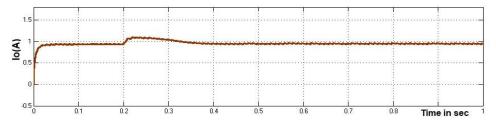


Figure 23. Current through R-L load of PID controlled proposed converter

Table 2. Comparison time domain parameters										
	Controllers	Tr(s)	Tp(s)	Ts(s)	Ess(v)					
	PI	0.22	0.26	0.50	1.8					
	PID	0.21	0.23	0.38	1.3					

4. HARDWARE SYSTEM RESULTS

This section Explains the prototype model of the proposed converter for voltage regulation in the DC Microgrid. The hardware protype model of the proposed converter is depicted in Figure 24. The input voltage of the hardware implementation is shown in Figure 25. The output voltage of hardware implementation of SEPIC and buck boost converter are shown in Figure 26. The hardware prototype verifies the simulation results.

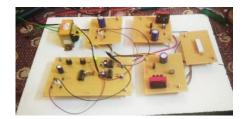


Figure 24. Hardware protype model of SEPIC and buck boost converter

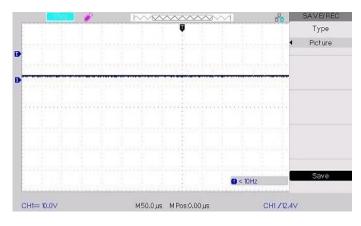


Figure 25. Input voltage of hardware implementation

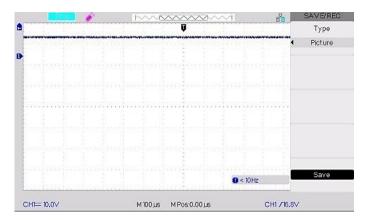


Figure 26. Output voltage of hardware implementation

5. CONCLUSION

The voltage regulation of DC microgrid system using PV and battery coupled SEPIC converter has been designed and simulated using MATLAB/Simulink. SEPIC converter is a DC-to-DC boost converter, it can produce non pulsating DC current with less ripples when compared to buck and buck boost converters. Non pulsating DC current is the demanding condition required in MPPT applications and battery charging. Hence, SEPIC converter in DC micro grid is superior to Buck boost converter in DC micro grid. Closed loop SEPIC converter with PI and PID controllers are simulated, and the comparison reveals that PID controller has better time domain responses in comparison with PI controller. Hence, a closed loop DC micro gridbased SEPIC converter with PID controller is superior to closed loop DC micro grid based SEPIC converter with PI controller.

REFERENCES

- G. Ramya, and V. Ganapathy, "Comparison of five level and seven level inverter based static compensator system," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 3, no. 3, pp. 706–713, 2016, doi: 10.11591/ijeecs.v3.i3.pp706-713.
- [2] C. I. Byrnes, F. D. Priscoli, and A. Isidori, "Output regulation of uncertain nonlinear systems," Birkhauser Boston, Francesco Delli Priscoli, 1997.
- [3] M. Becherif, D. Paire, and A. Miraoui, "Energy management of solar panel and battery system with passive control," *International Conference on Clean Electrical Power*, 2007, pp. 14-19, doi: 10.1109/ICCEP.2007.384179.
- [4] A.G. Karthikeyan, K. Premkumar, P. Suresh, G. Ramya, and A.J. Antony, "Multi input and multi output zeta converter for hybrid renewable energy storage systems," *Int. J. Innovative Technology & Explorer Eng*, vol. 9, no. 2, pp. 411–4-4119, 2019, doi: 10.35940/ijitee.B7417.129219.
- [5] A. Kwasinski, and C. N. Onwuchekwa, "Dynamic behavior and stabilization of DC microgrids with instantaneous constantpower loads," *IEEE Transactions on Power Electronics*, vol. 26, no. 3, pp. 822–834, 2011, doi: 10.1109/tpel.2010.2091285.
- [6] G. Ramya, V. Ganapathy, and P. Suresh, "Power quality improvement using multi-level inverter-based DVR and DSTATCOM using neuro-fuzzy controller," *International Journal of Power Electronics and Drive Systems (IJPEDS)*, vol. 8, no. 1, pp. 316–324, 2017.
- [7] C. U. Cassiani, John E. Candelo-Becerra, Fredy E. Hoyos, "Electricity market strategies applied to Micro grid development," *International Journal of Power Electronics and Drive Systems (IJPEDS)*, vol. 11, no. 1, pp. 530–546, 2020, doi: 10.11591/ijpeds.v11.i1.pp530-546.
- [8] G. Ramya, V. Ganapathy, and P. Suresh, "Comprehensive analysis of interleaved boost converter with simplified H-bridge multilevel inverter based static synchronous compensator system," *Electric Power Systems Research*, vol. 176, pp. 1–12, 2019, doi: 10.1016/j.epsr.2019.105936.
- Y. Tan, D. Kirschen, and N. Jenkins, "A model of PV generation suitable for stability analysis," *IEEE Transactions on Energy Conversion*, vol. 19, no. 4, pp. 748–755, 2004, doi: 10.1109/tec.2004.827707.
- [10] S. K. Bhuyan, P. K. Hota, and B. Panda, "Power quality analysis of a grid-connected solar/wind/hydrogen energy hybrid generation system," *International Journal of Power Electronics and Drive Systems (IJPEDS)*, vol. 9, no. 1, pp. 377–389, 2018, doi: 10.11591/ijpeds.v9n1.pp377-389.
- [11] S. Shobana, K. Praghash, G. Ramya, B.R. Rajakumar, and D. Binu, "Integrating renewable energy in electric V2G: improved optimization assisting dispatch model," *International Journal of Energy Research*, vol. 46, no. 6, pp. 7917–7934, 2022, doi: 10.1002/er.7690.
- [12] I. Alhamrouni, M. K. Rahmat, F. A. Ismail, M. Salem, A. Jusoh, and T. Sutikno, "Design and development of SEPIC DC-DC boost converter for photovoltaic application," *International Journal of Power Eletronicsc and Drive Systems (IJPEDS)*, vol 1, no. 1, pp. 406–413, 2019, doi: 10.11591/ijpeds.v10.i1.pp406-413.
- [13] X. Weng et al., "Comprehensive comparison and analysis of non-inverting buck boost and conventional buck boost converters," *The Journal of Engineering*, vol. 16, pp. 3030–3034, 2019, doi: 10.1049/joe.2018.8373.
- [14] J. P. V. Ceballos, C. A. Ramos-Paja, and E. E. Henao-Bravo, "Sliding-mode controller for a step up-down battery charger with a single current sensor," *International Journal of Electrical and Computer Engineering*, vol. 12, no. 2, pp. 1251–1264, 2022, doi: 10.11591/ijece.v12i2.pp1251-1264.
- [15] A. H. Mahmood, M. F. Mohammed, M. O. Ali, and A. H. Ahmad, "Single phase inverter fed through a regulated SEPIC converter," *Bulletin of Electrical Engineering and Informatics*, vol. 10, no. 6, pp. 2921–2928, 2021,

Voltage regulation of DC micro grid system using PV and battery coupled ... (Rajaboyana Narendra Rao)

doi: 10.11591/eei.v10i6.2853.

- [16] K.S. Reddy, and S.B. Veeranna, "Single phase multifunctional integrated converter for electric vehicles," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 24, no. 3, pp. 1342–1353, 2021, doi: 10.11591/ijeecs.v24.i3.pp1342-1353.
- [17] G. Ramya, P. Suresh, and B. Manimaran, "Power quality enhancement in transmission system using proposed switched capacitor multilevel inverter-based static synchronous compensator," *Electronic Systems and Intelligent Computin*, vol. 860, pp. 739–750, 2022, doi: 10.1007/978-981-16-9488-2_70.
- [18] N. Kacimi, S. Grouni, A. Idir, and M. S. Boucherit, "New improved hybrid MPPT based on neural network-model predictive control kalman filter for photovoltaic system," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 20, no. 3, pp. 1230–1241, 2020, doi: 10.11591/ijeecs.v20.i3.pp1230-1241.
- [19] M. Nagaiah, and K.C. Sekhar, "Analysis of fuzzy logic controller based bi-directional DC-DC converter for battery energy management in hybrid solar/wind micro grid system," *International Journal of Electrical and Computer Engineering*, vol. 10, no. 3, pp. 2271–2284, 2020, doi: 10.11591/ijece.v10i3.pp2271-2284.
- [20] G. Ramya, M. Vinil, J. Ajay Daniel and M. Moovendan, "Energy improvement in distribution network using sliding mode controller based SVC system," *ECS Transactions*, vol. 107, no. 1, p. 5155, 2022, doi: 10.1149/10701.5155ecst.
- [21] M. Vinil, G. Ramya, M. Moovendan, and J. Ajay Daniel, "Reactive power injection to grid using transformer-less MOSFET based single phase inverter using PV to achieve high efficiency," *ECS Transactions*, vol. 107, no. 1, p. 5305, 2022, doi: 10.1149/10701.5305ecst.
- [22] Z. H. Ali, Z. H. Saleh, R. W. Daoud, and A. H. Ahmed "Design and simulation of a microgrid for TIH campus," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 19, no. 2, pp. 729–736, 2020, doi: 10.11591/ijeecs.v19.i2.pp729-736.
- [23] A. M. Al-Modaffer, A. A. Chlaihawi, and H. A. Wahhab "Non-isolated multiple input multilevel output DC-DC converter for hybrid power system," *Indonesian Journal of Electrical Engineering and Computer Science*, vol. 19, no. 2, pp. 635–643, 2020, doi: 10.11591/ijeecs.v19.i2.pp635-643.
- [24] M. Z, Efendi, F. D. Murdianto, F. A. Fitri, and L. Badriyah, "Power factor improvement on LED lamp driver using BIFRED converter," *TELKOMNIKA (Telecommunication Computing Electronics and Control*, vol. 18, no. 1, pp. 571–578, 2020, doi: 10.12928/telkomnika.v18i1.13160.
- [25] A. R. Gautam, S. Singh, and D. Fulwani "DC bus voltage regulation in the presence of constant power load using sliding mode controlled dc-dc Bi-directional converter interfaced storage unit," 2015 IEEE First International Conference on DC Microgrids, 2015, pp. 257–262, doi: 10.1109/icdcm.2015.7152050.

BIOGRAPHIES OF AUTHORS



Rajaboyana Narendra Rao (D) **S S** has completed his M.Tech degree from RGPV, Bhopal in 2005. Presently he is working as Assistant Professor in JNTUA College of Engineering, Pulivendula. He has a total experience of 16 years in teaching. His research interests include power electronics, renewable energy sources, DC–DC converters, power semiconductor drives and power quality. He can be contacted at email: rnrao.jntuee@gmail.com.



Meda Sreenivasulu b S s c received his Master's degree from JNTU, Anantapur in the year 2006. His specialization in Master's degree is Power & Industrial Drives. He is working as assistant professor in Vasavi college of engineering, Hyderabad. He has an experience of 17 years in teaching. His research interests include Renewable energy sources, power quality and digital signal processing techniques. He can be contacted at email: m.srinivasulu@staff.vce.ac.in.



Busharaju Ramakrishna b Mission **b** has completed his M.Tech degree from JNTU, Anantapur in 2006. Presently he is working as Assistant Professor in Maturi Venkata Subba Rao (MVSR) Engineering College. He has a total experience of 16 years in teaching. His research interests include power systems, control systems, Renewable Energy Sources, and Power Quality. He can be contacted at email: bramakrishna_eee@mvsrec.edu.in.