A novel technique for torque ripple suppression in BLDC motor drive using switched capacitor based SEPIC converter

Sunam Saha¹, Debjyoti Chowdhury², Madhurima Chattopadhyay², Moumita Mukherjee³

¹Department of Electrical and Electronics Engineering Department, School of Engineering and Technology, Adamas University, Kolkata, India

²AEIE Department, Heritage Institute of Technology, Kolkata, India

³Physics Department, School of Basic and Applied Science, Adamas University, Kolkata, India

Article Info

Article history:

Received Jul 13, 2023 Revised Aug 30, 2023 Accepted Sep 14, 2023

Keywords:

BLDC drive Brushless DC motor DC-DC converter Hybrid SEPIC Switched capacitor Torque ripple

ABSTRACT

This article reports a strategy for suppressing commutation torque ripple has been proposed by modifying a SEPIC converter using switched capacitor. The torque ripple generated during the commutation period is one of the main disadvantages of a brushless DC (BLDC) motor. The cause for the torque ripple in brushless DC motor has been mathematically analyzed in this work. Henceforth, a modified switched-capacitor based DC-DC converter has been proposed for integration with BLDC drive. Moreover, a theoretical analysis of the relationship has been established between the duty ratio of the DC-DC converter with commutation time and torque ripple. A low cost and simple method to control the dc link voltage of BLDC drive has been proposed and a comparative analysis of the proposed converter with previously existing similar converters has been shown. This work aims at reducing ripple in electromagnetic torque of BLDC motor from the view point of duty ratio of the DC-DC converter and commutation time of the drive. The feasibility of the drive has been evaluated experimentally and through simulation. It has been observed that the integration of the proposed converter with BLDC drive help in torque ripple suppression to 16.5% as compared to BLDC drive without any DC-DC converter.

This is an open access article under the <u>CC BY-SA</u> license.



Corresponding Author:

Sunam Saha Department of Electrical and Electronics Engineering, School of Engineering and Technology Adamas University Kolkata,700126, India Email: sunamsaha.ee@gmail.com

1. INTRODUCTION

The brushless DC (BLDC) motor has excellent speed regulation capability enabling it to be widely applicable in industry and domestic applications [1], [2]. The primary factor limiting the use of BLDC motors in many industries is the commutation torque ripple they create [2], [3]. The torque ripple may even reach up to 50% of the value of the average electromagnetic torque. Therefore, reduction of torque ripple is important for improving the performance of the BLDC motor.

The research on reducing torque ripple in BLDC motor has attracted a lot of attention since last decade [4]–[12]. Through literature survey it is observed that commutation torque ripple can be reduced by controlling the rise time and fall time of phase current during commutation [6]. Different modulated duty ratio has been applied for controlling the slopes of the incoming and outgoing phase currents as described in [7]. The torque ripple can be minimized by applying modulated duty ratio to each phase. Hu *et al.* and Li *et al.* [8], [9] presented a different modulation technique for torque ripple minimization using

F

field-programmable gate array (FPGA) algorithm and coordinate transformation theory for both low speed and high-speed operation. The simplest method for torque ripple minimization is application of power modulator along with inverter. The reason for terming the method simplest is that it offers a solution with less complicated circuitry. As a result, different converter topologies have been proposed and studied that offers better reduction of torque ripple [10]–[16]. A bidirectional buck boost converter has been used for torque ripple reduction in BLDC drive, however, the mathematical evaluation of torque ripple reduction has not been elaborated as can be seen [11]. The bidirectional buck boost converter is integrated with inverter through common three switch leg for BLDC drive which increases the complexity of the control circuitry required for the said drive. A torque ripple minimization scheme for BLDC drive has been introduced based on a newly proposed single-inductor multiple output (SIMO) DC-DC converter using FPGA as illustrated in [12]. However, the relation between the converter and torque ripple has not been elaborated. A comparative analysis on torque ripple reduction techniques using four converter topologies has been shown for different operating speed as explained in [13]. Moreover, in recent years, it has been observed that hybrid converters also gained popularity for torque ripple suppression in BLDC drive [14]–[19].

Thus, all these studies [11]–[15] proves that the inclusion of a power modulator in BLDC drive circuitry reduces the unwanted torque ripple but none of them reveals any mathematical relationship of torque ripple reduction with the duty ratio of DC-DC converter. In order to understand the introduction of converter for torque ripple suppression, we have established the mathematical relationship between the duty ratio of the converter along with the electromagnetic torque in BLDC drive. The major contribution of this article is this numerical analysis, which will enable the future studies to incorporate the appropriate DC-DC converter for BLDC drive. In this context, switched-capacitor based SEPIC converter has been introduced in this paper that shows a higher voltage boost ratio compared to other converters presented in [20]–[23]. The switched capacitor plays a vital role here as it helps to reduce the voltage stress across switches used in converters.

In this paper, the work is introduced in: i) Section 2 presents the mathematical analysis of the torque ripple at the time of commutation and its relation with DC link voltage; ii) The proposed scheme of brushless DC motor drive is put forward in section 3 along with the relation between the duty ratio of converter and torque ripple and a modified switched capacitor-based converter is proposed; iii) The method to perform the experiment, results, and discussions are presented in section 4; and iv) The concluding remark has been described in section 5.

2. MATHEMATICAL ANALYSIS OF TORQUE RIPPLE DURING COMMUTATION

An equivalent circuit representation of the brushless DC motor drive has been shown in Figure 1. The voltage equation for the three phases in BLDC motor can be represented as (1). The equation for electromagnetic torque is given by (2).

$$\begin{bmatrix} V_r \\ V_y \\ V_b \end{bmatrix} = \begin{bmatrix} R & 0 & 0 \\ 0 & R & 0 \\ 0 & 0 & R \end{bmatrix} \begin{bmatrix} l_r \\ i_y \\ i_b \end{bmatrix} + \begin{bmatrix} L & 0 & 0 \\ 0 & L & 0 \\ 0 & 0 & L \end{bmatrix} \frac{d}{dt} \begin{bmatrix} l_r \\ i_y \\ i_b \end{bmatrix} + \begin{bmatrix} e_r \\ e_y \\ e_b \end{bmatrix} + \begin{bmatrix} V_{no} \\ V_{no} \\ V_{no} \end{bmatrix}$$
(1)

$$T_e = \frac{e_r i_r + e_y i_y + e_b i_b}{\omega_m} \tag{2}$$

Where, V_{no} is the neutral to ground voltage, V_r , V_y , and V_z are the terminal voltages of the three phases. The three phase currents are given by i_r , i_y , and i_b . The trapezoidal back emf are given by e_r , e_y , and e_b . R is the resistance of the phase winding. Assuming all three phases have same resistance and L is the equivalent inductance of the phase winding.



Figure 1. Equivalent circuit representation of BLDC motor

(7)

29

The desired waveform of current is rectangular in shape but practically it is not the same. The actual phase current is trapezoidal in shape comprising of finite rise time and fall time due to the presence of stator inductance [23]–[27]. The actual shape of phase current obtained is presented in Figure 2. The slope of phase current has a direct impact on the torque ripple of the brushless DC motor.

Assuming very short duration of commutation, we can consider the back emf to be constant during commutation. Then the initial values of voltage at the start of the commutation can be considered as $V_r = 0$, $V_y = V_{dc}$, $V_b = 0$, $e_r = E_c$, $e_y = E_c$, and $e_b = -E_c$. Rewriting (1), using the values of initial condition, we get (3).

$$Ri_{r} + L\frac{di_{r}}{dt} + e_{r} + V_{no} = 0$$

$$Ri_{y} + L\frac{di_{y}}{dt} + e_{y} + V_{no} = V_{dc}$$

$$Ri_{b} + L\frac{di_{b}}{dt} + e_{b} + V_{no} = 0$$

$$(3)$$

The neutral point voltage drop can be written as (4). Substituting the initial values of emf and current in the (2), the torque equation before commutation is given by (5).

$$V_{no} = \frac{1}{2} (V_{dc} - E_c)$$
(4)

$$T_{e_start} = \frac{2I_c E_c}{\omega_m} \tag{5}$$

Considering high switching frequency of the pulse width modulation and shorter time period for commutation. The effect of resistance can be neglected. The slope of the phase current obtained using the above assumption has been shown in (6). The time taken for the current (i_r) to decay and reduce to initial value as shown in Figure 3 can be obtained using (6) and has been expressed as (7).

$$\frac{di_r}{dt} = -\frac{V_{dc} + 2E_c}{3L}$$

$$\frac{di_y}{dt} = \frac{2(V_{dc} - E_c)}{3L}$$

$$\frac{di_b}{dt} = -\frac{V_{dc} - 4E_c}{3L}$$
(6)

$$t_{decay} = \frac{3LI_c}{V_{dc} + 2E_c}$$



Figure 2. Phase current of three phases during commutation

Figure 3. Proposed BLDC drive circuit

The time taken for the current (i_y) to rise from 0 to Ic as shown in Figure 3 can be obtained using (6) and can be expressed as (8).

$$t_{rise} = \frac{3LI_c}{2(V_{dc} - E_c)} \tag{8}$$

Using (2), (6), and the initial conditions, the equation of torque during commutation can be obtained. The expression for electromagnetic torque during commutation is given by (9).

$$T_{e_end} = \frac{2E_c}{\omega_m} \left[I_c + \frac{V_{dc} - 4E_c}{3L} t \right]$$
(9)

The torque ripple is the difference between the maximum and minimum electromagnetic torque of the BLDC motor [27-28]. Therefore, the torque ripple is expressed as (10). The relationship between DC link voltage and torque ripple can be described using (10).

$$\Delta T = \frac{2E_c}{\omega_m} \left[\frac{V_{dc} - 4E_c}{3L} t \right] \tag{10}$$

3. PROPOSED SCHEME FOR BLDC DRIVE

The schematic diagram of the proposed brushless DC motor drive has been presented in Figure 3. The proposed BLDC drive comprises of SEPIC derived topology integrated with inverter. The proposed DC-DC converter is derived from the conventional SEPIC by connecting switched capacitor cell with it. Unlike classical switched-capacitor based SEPIC converter presented in [22], the proposed converter comprises of one switch, which is required to be controlled.

3.1. Switched-capacitor based SEPIC

The circuit diagram of the newly proposed switched capacitor based SEPIC converter has been presented in Figure 4. The mode of operation of the circuit can be divided into two types: i) continuous conduction mode and ii) discontinuous conduction mode. For brushless DC motor drive operation, we have considered only the continuous conduction mode. The continuous conduction mode of operation can be divided into two more modes based upon the switch on and off state of the switch. The circuit operation during switch on and off mode has been shown in the Figures 5 and 6 respectively.

The relationship between the input and output voltage of the proposed converter is given in (11). In (11), D is the duty ratio, that is equal to the ratio of time the switch is on to the total time period. By changing the operating time of the switch S_1 of the converter, the output voltage obtained from the converter can be changed. This V_0 is fed to the inverter of BLDC drive and acts as the DC link voltage.

$$V_o = \frac{2-D}{(1-D)} V_i$$
(11)



Figure 4. Switched capacitor-based DC-DC converter



Figure 5. Mode 1 of switched capacitor-based DC-DC converter



Figure 6. Mode 2 of switched capacitor-based DC-DC converter

3.2. Estimation of torque ripple in the proposed BLDC drive

The proposed DC-DC converter is integrated with inverter to run the BLDC drive. Substituting V_{dc} from (11) in (10), we get (12).

$$\Delta T = \frac{2E_c}{\omega_m} \left[\frac{\frac{(2-D)V_{in}}{(1-D)} - 4E_c}{3L} t \right]$$
(12)

D 31

The equation for torque ripple after the integration of the modified DC-DC converter has been presented in (12). The proposed brushless DC motor drive is operated at different duty ratio of the DC-DC converter. The commutation torque ripple obtained can be calculated using (12). It has been observed that with the change in duty ratio the commutation torque ripple changes. The (10) shows that with change of DC-DC converter, the DC link voltage will change and so does the torque ripple. Therefore, the choice of converter for BLDC drive will become easier for future studies.

4. RESULTS AND DISCUSSION

The proposed DC-DC converter shown in Figure 4, has been designed for an operating frequency of 50 kHz, taking L_1 as 500 µH, C_1 as 5 µF, C_{21} and C_{22} as 5 µF and C_0 as 1 µF. In order to analyze the performance of the proposed converter, a comparative analysis has been performed between single-ended primary-inductor converter (SEPIC) [20], ZETA [21], and SC based ZETA [22]. Table 1 shows the comparative analysis of the output voltage of the proposed converter with the above-mentioned converters. The input voltage has been varied from 5 to 30 volt, keeping the duty ratio constant at 65%. The obtained outcome has been presented in Table 1. The total harmonic distortion of the output voltage for all the converters has also been observed for different duty ratio. Table 2 represents the comparative analysis for the mentioned converters at 50 kHz frequency.

Table 1. A comparative analysis of output voltages of converters presented in [20]–[23] and the proposed converter for D = 65%

Vin (Volt)	SEPIC Vout (Volt) [20]	ZETA Vout (Volt) [21]	SC based ZETA Vout (Volt) [22]	Proposed Converter Vout (Volt)				
5	11.16	9.45	15.85	16.21				
10	17.87	19.31	33.19	34.75				
15	27.16	29.15	50.53	53.29				
20	36.45	39	67.88	71.82				
25	45.73	48.85	85.22	90.36				
30	55.02	58.7	102.6	108.9				

Table 2. Comparison of converter topologies

Parameters	SEPIC [20]	ZETA [21]	SC based ZETA [22]	Modified SC based SEPIC			
Active switches	1	1	1	1			
Vo/Vi at $D = 0.65$	1.8	1.95	3.39	3.6			
THD% at Vo for $Vin = 20$ volt	22.37	14.79	14.79	11.92			

The comparative analysis presented in Table 2 has been done for an input of 20 volt at a duty ratio of 65%. The output voltage obtained using the proposed converter for an input of 20 voltage is 72 volts with total harmonic distortion of 11.92%, which is a much better output as compared to the previously existing converters presented in [20]–[22]. The simulation of the proposed drive is presented in Figure 7. An experimental setup has been developed for verifying the effectiveness of the proposed drive. The experimental test setup for the BLDC drive has been presented in Figure 8. The inverter has been supplied with the output from the DC-DC converter. The V_{dc} applied to the inverter is varied from 12 to 15 volt. The switches used in the inverter circuit are controlled with the help of signals generated by Arduino-UNO. The designed proposed DC-DC converter is applied to generate the V_{dc} voltage. The stator back EMF of the BLDC motor is been observed in the oscilloscope. The obtained stator back EMF waveform for one of the three phases in the proposed drive with 15-volt V_{dc} has been presented in the Figure 9.

As mentioned above, the (12) shows that by varying the duty ratio, the DC link voltage can be controlled, which in turn helps in controlling the ripple in electromagnetic torque of BLDC drive. The Table 3 presents the variation of torque ripple and commutation time with respect to the change in duty ratio of the proposed converter. Moreover, the torque ripple is defined as (14). It can be observed from Table 3 that, with the increase of duty ratio, the commutation time decreases. The torque ripple can be reduced to 16.5% using the proposed simple BLDC drive.

$$\Delta T = T_{MAX} - T_{MIN} \tag{13}$$

$$T_{ripple} = \frac{\Delta T}{T_{avg}} \tag{14}$$

Figure 10(a) illustrates the output current waveform and electromagnetic torque of the proposed drive at a duty ratio of 65%. The BLDC drive has also been operated without using DC-DC converter and the waveform of phase currents and torque is presented in Figure 10(b). The torque ripple obtained in BLDC

drive, when operated without using DC-DC converter is 44.9%. It is observed from Table 3 that with the integration of the proposed converter, torque ripple percentage has been reduced down to 16.5% at a duty ratio of 85% which is much less as compared to the BLDC drive without any DC-DC converter.

Table 3. Analysis of commutation time and ripple for proposed drive								
Duty ratio	Commutation time	Change in torque	Average torque	Torque ripple				
	(µsec)	(ΔT)	(Nm)	(%)				
0.55	637.926	0.8318	2.983	27.88468				
0.6	602.33	0.799	2.976	26.84812				
0.65	685.7	0.7604	3.055	24.89034				
0.7	544.41	0.8554	3.155	27.11252				
0.75	400.59	0.7354	3.288	22.36618				
0.8	372.3	0.7213	3.475	20.75683				
0.85	272.39	0.5863	3.55	16.51549				



Figure 7. Simulation of the proposed drive



Figure 8. Experimental test setup



Figure 9. Stator back EMF Waveforms



Figure 10. Waveforms of BLDC drive with (a) proposed converter obtained at duty ratio 65% and (b) without DC-DC converter

D 33

From the results we may say that, the proposed DC-DC converter offers higher gain of about 7 times the input at a lower duty ratio of 65%. To maintain a lower torque ripple in BLDC, drive high gain converter is a simple solution. Thus, the proposed converter which offers lower THD helps maintain high voltage gain with lower voltage stress across the switch and in turn helps to minimize torque ripple. The proposed drive offers better solution to torque ripple suppression as compared to the complicated modulation technique presented in [8] and complex circuit analysis presented in [26].

5. CONCLUSION

In this study, a modified switched capacitor-based DC-DC converter has been presented to suppress the commutation torque ripple of BLDC motor drive and in previous section it was compared with other such converter fed drives. The main contribution of this paper lies with the mathematical analysis of the cause of torque ripple and the condition for refraining torque ripple. The detailed analysis of the proposed converter fed BLDC drive is presented and in section 4 experimental results are provided. The results obtained shows that the ripple obtained in the electromagnetic torque of the BLDC drive can be minimized with the application of proposed DC-DC converter without using any complicated control techniques. It can be seen that at duty ratio of 55% the torque ripple is 27.88% and with increase in duty ratio to 85%, the torque ripple reduces to 16.27%. The torque ripple in electromagnetic torque can be minimized by simply modulating the converter duty ratio.

REFERENCES

- A. Rajeev VK and V. Prasad, "Online Adaptive Gain for Passivity-Based Control for Sensorless BLDC Motor Coupled With DC Motor for EV Application," *IEEE Trans. Power Electron.*, vol. 38, no. 11, pp. 13625–13634, Nov. 2023, doi: 10.1109/TPEL.2023.3288939.
- [2] J. D. Navamani, A. Geetha, D. Almakhles, A. Lavanya, and J. S. M. Ali, "Modified LUO High Gain DC-DC Converter With Minimal Capacitor Stress for Electric Vehicle Application," *IEEE Access*, vol. 9, pp. 122335–122350, 2021, doi: 10.1109/ACCESS.2021.3109273.
- [3] D. S. Nayak and R. Shivarudraswamy, "A solar fed BLDC motor drive for mixer grinder using a buck converter," Int. J. Electr. Comput. Eng., vol. 10, no. 2, pp. 1113–1121, Apr. 2020, doi: 10.11591/ijece.v10i2.pp1113-1121.
- [4] L. Yang, Z. Q. Zhu, H. Bin, and L. Gong, "Spectral Analysis and Sideband Harmonic Cancellation of Six-Step Operation With Low Carrier–Fundamental Frequency Ratio for High-Speed Brushless DC Drives," *IEEE Trans. Ind. Electron.*, vol. 68, no. 9, pp. 7778–7792, Sep. 2021, doi: 10.1109/TIE.2020.3018069.
- [5] M. Sumega, Š. Zoššák, P. Varecha, and P. Rafajdus, "Sources of torque ripple and their influence in BLDC motor drives," *Transp. Res. Procedia*, vol. 40, pp. 519–526, 2019, doi: 10.1016/j.trpro.2019.07.075.
- [6] J.-H. Song and I. Choy, "Commutation Torque Ripple Reduction in Brushless DC Motor Drives Using a Single DC Current Sensor," *IEEE Trans. Power Electron.*, vol. 19, no. 2, pp. 312–319, Mar. 2004, doi: 10.1109/TPEL.2003.823177.
- [7] C.-L. Huang and S.-C. Yang, "Torque Ripple Reduction for BLDC Permanent Magnet Motor Drive using DC-link Voltage and Current Modulation," in 2021 IEEE International Future Energy Electronics Conference (IFEEC), IEEE, Nov. 2021, pp. 1–4. doi: 10.1109/IFEEC53238.2021.9661795.
- [8] J. Hu, D. Yang, and Z. Sun, "Torque Fluctuation Suppression Strategy of Integrated Electric Drive System Based on the Principle of Minimization of Instantaneous Current Tracking Error," *IEEE Access*, vol. 10, pp. 124673–124684, 2022, doi: 10.1109/ACCESS.2022.3225103.
- [9] Z. Li, Q. Kong, S. Cheng, and J. Liu, "Torque ripple suppression of brushless DC motor drives using an alternating two-phase and three-phase conduction mode," *IET Power Electron.*, vol. 13, no. 8, pp. 1622–1629, Jun. 2020, doi: 10.1049/iet-pel.2019.0960.
- [10] A. A. A. Ismail and A. Elnady, "Advanced Drive System for DC Motor Using Multilevel DC/DC Buck Converter Circuit," *IEEE Access*, vol. 7, pp. 54167–54178, 2019, doi: 10.1109/ACCESS.2019.2912315.
- [11] J. Cai, X. Zhang, W. Zhang, and Y. Zeng, "An Integrated Power Converter-Based Brushless DC Motor Drive System," IEEE Trans. Power Electron., vol. 37, no. 7, pp. 8322–8332, Jul. 2022, doi: 10.1109/TPEL.2022.3144897.
- [12] S. Sun et al., "Novel modulation method for torque ripple suppression of brushless DC motors based on SIMO DC-DC converter," J. Power Electron., vol. 20, pp. 720–730, May 2020, doi: 10.1007/s43236-020-00071-1.
- [13] K. Mahalingam and N. K. C. Ramji, "A comparative analysis of torque ripple reduction techniques for sensor BLDC drive," *Int. J. Power Electron. Drive Syst.*, vol. 13, no. 1, pp. 122–131, Mar. 2022, doi: 10.11591/ijpeds.v13.i1.pp122-131.
 [14] W. Jiang, Y. Liao, J. Wang, P. Wang, and Y. Xie, "Improved Control of BLDCM Considering Commutation Torque Ripple and
- [14] W. Jiang, Y. Liao, J. Wang, P. Wang, and Y. Xie, "Improved Control of BLDCM Considering Commutation Torque Ripple and Commutation Time in Full Speed Range," *IEEE Trans. Power Electron.*, vol. 33, no. 5, pp. 4249–4260, May 2018, doi: 10.1109/TPEL.2017.2717484.
- [15] S. Saha, S. Modal, M. Chattopadhyay, and M. Mukherjee, "A Scheme for Torque Ripple Minimization in BLDC Drive Using Two-Inductor Boost Converter," in 2022 IEEE International Conference of Electron Devices Society Kolkata Chapter (EDKCON), IEEE, Nov. 2022, pp. 1–6. doi: 10.1109/EDKCON56221.2022.10032907.
- [16] R. A. Raj, D. S. Nair, S. MP, and S. George, "A Novel High-Voltage Gain Circuit Topology for Commutation Torque Ripple Reduction," *IEEE Trans. Ind. Appl.*, vol. 58, no. 5, pp. 6227–6236, Sep. 2022, doi: 10.1109/TIA.2022.3186289.
- [17] R. Heidari and J.-W. Ahn, "Torque Ripple Reduction of BLDC Motor with a Low-Cost Fast-Response Direct DC-Link Current Control," *IEEE Trans. Ind. Electron.*, vol. 71, no. 1, pp. 150–159, Jan. 2024, doi: 10.1109/TIE.2023.3247732.
- [18] N. Mohanraj and R. Sankaran, "Converter Control Strategy for Torque Ripple Minimization in BLDC Motor," *Energy Proceedia*, vol. 117, pp. 951–957, Jun. 2017, doi: 10.1016/j.egypro.2017.05.215.
- [19] S. Arunkumar and S. Thangavel, "A Review Paper on Torque Ripple Reduction in Brushless DC Motor Drives with Different Multilevel Inverter Topology," *TELKOMNIKA Indones. J. Electr. Eng.*, vol. 13, no. 1, p. 6904, Jan. 2015, doi: 10.11591/telkomnika.v13i1.6904.
- [20] F. I. Kravetz and R. Gules, "Soft-Switching High Static Gain Modified SEPIC Converter," IEEE J. Emerg. Sel. Top. Power

34

Electron., vol. 9, no. 6, pp. 6739-6747, Dec. 2021, doi: 10.1109/JESTPE.2021.3079573.

- [21] M. Bindi et al., "Failure Prevention in DC–DC Converters: Theoretical Approach and Experimental Application on a Zeta Converter," IEEE Trans. Ind. Electron., vol. 70, no. 1, pp. 930–939, Jan. 2023, doi: 10.1109/TIE.2022.3153827.
- [22] B. Axelrod, Y. Berkovich, and A. Ioinovici, "Hybrid Switched-Capacitor Cuk/Zeta/Sepic Converters in Step-Up Mode," in 2005 IEEE International Symposium on Circuits and Systems, IEEE, pp. 1310–1313. doi: 10.1109/ISCAS.2005.1464836.
- [23] X. Wu, J. Wang, Y. Zhang, and Z. Liu, "Switched-capacitor-based high-gain DC-DC converter for fuel cell vehicle powertrain," J. Power Electron., vol. 22, pp. 557–568, Apr. 2022, doi: 10.1007/s43236-022-00388-z.
- [24] W. Li, J. Fang, H. Li, and J. Tang, "Position Sensorless Control Without Phase Shifter for High-Speed BLDC Motors With Low Inductance and Nonideal Back EMF," *IEEE Trans. Power Electron.*, vol. 31, no. 2, pp. 1354–1366, Feb. 2016, doi: 10.1109/TPEL.2015.2413593.
- [25] J. S. Park and K.-D. Lee, "Design and Implementation of BLDC Motor with Integrated Drive Circuit," Int. J. Power Electron. Drive Syst., vol. 8, no. 3, pp. 1109–1116, Sep. 2017, doi: 10.11591/ijpeds.v8.i3.pp1109-1116.
- [26] B. Sirisha and L. Yalakanti, "A mitigation technique for torque ripple in a brushless DC motor by controlled switching of small DC link capacitor," *Bull. Electr. Eng. Informatics*, vol. 11, no. 2, pp. 1167–1176, Apr. 2022, doi: 10.11591/eei.v11i2.3408.
- [27] I. Etier, A. M. M. A., and N. Kannan, "MATLAB based design and performance analysis of electronically commutated BLDC motor," *Indones. J. Electr. Eng. Comput. Sci.*, vol. 24, no. 1, pp. 22–28, Oct. 2021, doi: 10.11591/ijeecs.v24.i1.pp22-28.

BIOGRAPHIES OF AUTHORS



Sunam Saha **b** K score completed her B.Tech. in the year 2012 on Electrical and Electronics Engineering and M.Tech. in the year 2016 on Power Electronics from Sikkim Manipal Institute of Technology, Sikkim. She has 10 years of teaching experience and currently working in Adamas University as an assistant professor in the department of electrical engineering. She is currently pursuing her Ph.D. Her area of research is power electronics converters and electrical machines. She can be contacted at email: sunamsaha.ee@gmail.com.



Debjyoti Chowdhury D S S received his B.Tech., M.Tech., and Ph.D. degrees from the West Bengal University of Technology, Kolkata, India. He is currently working as an assistant professor in the department of the applied electronics and instrumentation engineering, Heritage Institute of Technology. His current research interests include wearable sensors, motion analysis using body-worn sensors, and embedded systems. He can be contacted at email: debjyoti.chowdhury@heritageit.edu.



Madhurima Chattopadhyay 💿 🕺 🖾 🗘 received her B.Tech. and M.Tech. in Instrumentation Engineering from Jadavpur and Calcutta University, Kolkata, India, respectively, and her Ph.D. degree in Instrumentation from the Indian Institute of Science, Bangalore, India. She is currently a professor with the Applied Electronics and Instrumentation Engineering, Heritage Institute of Technology, where she is the head of the department. Her area of interest towards electrical machines, converters, microsensors, and electrical impedance spectroscopy. She can be contacted email: at madhurima.chattopadhyay@heritageit.edu.



Moumita Mukherjee b x c received her M.Sc. in Physics (Applied) from the University of Calcutta, and her M.Tech. in Biomedical Engineering from WB University of Technology. She received her Ph.D. (Technology) degree in Defense R&D Org., DRDO, under University of Calcutta. She is currently a professor & dean (research & development) at Adamas University. She can be contacted at email: moumita.mukherjee@adamasuniversity.ac.in.