

Optimal speed control of SRM with integration of switching variable proportional desaturation fuzzy logic regulator for EV application

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ABSTRACT

SRMs are recognized for their high efficiency, strong torque output, and capability to operate at high speeds, making them well-suited for electric vehicle (EV) applications. Their high initial torque effectively overcomes the vehicle's inertia during the drive mode, enhancing overall performance. Switched reluctance machines (SRMs) come in various structural configurations such as 8/6, 6/4, and 4/2, offering flexible options for different vehicle requirements. Speed control in SRMs is typically managed through current regulation, which is handled by a current controller. Traditionally, a proportional integral (PI) controller is used to generate the required reference current. However, the PI regulator is prone to high damping and sensitivity to disturbances, leading to increased speed overshoot during startup and longer settling times. To address these limitations, a switched variable proportional desaturation PI (SVPDPI) controller is employed. Nevertheless, due to its relatively slow response, the PI component in the SVPDPI is replaced with a fuzzy logic module to enhance controller performance. This results in a switched variable proportional desaturation fuzzy logic (SVPDFL) regulator, which significantly reduces initial speed overshoot and improves settling time toward the desired reference speed. This paper presents a comparative analysis of these controllers, with simulations conducted using MATLAB/Simulink to evaluate performance improvements.

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1. INTRODUCTION

Due to the rising environmental pollution caused by the combustion of fossil fuels such as petrol and diesel in transportation, the frequency of climate-related disasters is increasing globally. To prevent further environmental damage, it is essential to replace conventional internal combustion engine vehicles with zero-emission alternatives [1]. This shift can be effectively achieved through the adoption of electric vehicles (EVs), which operate entirely on electrical energy stored in onboard batteries [2]. These batteries power the electric motor, which drives the vehicle. Various types of electric machines can be used for traction in EVs, including induction machines, permanent magnet synchronous motors (PMSMs), and direct current (DC)

machines. However, these machines come with certain limitations, such as low starting torque, limited speed range, or high torque ripple. To address these challenges, the switched reluctance machine (SRM) is considered a suitable choice for EV drive applications. Table 1 highlights the advantages of SRM compared to induction machines and PMSMs [3].

Given these advantages, the SRM is highly suitable for EV applications. Its robust construction, high temperature tolerance, and cost-effectiveness enhance the overall reliability of the vehicle [4], [5]. For efficient operation, the speed of the SRM must be controlled while maintaining stable torque and minimizing the initial current peak and overshoot. This is achieved through current control using a power converter connected to the SRM. In this setup, the SRM is interfaced with the EV battery pack via three active hybrid bridges (AHBs), each configured with insulated gate bipolar transistor (IGBT) and diode switches in a full-bridge arrangement [5], [6]. The integration of the SRM with AHBs and the current control strategy is illustrated in Figure 1.

As illustrated in Figure 1, the battery pack supplies power to the AHBs, which are controlled by a feedback controller. This controller generates gate pulses for the AHB switches (S1–S6) based on the rotor position detected by the ‘Position Detector’ module [7]. The switching of the AHBs is synchronized with the rotor position and governed by the reference speed set within the controller. In conventional speed control schemes, a proportional-integral (PI) regulator is typically used to generate the reference current (I_{ref}). However, due to the underdamped nature of the PI regulator, the system experiences significant initial current overshoot and oscillations, which result in increased settling time and potential stress or damage to the motor [8].

To address these limitations, the traditional PI regulator is replaced with a switched variable proportional desaturation PI (SVPDPI) regulator. This improves current stability by reducing peak overshoot and decreasing the settling time [9]. However, since SVPDPI still incorporates a proportional integral (PI) component, it suffers from a relatively slow dynamic response [10]. To enhance responsiveness, this paper proposes an upgraded version of SVPDPI by integrating a fuzzy logic (FL) regulator, forming the switched variable proportional desaturation fuzzy logic (SVPDFL) controller [11]. The FL regulator employs a set of membership functions (MFs) and a rule base to dynamically generate the required reference signal, enabling faster and more adaptive control performance under varying system conditions [12].

This paper is structured as follows: i) Section 1 presents an introduction to the proposed SRM-integrated EV architecture; ii) Section 2 details the modelling of the proposed system along with the design of the speed controller; iii) Section 3 focuses on the development of the SVPDFL regulator for speed control, including the complete internal structure and rule base of the fuzzy logic module; iv) Section 4 provides a comprehensive analysis of the simulation results obtained using MATLAB Simulink, evaluating the performance of the proposed regulators; and v) Finally, section 5 concludes the paper by validating the effectiveness of the SVPDFL regulator as an optimal controller for SRM speed control. References cited throughout the paper are listed after section 5.

Table 1. Machine type comparison table

Parameter	SRM	Induction motor	PMSM
Rotor complexity	Very simple (no windings)	Moderate (squirrel cage)	Complex (magnets)
Cost	Low	Moderate	High (due to rare-earth)
Efficiency (Wide range)	High	Moderate	High
Temperature tolerance	High	Moderate	Low (magnet degradation)
Fault tolerance	Excellent	Poor	Poor

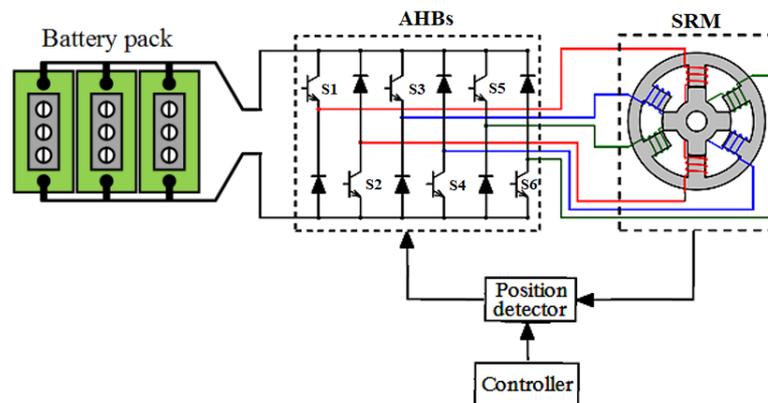


Figure 1. Structure of SRM integration into EV

2. PROPOSED SYSTEM

As described in section 1, the speed control of the SRM for EV applications is achieved through current regulation of the stator phases. To operate the switches of the AHBs, it is essential to determine the rotor position [13]. Based on the rotor’s position, gate pulses are generated to turn ON the switches connected to the corresponding stator pole windings. Once the rotor position is identified, the reference current amplitude required for stator pole excitation is computed [14]. This reference current is generated by the speed regulator, which calculates the appropriate value based on the difference between the reference and measured speeds. The complete control structure of the SRM speed controller, including the current reference generator and position sensor, is illustrated in Figure 2.

As shown in Figure 2, the position of each stator pole is detected using a position sensor, which is essential for generating pulse signals to control the switches in the asymmetric half bridge (AHB) [15]. The internal configuration of this position sensor is illustrated in Figure 3. As shown in Figure 3, the input to the position sensor is the measured speed of the SRM (ω rad/sec). This speed is converted into degrees to determine the rotor’s angular position (α°) [16]. The position angles corresponding to the three rotor phase poles are given as (1)-(3).

$$\theta_a = \text{mod}(90^\circ, \int \alpha^\circ) \tag{1}$$

$$\theta_b = \text{mod}(90^\circ, \int \alpha^\circ - 30^\circ) \tag{2}$$

$$\theta_c = \text{mod}(90^\circ, \int \alpha^\circ - 60^\circ) \tag{3}$$

The calculated rotor phase angles for each pole are compared against a defined angular range. A trigger signal is generated only when the rotor angle of any phase lies between 40° and 75° . This trigger signal is then multiplied by the reference current from the speed regulator, indicating the rotor position and activating the corresponding AHB switches for that phase. The reference current generated by the PI-based speed regulator is given as (4).

$$I_{ref} = (\omega^* - \omega) \left(k_p + \frac{k_i}{s} \right) \tag{4}$$

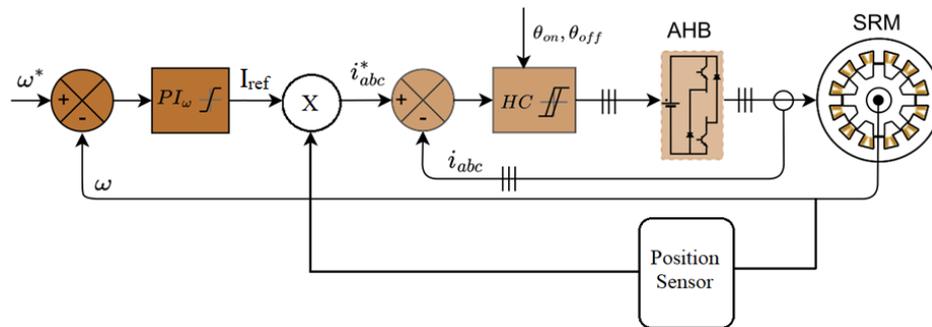


Figure 2. Speed control structure of SRM

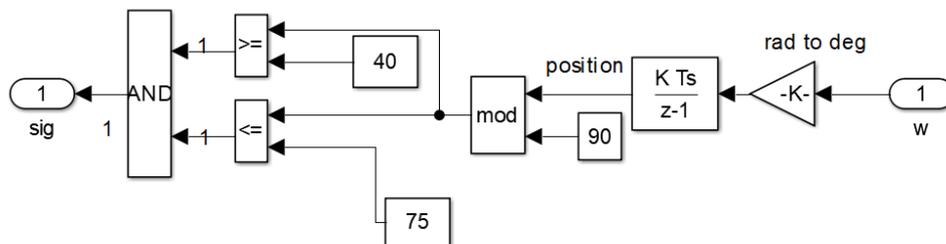


Figure 3. Position sensor internal structure

In (4), the reference speed (ω^*) is compared with the measured speed (ω) to produce a speed error [17]. This error is processed by a PI controller, which uses proportional gain (k_p) and integral gain (k_i), both tuned based on the system's dynamic response. While the PI regulator is known for its reliability, low cost, simplicity, and ease of implementation, it suffers from low damping, resulting in higher oscillations and peak overshoots in the output signals. Moreover, the PI controller struggles to maintain stable operation under external transients, disturbances, and non-linearities, limiting its effectiveness to only stable system models [18]. The integral gain saturation further restricts its performance, especially in non-linear systems. Due to its lack of adaptability to system variations and error dynamics, the traditional PI regulator is unsuitable for complex environments with frequent disturbances. To achieve better control over the SRM characteristics, it becomes necessary to replace the PI regulator with more advanced adaptive regulators. A comparison table between some categories of the controllers is presented in Table 2.

Table 2. Controller type comparison table

Category	PI	Fuzzy logic	Sliding PI	Fuzzy-PI
Type	Feedback linear control	Fuzzy rule-based non-linear control	Variable gain structure	Hybrid (rule-based variable gain) control
Adaptability	No adaptability (fixed gains)	Good adaptability	Medium adaptability	Optimal adaptability
Robustness during dynamic states	Very low	Moderate	High	Very high
Response time	Low	Moderate	Good	Optimal
Damping	Very low damping	Medium damping	High damping	Critical damping
Sensitivity to disturbances	Medium	Lower	High	Mitigated

3. SWITCHING VARIABLE PROPORTIONAL DESATURATION FUZZY LOGIC REGULATOR

In order to improve the performance of the SRM for EV applications, the SRM speed controller needs to be enhanced with adaptive regulators [19]. This enhancement needs to improve the speed and torque characteristics of the machine when operated with the same rating of the system. To achieve this SVPDPI regulator is integrated into the SRM speed controller, which generates the reference current value [20], [21]. The internal structure of the SVPDPI regulator is presented in Figure 4.

The input to the SVPDPI regulator in the speed error (w_e) is generated by comparison of the reference and measured speed of the SRM. The output reference current of the regulator is expressed as (5)-(7).

$$If\ w_e \leq s; I_{ref} = \begin{cases} w_e * \left(K_{P1} + \gamma \frac{Ki}{s} \right); & If\ w_e > c\ and\ w_e > \varphi \\ w_e * \left(K_{P1} + \frac{Ki}{s} \right); & If\ w_e > c\ and\ w_e \leq \varphi \\ w_e * \left(K_{P2} + \gamma \frac{Ki}{s} \right); & If\ w_e \leq c\ and\ w_e > \varphi \\ w_e * \left(K_{P2} + \frac{Ki}{s} \right); & If\ w_e \leq c\ and\ w_e \leq \varphi \end{cases} \quad (5)$$

$$If\ w_e > s; I_{ref} = w_e * \left(K_{P3} + \frac{Ki}{s} \right) \quad (6)$$

Here,

$$w_e = \omega^* - \omega \quad (7)$$

In (5) and (6), the K_{P1} , K_{P2} and K_{P3} are three different proportional gains, Ki is the integral gain, γ is the feedback compensation coefficient, φ and c are the threshold values, and 'S' is the selection coefficient [22]. These values are tuned as per the response of the system for the given reference value in the speed controller. However, in the structure of the SVPDPI regulator, due to the presence of the traditional PI regulator during $w_e > s$ condition, the response time of the regulator is restricted due to the underdamping of the PI regulator. This issue of the regulator is solved by replacing the traditional PI regulator with a fuzzy logic regulator during $w_e > s$ condition [23]. The updated SVPDFL regulator with fuzzy rules is presented in Figure 5.

As shown in Figure 5, the fuzzy logic module receives two input variables: the speed error (w_e) and the change in speed error (cw_e). The change in speed error is computed using a derivative block that calculates the difference between the current and previous values of the speed error [24]. Each input variable is fuzzified using seven membership functions (MFs), defined within specific ranges based on their respective minimum and maximum limits. The MFs are designed with a Gaussian ('gauss') shape to provide smoother transitions and enhanced flexibility across a wider operating range. The membership functions for the input variables ' w_e ' and ' cw_e ' are illustrated in Figures 6(a) and 6(b), respectively.

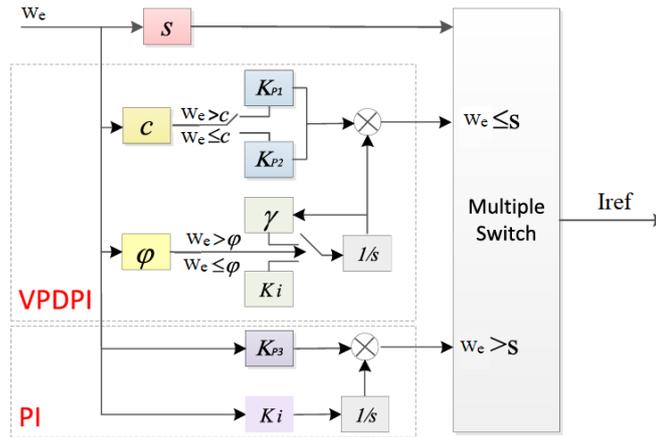


Figure 4. SVPDPI regulator internal structure

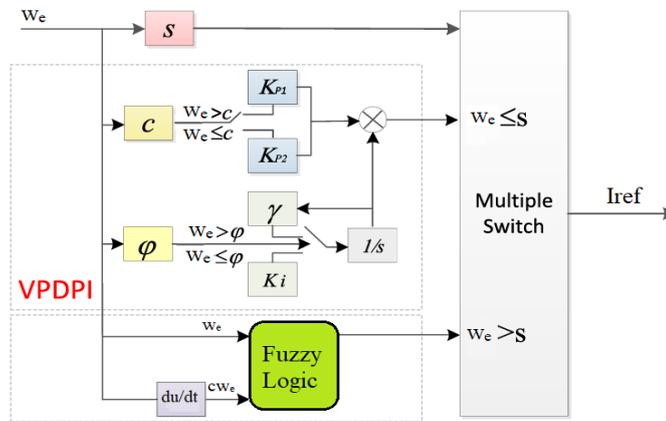


Figure 5. SVPDFL regulator internal structure

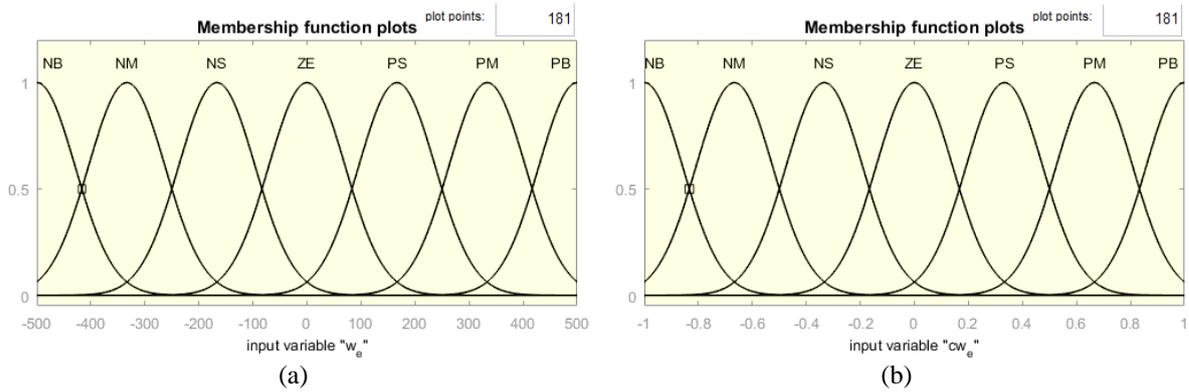


Figure 6. Fuzzy logic variables: (a) input variable ‘ w_e ’ MFs and (b) input variable ‘ cw_e ’ MFs

The membership functions (MFs) for the input variable ‘ w_e ’ are defined within the range of -500 to 500, representing the minimum and maximum speed error values. This reduced speed range is selected to enable a faster response from the controller. The MFs for the input variable ‘ cw_e ’ are set between -1 and 1, reflecting the typical minimum and maximum values produced by the derivative. Additionally, the output variable ‘ I_{ref} ’ is defined using seven triangular MFs to ensure precise output value generation. Figure 7 illustrates the MFs of the output variable.

As observed, the output variable ‘I_{ref}’ is defined within a range of -50 to 50, representing its minimum and maximum current values. The MFs for both input and output variables are named according to their position within this range [25]. On the negative side of the zero (ZE) MF, the functions are labelled as negative big (NB), negative medium (NM), and negative small (NS). On the positive side, they are labelled as positive small (PS), positive medium (PM), and positive big (PB). The fuzzy logic module operates based on a rule base consisting of 49 rules, as detailed in Table 3.

Based on the provided table, the output of the fuzzy logic module is generated according to the input signal range and the defined rule base. The updated SVPDFL regulator is then integrated into the SRM speed controller, and the resulting machine characteristics are analyzed and compared through simulation in the following section.

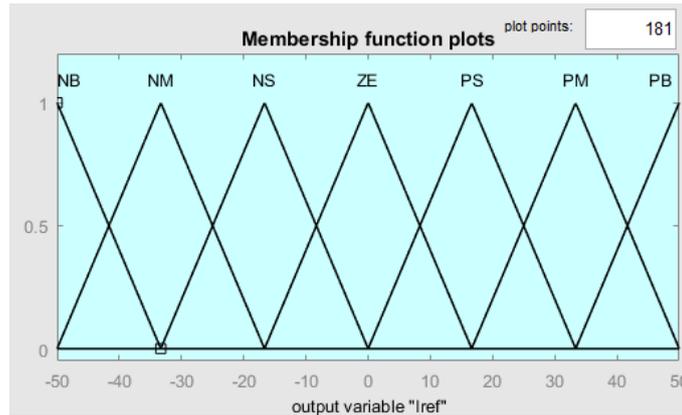


Figure 7. Output variable ‘I_{ref}’ MFs

Table 3. Rule base

49 rule-based		w _e						
		NB	NM	NS	ZE	PS	PM	PB
c _{w_e}	PB	Z	PS	PM	PB	PB	PB	PB
	PM	NS	Z	PS	PM	PB	PB	PB
	PS	NB	NS	Z	PS	PM	PB	PB
	ZE	NB	NM	NS	Z	PS	PM	PB
	NS	NB	NB	NM	NS	Z	PS	PM
	NM	NB	NB	NB	NM	NS	Z	PS
	NB	NB	NB	NB	NB	NM	NS	Z

4. RESULT ANALYSIS

The proposed system, incorporating SRM speed control with various regulators, is modelled in the MATLAB Simulink environment. All modules are developed using blocks from the 'Electrical' subset of the Simulink library browser. The AHB switches are selected from the 'Power Electronics' section, the SRM is sourced from 'Machines', and the controller components are taken from 'Commonly Used Blocks' and 'Signal Routing'. Each block is configured with parameters specified in Table 4. The simulation model is run with a fixed initial speed using the updated parameters, and the results obtained with different speed regulators are recorded and presented with respect to time.

Table 4. Configuration parameters

Name of the module	Parameters
DC source	V _{dc} = 240 V
AHB	R _{igbt} = 1 mΩ, R _{diode} = 1 mΩ
SRM	Type: 6/4, R _s = 0.05Ω, J = 0.05 kg.m ² , F = 0.02 N.m.s, Unaligned inductance = 0.67 mH, aligned Inductance = 23.62 mH, Saturated aligned inductance = 0.15 mH, I _{max} = 65 A, Φ = 0.4 V.s
Position sensor	Lower limit = 40°, Upper limit = 75°
Speed control	PI regulator: K _p = 10; K _i = 0.5 SVPDPI: K _{p1} = 0.1, K _{p2} = 1, γ = -14, K _i = 0.1, K _{p3} = 0.023, K _{ij} = 0.05, c = 230, φ = 100, s = 50

Figure 8 shows the position sensor trigger signals generated by comparison of three rotor pole angles with the given range. It can be seen that each pulse is 120 deg phase shift to each other. Figure 9 is the reference current signal comparison of PI, SVPDPI, and SVPDFL regulators.

The initial peak of the I_{ref} of the PI regulator is very high, which leads to increased settling time. Compared to PI regulator, the SVPDPI and SVPDFL regulators, I_{ref} signals have reduced initial overshoot and also the settling time. The initial peak overshoot of PI regulator is observed to be 8000, SVPDPI and SVPDFL regulator is 50. Settling time is noted to be 0.38 sec for PI regulator, 0.28 sec for the SVPDPI regulator, and 0.12 sec for the SVPDFL. Along with the initial peak overshoot and settling time, the ripple in the I_{ref} signal is also less for the proposed regulators.

Figure 10 presents the rules of the fuzzy logic module with input (e and ce) and output (I_{ref}) variables. The output of the fuzzy logic module is integrated into the SVPD regulator for adaptive control over the speed of the SRM. As the initial peak of the reference current for the PI regulator is high the torque of SRM is also very high at 1100 Nm, which can damage the machine. Limiting the reference current using adaptive desaturation regulators is necessary for control over the initial torque of the machine to avoid uncertainties. Figure 11 represents the SRM electromagnetic torque, which shows the initial value of the SVPDPI and SVPDFL regulators to be 70 Nm.

Figure 12 is the comparison of flux of the SRM, which can be observed to be in given limits with a maximum flux value of 0.3 V.s. After the speed settles to the reference desired value, the flux drops to 0.1 V.s and maintains the same throughout the simulation. After the speed settles, the flux magnitude is maintained the same for all the regulators. As observed in Figure 13, the speed of the SRM with the SVPDFL regulator integrated SRM speed controller has no overshoot and a lower settling time as compared to PI and SVPDPI regulators. A parameter comparative table with SRM characteristics is presented in Table 5.

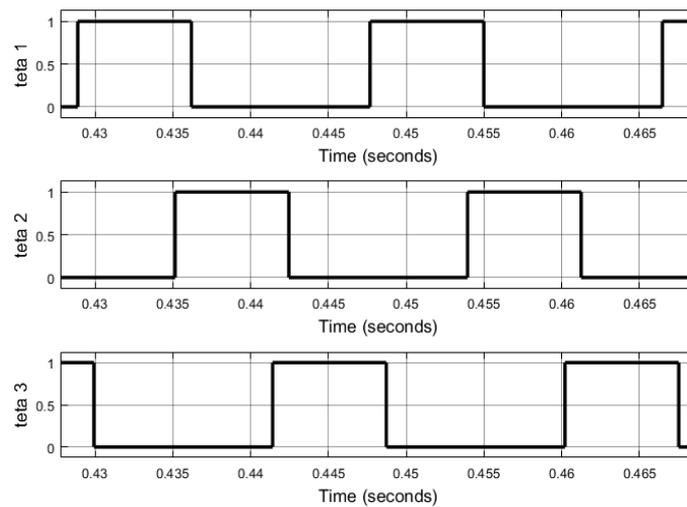


Figure 8. Position sensor trigger signals

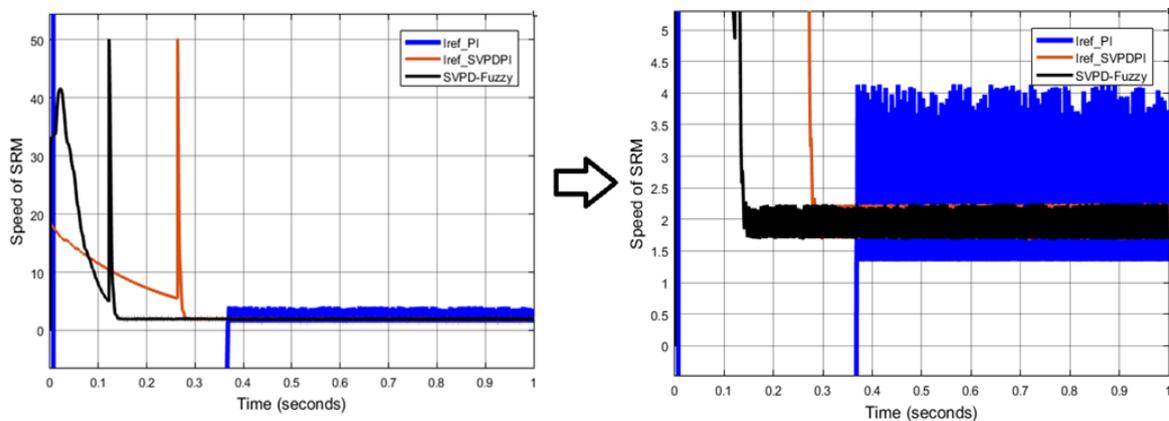


Figure 9. I_{ref} signal generated by regulators (a) full simulation time plot and (b) zoomed plot

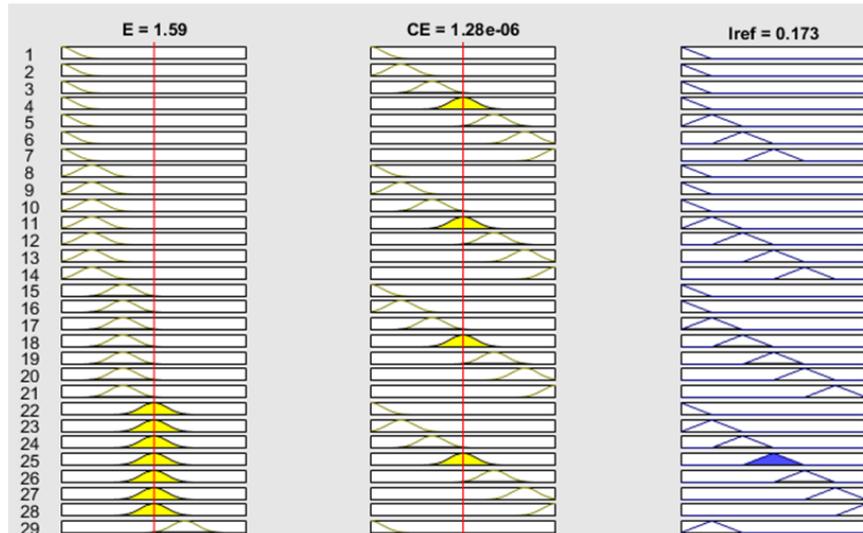


Figure 10. Fuzzy output with respect to the rule base

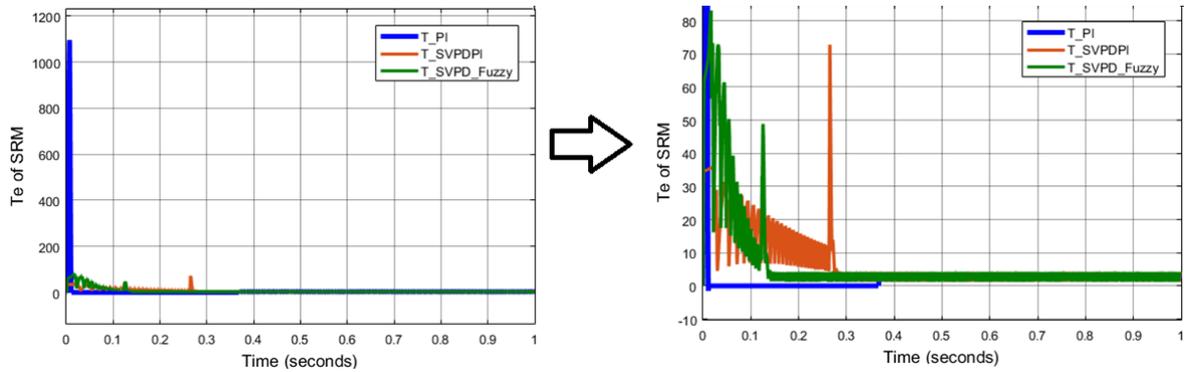


Figure 11. Comparison of electromagnetic torque of SRM: (a) full simulation time plot and (b) zoomed plot

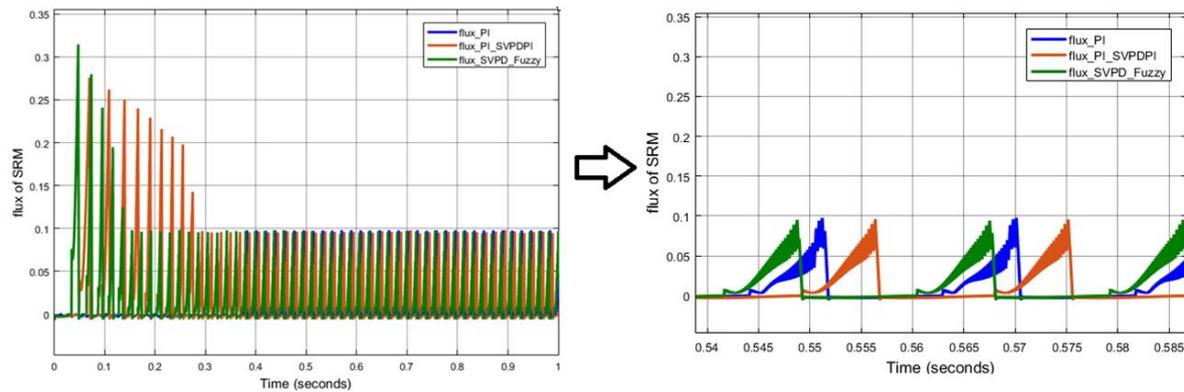


Figure 12. Comparison of flux of SRM: (a) full simulation time plot and (b) zoomed plot

Table 5. Comparison table

Name of the parameter	PI	SVPDPI	SVPDFL
Iref peak	8000 A	50 A	50 A
Iref ripple	60%	20%	20%
Te peak	1100 Nm	70 Nm	70 Nm
Te settling time	0.38 sec	0.28 sec	0.12 sec
Speed peak	1020 rpm	No peak	No peak
Speed settling time	0.35 sec	0.28 sec	0.12 sec

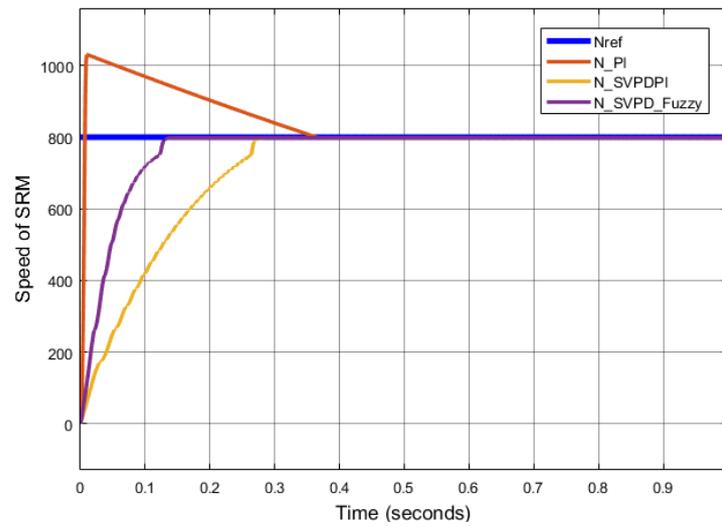


Figure 13. Comparison of SRM speeds

5. CONCLUSION

The speed control of the SRM is implemented in the MATLAB Simulink environment, with performance analysis conducted through graphical representations. Initially, the use of a conventional PI regulator resulted in an underdamped response, causing significant initial peaks and ripples in the SRM’s current, torque, and speed. To address this issue, the PI regulator was replaced with an SVPDPI regulator, which enhanced damping and controller responsiveness, thereby reducing peak overshoot in these parameters. However, since the SVPDPI regulator still incorporates a traditional PI component, the improvement in response time was limited. To overcome this limitation, the traditional PI regulator was replaced with a fuzzy logic module within the SVPDPI structure. This modification significantly enhanced the controller’s response time, eliminating overshoots and ensuring faster settling of the SRM speed. The graphical results confirm improved machine performance, with reduced current peaks and torque ripples, thereby increasing system reliability.

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AUTHOR CONTRIBUTIONS STATEMENT

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C : **C**onceptualization
 M : **M**ethodology
 So : **S**oftware
 Va : **V**alidation
 Fo : **F**ormal analysis

I : **I**nvestigation
 R : **R**esources
 D : **D**ata Curation
 O : **O** Writing - **O**riginal Draft
 E : **E** Writing - **R**eview & **E**ditting

Vi : **V**isualization
 Su : **S**upervision
 P : **P**roject administration
 Fu : **F**unding acquisition

CONFLICT OF INTEREST STATEMENT

Authors state no conflict of interest.

DATA AVAILABILITY

The authors confirm that the data supporting the findings of this study are available within the article [and/or its supplementary materials].

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