Sliding mode Control using 3D-SVM for Three-phase Four-Leg Shunt Active Filter

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Article Info	ABSTRACT	
Article history:	This paper proposes a sliding mode control strategy for a three-phase shunt	
Received Jan 8, 2013 Revised Apr 22, 2013 Accepted May 17, 2013	active power filter. The SAPF consists of four-leg voltage source inverter bridge. The SAPF ensures full compensation for harmonic phase currents, harmonic neutral current, reactive power compensation and unbalanced nonlinear load currents. The modulation task can be carried out with three dimensional space vector modulation, which operate under a constant switching frequency. The simulation results show that the performance of the four-leg SAPF with the proposed control algorithm – compared with PI	
Keyword:		
Sliding mode control Four-leg shunt active power filter	controller - is found considerably effective and adequate to compensate harmonics, reactive power, neutral current and balance load currents.	
3D-Space vector modulation	Copyright \bigcirc 2013 Institute of Advanced Engineering and Science. All rights reserved.	
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1. INTRODUCTION

One of the most important problems to be solved in power systems, in recent years, is the linecurrent harmonics problem. It is well known that harmonic currents, in the case of sinusoidal line voltage, do not contribute to active power [1]. The same holds for the reactive current. The undesired current components cause stress to the power system, generating disturbed fundamental and harmonic voltage drops in the network impedances. The active power filter (APF) is a common approach to eliminate the undesired current components by injection of compensation currents in opposition to them [1].

The distribution systems are inherently unbalanced because of untransposed distribution lines and unbalanced loads. Therefore maintaining the distribution voltage within certain limits of the perfectly balanced waveform is quite difficult. In addition, if the phases are unequally loaded, they produce undesired negative and zero sequence currents. Under nonlinear voltage and current, the harmonic current have contribution to active power [2].

The negative sequence will cause excessive heating in machines and low-frequency ripples in rectifiers [2, 3]. The zero sequence currents cause not only excessive power losses in neutral lines, but also degrade the circuit protection [3].

For full compensation capability a four-leg inverter connected as a shunt APF (SAPF) can be used [4]. In this configuration, as shown in Fig. 1, the compensated neutral current is provided through a fourth leg allowing a better controllability than the three-leg with split-capacitor configuration [5]. The main advantage of the four-leg configuration is the ability to suppress the neutral current from the source without any drawback in the filtering performance [5].

A large number of control strategies applied to APF have been reported; however the sliding mode control (SMC) seems to be the most appropriate one, because the time varying nature of an APF makes it suitable to be controlled by a variable structure approach such as the SMC [6]-[9].

Furthermore, the robustness characteristic and the simplicity of the implementation make the sliding mode control particularly attractive. In addition to the abovementioned features, the chattering caused by the switching operation, which is commonly considered a weakness of sliding mode control, is an intrinsic characteristic of power electronic converters, and hence unrelated to the control methodology.

In this work a three-phase four-leg SAPF is considered to compensate harmonic distortion, reactive power and current unbalance caused by the nonlinear load. The state space model of the SAPF, developed in the stationary $\alpha\beta$ o frame, is used to obtain the desired closed loop dynamics and to generate the reference inverter voltage by applying a sliding mode control strategy.

2. CONTROL DESIGN

The basic operation of the proposed control method is shown in Fig. 1. It consists of a four-leg inverter connected at the PCC to a three-phase four-wire grid through the interface inductances. The switch control signals are derived from a 3D-SVM. The voltage references for the 3D-SVM are derived from a sliding mode current controller. The reference currents are computed by using the instantaneous p-q theory. The compensation objective is to compensate for load current harmonics, reactive power compensation, neutral current elimination, and to regulate the dc bus during bidirectional active power exchange between the DC side load/source and the power system grid.

2.1. Mathematical Model of Four-leg PWM inverter

The differential equations describing the dynamic model of the inverter are defined in α - β -o reference frame, as given in equation (1).

$$\frac{di_{F\alpha}}{dt} = \frac{1}{L_F} (v_{F\alpha} - v_{\alpha} - R_F i_{F\alpha})$$

$$\frac{di_{F\beta}}{dt} = \frac{1}{L_F} (v_{F\beta} - v_{\beta} - R_F i_{F\beta})$$

$$\frac{di_{Fo}}{dt} = \frac{1}{L_F} (v_{Fo} - v_{o} - R_F i_{Fo})$$

$$\frac{dv_{dc}}{dt} = \frac{p_{dc}}{Cv_{dc}}$$
(1)

Where, v_{α} , v_{β} and v_{o} are voltages of (PCC) in the α - β -o coordinates, $i_{F\alpha}$, $i_{F\beta}$ and i_{Fo} are the α - β -o axis AC current of SAPF, and $v_{F\alpha}$, $v_{F\beta}$ and v_{Fo} are the AC side voltages of four-leg SAPF.

2.2. The p-q Theory Based Control Strategy

Instantaneous active and reactive powers of the nonlinear load are calculated as:

$$\begin{bmatrix} p_L \\ q_L \end{bmatrix} = \begin{bmatrix} v_{\alpha} & v_{\beta} \\ -v_{\beta} & v_{\alpha} \end{bmatrix} \begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix}$$
(2)



Figure 1. Sliding mode control scheme of four-leg SAPF

The instantaneous active and reactive powers include AC and DC values and can be expressed as follows:

$$p_L = \bar{p}_L + \tilde{p}_L$$

$$q_L = \tilde{q}_L + \tilde{q}_L$$
(3)

DC values (\bar{p}_L, \bar{q}_L) of the p_L and q_L are the average active and reactive power originating from the positive-sequence component of the nonlinear load current. AC values $(\tilde{p}_L, \tilde{q}_L)$ of the p_L and q_L are the ripple active and reactive powers.

For harmonic, reactive power compensation and balancing of unbalanced 3-phase load currents, all of the reactive power (\bar{q}_L and \tilde{q}_L components) and harmonic component (\tilde{p}_L) of active power are selected as compensation power references and the compensation currents reference are calculated as (4).

$$\begin{bmatrix} i_{F\alpha \ ref} \\ i_{L\beta \ ref} \end{bmatrix} = \frac{1}{v_{\alpha}^{2} + v_{\beta}^{2}} \begin{bmatrix} v_{\alpha} & \neg v_{\beta} \\ v_{\beta} & v_{\alpha} \end{bmatrix} \begin{bmatrix} \tilde{p}_{L} - p_{dc} \\ q_{L} \end{bmatrix}$$
(4)

The signal p_{dc} is used as an average real power and is obtained from the DC voltage controller. Since the zero-sequence current must be compensated, the reference of homopolar current is given as:

$$i_{F0 ref} = i_{L0} \tag{5}$$

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2.3. Sliding Mode Controller Synthesis

From equations (1), it is obvious that the converter is a nonlinear and coupled system. So a nonlinear controller based on the sliding mode method is developed in this section. The system (1) is subdivided in four subsystems as follows:

Subsystem 1:

The first subsystem is characterized by only one state $x = v_{dc}$ and only one control input $u_1 = p_{dc}$.

$$\frac{dv_{dc}}{dt} = \frac{p_{dc}}{Cv_{dc}} \tag{6}$$

The equation (6) can be written as follow:

$$\begin{cases} \dot{x}_{1} = L_{f}h_{1} + L_{g}h_{1}u_{1} \\ y_{1} = h_{1}(x) = v_{dc}, \quad y_{1d} = v_{dc}^{*} \end{cases}$$
(7)

Where:

$$x_1 = v_{dc}, \quad u = p_{dc}, \quad L_f h_1 = 0, \ L_g h_1 = \frac{1}{C v_{dc}}$$

Subsystem 2:

The second subsystem is also characterized by only one state $x = i_{F\alpha}$ and only one control input $u = v_{F\alpha}$.

$$\frac{di_{F\alpha}}{dt} = \frac{1}{L_F} (v_{F\alpha} - v_{\alpha} - R_F i_{F\alpha}) \tag{8}$$

The equation (8) can also be written as follow:

$$\begin{cases} \dot{x}_2 = L_f h_2 + L_g h_2 u_2 \\ y_2 = h_2 = i_{F\alpha}, y_{2d} = i_{F\alpha}^* \end{cases}$$
(9)

Where:

$$x_2 = i_{F\alpha}, \quad L_f h_2(x) = \frac{1}{L_F} (-v_\alpha - R_F i_{F\alpha}), \ L_g h_2 = \frac{1}{L_F}, \ u_2 = v_{F\alpha}$$

Subsystem 3:

The third subsystem is characterized by one state $x = i_{F\beta}$ and one control input $u = v_{F\beta}$.

$$\frac{di_{F\beta}}{dt} = \frac{1}{L_F} (v_{F\beta} - v_{\beta} - R_F i_{F\beta}) \tag{10}$$

The equation (10) can also be written as follow:

$$\begin{cases} \dot{x}_{3} = L_{f}h_{3} + L_{g}h_{2}u_{3} \\ y_{3} = h_{3} = i_{F\beta}, y_{3d} = i_{F\beta}^{*} \end{cases}$$
(11)

Where:

$$x_3 = i_{F\beta}, \quad L_f h_3(x) = \frac{1}{L_F} (-v_\beta - R_F i_{F\beta}), L_g h_3 = \frac{1}{L_F}, u_3 = v_{F\beta}$$

Subsystem 4:

The fourth subsystem is characterized by one state $x = i_{Fo}$ and one control input $u = v_{Fo}$.

$$\frac{di_{Fo}}{dt} = \frac{1}{L_F} (v_{Fo} - v_o - R_F i_{Fo})$$
(12)

The fourth subsystem (12) can also be written as follow:

$$\begin{cases} \dot{x}_4 = L_f h_4 + L_g h_2 u_4 \\ y_4 = h_4 = i_{F_0}, y_{4d} = i_{F_0}^* \end{cases}$$
(13)

Where

$$x_4 = i_{F0}, \quad L_f h_4(x) = \frac{1}{L_F} (-v_0 - R_F i_{F0}), L_g h_4 = \frac{1}{L_F}, u_4 = v_{Fo}$$

For v_{dc} , i_{α} , i_{β} and i_{o} , the surfaces S_{1} , S_{2} , S_{3} and S_{4} are given by the following expression:

$$S_{1} = k_{1} \left(v_{dc} - v_{dc}^{*} \right) + k_{i1} \int \left(v_{dc} - v_{dc}^{*} \right) dt$$

$$S_{2} = k_{2} \left(i_{F\alpha} - i_{F\alpha}^{*} \right) + k_{i2} \int \left(i_{F\alpha} - i_{F\alpha}^{*} \right) dt$$

$$S_{3} = k_{3} \left(i_{F\beta} - i_{F\beta}^{*} \right) + k_{i3} \int \left(i_{F\beta} - i_{F\beta}^{*} \right) dt$$

$$S_{4} = k_{4} \left(i_{Fo} - i_{Fo}^{*} \right) + k_{i4} \int \left(i_{Fo} - i_{Fo}^{*} \right) dt$$
(14)

And consequently, their temporal derivatives are given by:

$$\begin{split} \dot{S}_{1} &= k_{1} \frac{d}{dt} \left(v_{dc} - v_{dc}^{*} \right) + k_{i1} \left(v_{dc} - v_{dc}^{*} \right) \\ \dot{S}_{2} &= k_{2} \frac{d}{dt} \left(i_{F\alpha} - i_{F\alpha}^{*} \right) + k_{i2} \left(i_{F\alpha} - i_{F\alpha}^{*} \right) \\ \dot{S}_{3} &= k_{3} \frac{d}{dt} \left(i_{F\beta} - i_{F\beta}^{*} \right) + k_{i3} \left(i_{F\beta} - i_{F\beta}^{*} \right) \\ \dot{S}_{4} &= k_{4} \frac{d}{dt} \left(i_{Fo} - i_{Fo}^{*} \right) + k_{i4} \left(i_{Fo} - i_{Fo}^{*} \right) \end{split}$$
(15)

The equivalent control can be calculated from the formula $\dot{S} = 0$, and the stabilizing control is given to guarantee the convergence condition [6-9]. Finally, the control law is given by:

$$p_{dc} = \frac{1}{L_{g}h_{l}} \left(\left(-L_{f}h_{l} + \frac{dv_{dc}^{*}}{dt} \right) - \frac{k_{i1}}{k_{1}} \left(v_{dc} - v_{dc}^{*} \right) \right) - k_{dc}sign(S_{1})$$

$$v_{F\alpha}^{*} = \frac{1}{L_{g}h_{2}} \left(\left(-L_{f}h_{2} + \frac{di_{F\alpha}^{*}}{dt} \right) - \frac{k_{i2}}{k_{2}} \left(i_{F\alpha} - i_{F\alpha}^{*} \right) \right) - k_{\alpha}sign(S_{2})$$

$$v_{F\beta}^{*} = \frac{1}{L_{g}h_{3}} \left(\left(-L_{f}h_{3} + \frac{di_{F\beta}^{*}}{dt} \right) - \frac{k_{i3}}{k_{3}} \left(i_{F\beta} - i_{F\beta}^{*} \right) \right) - k_{\beta}sign(S_{3})$$

$$v_{Fo}^{*} = \frac{1}{L_{g}h_{4}} \left(\left(-L_{f}h_{4} + \frac{di_{Fo}^{*}}{dt} \right) - \frac{k_{i4}}{k_{4}} \left(i_{Fo} - i_{Fo}^{*} \right) \right) - k_{o}sign(S_{4})$$
(16)

Where: k_{dc} , k_{α} , k_{β} , k_{o} , k_{i1} , k_{i2} , k_{i3} , k_{i4} , k_1 , k_2 , k_3 and k_4 are positive constants.

2.4. 3D-SVM for the four-leg inverter

In 3D-SVM, there are 16 possible switching vectors: fourteen active nonzero vectors and two null vectors. These are shown in Fig. 2. The entire 3-D Space is divided in 6 prisms and 24 tetrahedrons. Each prism consists of four tetrahedrons. An instantaneous reference input vector may lie in any of these tetrahedrons at any point of time [10].

The 3D-SVM algorithm is composed by four essential steps as follows: Identification of prism;

- Identification of tetrahedrons;
- Duty cycle calculation;
- Generation of PWM sequence for the switches.



Figure 2. Switching vectors of a three-phase four legged inverter

3. SIMULATION RESULTS

Harmonic current filtering, reactive power compensation, load current balancing and neutral current elimination performance of the four leg-SAPF with the proposed control have been examined under balanced and unbalanced nonlinear load. The parameters used are shown in Table I.

RMS value of phase voltage	220 V
DC-link capacitor C	5 mF
Source impedance R_s , L_s	1 mΩ, 1 mH
Filter impedance R_F , L_F	0.1 mΩ, 0.1 mH
Line impedance R_l , L_l	1 mΩ, 1 mH
DC-link voltage reference v _{dc ref}	800 V
Diode rectifier load R_l , L_l	5 Ω, 10 mH
Switching frequency f_s	10kHz
Sampling frequency	1 MHz
$k_{dc}, k_{I} = k_{iI}$ constants	120, 10^4
$k_2 = k_3 = k_4, k_{i2} = k_{i3} = k_{i4}, k_a = k_{\beta} = k_o$ constants	4.10 ⁻⁸ , 5.10 ⁵ , 10 ⁴



Figure 3. Simulation results of the proposed sliding mode controller. (a) Source current, (b) Load current, (c) Source voltage and source current of a-phase, (d) Neutral current. (e) DC-link voltage v_{dc} .



Figure 4. Simulation results with PI controller. (a) Source current, (b) Load current, (c) Source voltage and source current of a-phase, (d) Neutral current, (e) DC-link voltage v_{dc} .

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Figure 5. Harmonic spectrum. (a) With sliding mode controller. (b) With PI controller, before compensation

The harmonic spectrums of AC supply current before and after compensation are illustrated in Fig. 5. It results that the active filter decreases the total harmonic distortion (THD) in the supply currents from 16.94% to 2.18% with PI controller. However, with sliding mode controller, the THD is increased to 0.09% which proves the effectiveness of the proposed nonlinear controller.

The dynamic behavior under a step change of the load in phase b at t = 0.3s (to make unbalanced of the nonlinear load) is presented in Figs. 3 and 4. It can be observed that the network current become perfectly sinusoidal after the control application, and unity power factor operation is successfully achieved, even in this transient state.

We also observe that the neutral current is almost canceled with a low ripple in case where the sliding mode control is applied (2.86% for the sliding mode controller and 18.57% for the PI controller).

The absence of an overshoot in dc voltage response during load change, low rise time and low source current THD, demonstrates the superiority of the sliding mode controller compared to its counterpart traditional PI controller.

4. CONCLUSION

This work presented a sliding mode control technique applied to a three-phase four-leg SAPF. The developed controller was aimed to compensate for harmonics and unbalance in the case of distorted nonlinear load currents, by making the source currents in phase with their corresponding phase voltages. Also, the controller was capable of eliminating the current flowing in the neutral line. Finally, the simulation results validated both the steady state and dynamic behavior of the proposed controller.

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